

# CDCLVC1112 Evaluation Module

(ACTIVE) CDCLVC1112EVM

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## Description

The CDCLVC1112 is a high-performance, low additive phase noise LVCMOS clock buffer. It has one LVCMOS input and twelve LVCMOS outputs. It has also an enable pin. This evaluation module (EVM) is designed to demonstrate the electrical performance of the CDCLVC1112. However, this EVM can also be used for customers interested in evaluating the performance of the CDCLVC1106, CDCLVC1108 or CDCLVC1110 as well. For optimum performance, the board is equipped with 50-ohm SMA connectors and well controlled 50-ohm impedance microstrip transmission lines.

## Features

- Easy-to-use evaluation board to fan out low phase noise LVCMOS clock signals
- Easy device setup
- Enable pin configurable through jumper and SMA
- Board powered at 3.3V

## What's Included

- [SCAU042, Low Additive Phase Noise LVCMOS Clock Buffer Eval Board](#)
- [SCAS895, CDCLVC11xx 3.3-V and 2.5-V LVCMOS High-Performance Clock Buffer Family \(Rev. A\)](#)

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CDCLVC1112EVM: CDCLVC1112 Evaluation Module	\$149.00(USD)  In Stock <b>Typically Ships in 1 to 3 Business Days</b>  <b>Buy from TI</b>	Pricing may vary.  <b>Buy from distributor</b>	ACTIVE	Yes	Yes	Yes	NA	NA	NA	NA	NA	NA	No	No	No

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## Technical Documents

### User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
<a href="#">Low Additive Phase Noise LVCMOS Clock Buffer Eval Board</a>		PDF	83	07 Jul 2010	86

## Related Products

### TI Devices (4)

Part Number	Name	Product Family
<a href="#">CDCLVC1106</a>	Low Jtter, 1:6 LVCMOS Fan-out Clock Buffer	Fanout Buffers
<a href="#">CDCLVC1108</a>	Low Jtter, 1:8 LVCMOS Fan-out Clock Buffer	Fanout Buffers
<a href="#">CDCLVC1110</a>	Low Jtter, 1:10 LVCMOS Fan-out Clock Buffer	Fanout Buffers
<a href="#">CDCLVC1112</a>	Low Jtter, 1:12 LVCMOS Fan-out Clock Buffer	Fanout Buffers

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