TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

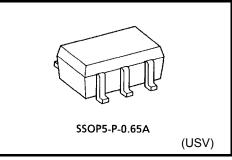
TC7SG00FU

2 Input NAND Gate

Features

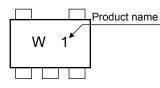
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- High output current: $\pm 8 \text{ mA} \text{ (min)}$ at V_{CC} = 3 V
- High-speed operation: $t_{pd} = 2.5 \text{ ns (typ.)}$
 - at V_{CC} = 3.3 V,15pF
- Operating voltage range: V_{CC} = 0.9 to 3.6 V
- 5.5-V tolerant inputs.
- 3.6-V power down protection output.

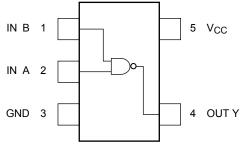


Weight: 0.006 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	-0.5 to 4.6	V	
DC input voltage	VIN	-0.5 to 7.0	V	
	Vout	-0.5 to 4.6 (Note 1)	V	
DC output voltage		-0.5 to V _{CC} + 0.5 (Note 2)	v	
Input diode current	IIK	-20	mA	
Output diode current	I _{OK}	-20 (Note 3)	mA	
DC output current	IOUT	±25	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	−65 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0V$$

Note 2: High or Low State. Do not exceed I_{OUT} of absolute maximum ratings

Note 3: V_{OUT} < GND

Start of commercial production 2005-02

<u>TOSHIBA</u>

IEC Logic Symbol



A B Y L L H				
	А	В	Y	
	L	L	Н	
	L	Н	Н	

L

н

Н

L

Н

Н

Operating Ranges

Characteristics	Symbol	Rating	Unit		
Supply voltage	V _{CC}	0.9 to 3.6	V		
Input voltage	V _{IN}	0 to 5.5	V		
	Vout	0 to 3.6 (Note 4)	V		
Output voltage	VOUT	0 to V _{CC} (Note 5)	v		
Output Current		± 8.0 (Note 6)			
		± 4.0 (Note 7)			
		± 3.0 (Note 8)	mA		
	IOH/IOL	± 1.7 (Note 9)	ША		
		± 0.3 (Note 10)			
		± 0.02 (Note 11)			
Operating temperature	T _{opr}	– 40 to 85	°C		
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V		

Note 4: $V_{CC} = 0V$ Note 5: High or Low state.

Note 6: $V_{CC} = 3.0$ to 3.6 V Note 7: $V_{CC} = 2.3$ to 2.7 V Note 8: $V_{CC} = 1.65$ to 1.95 V Note 9: $V_{CC} = 1.4$ to 1.6 V Note 10: $V_{CC} = 1.1$ to 1.3 V Note 11: $V_{CC} = 0.9$ V Note 12: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics

Characteristics	acteristics Symbol Test Condition		Ta = 25°C			$Ta = -40 \text{ to } 85^{\circ}C$		Unit		
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit		
				0.9	V _{CC}		_	V _{CC}		V
High-level input		_		1.1 to 1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.7 \end{array}$			V _{CC} × 0.7		
	VIH			1.4 to 1.6	$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$			V _{CC} × 0.65		
voltage					$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$			V _{CC} × 0.65		
				2.3 to 2.7	1.7		—	1.7		
				3.0 to 3.6	2.0	_	_	2.0	_	
				0.9		_	GND	_	GND	
				1.1 to 1.3	_		$V_{CC} \times 0.3$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	v
Low-level input	V _{IL}			1.4 to 1.6			$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$		$V_{CC} \times 0.35$	
voltage				1.65 to 1.95			$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$		$V_{CC} \times 0.35$	
				2.3 to 2.7	_	_	.0.7		0.7	
				3.0 to 3.6	_	_	0.8		0.8	
	Vон	VIN = VIH or VIL	I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	V
			I _{OH} = -0.3 mA	1.1 to 1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		_	V _{CC} × 0.75	_	
High-level output voltage			I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
ouput voltage			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45		_	V _{CC} -0.45	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0		_	2.0		
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48		—	2.48	_	
	V _{OL}	VIN = VIH	$I_{OL} = 0.02 \text{ mA}$	0.9			0.1	_	0.1	
Low-level output voltage			I _{OL} = 0.3 mA	1.1 to 1.3			$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	—	V _{CC} × 0.25	
			I _{OL} = 1.7 mA	1.4 to 1.6	_		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	—	V _{CC} × 0.25	v
			I _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	-
			I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	—	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5V		0 to 3.6		_	±0.1	—	±1.0	μA
Power off leakage current	I _{OFF}	V _{IN} = 0 to 5.5V V _{OUT} = 0 to 3.6V		0	_		1.0	_	10.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND				1.0	_	10.0	μΑ

AC Electrical Characteristics (unless otherwise specified, Input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
			V _{CC} (V)	Min	Тур.	Max	Min	Max	
		C _L = 10 pF,	0.9	_	26.9	_	_	_	
			1.1 to 1.3	_	10.9	20.7	1.0	38.6	
			1.4 to 1.6	_	5.9	9.6	1.0	11.3	ns
		$R_L = 1 M\Omega$	1.65 to 1.95	_	4.5	7.0	1.0	7.5	
			2.3 to 2.7	_	2.9	4.4	1.0	4.9	
			3.0 to 3.6		2.2	3.5	1.0	4.1	
	tрLH tpHL	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		30.0				
			1.1 to 1.3		12.0	24.2	1.0	42.0	
Propagation delay time			1.4 to 1.6	_	6.5	10.5	1.0	12.6	
Fropagation delay time			1.65 to 1.95	_	5.0	7.7	1.0	8.0	
			2.3 to 2.7		3.2	4.9	1.0	5.6	
			3.0 to 3.6		2.5	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		45.0				
			1.1 to 1.3		18.0	33.4	1.0	63.2	
			1.4 to 1.6		8.9	14.8	1.0	17.9	
			1.65 to 1.95		6.9	10.3	1.0	10.8	
			2.3 to 2.7		4.4	6.4	1.0	6.8	
			3.0 to 3.6		3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}	—	3.6		3				pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6		6				pF

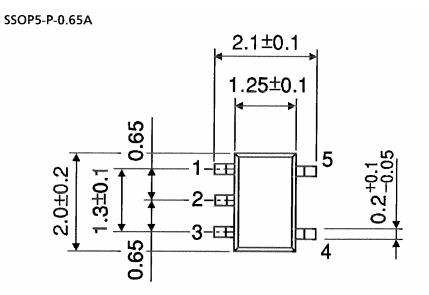
Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

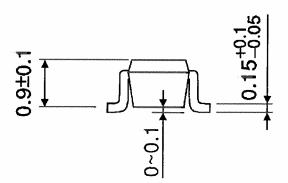
Average operating current can be obtained by the equation: $\log (u_{eq}) = C P p V \cos(h u_{eq})$

 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions





Weight: 0.006 g (typ.)

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