VSMY4850X01



Vishay Semiconductors

High Speed Infrared Emitting Diodes, 850 nm, Surface Emitter Technology



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

As part of the <u>SurfLight</u>[™] portfolio, the VSMY4850X01 is an infrared, 850 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, in a low profile 0805 surface-mount (SMD) package.

FEATURES

- Package type: surface-mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.8
- Peak wavelength: $\lambda_p = 850 \text{ nm}$
- AEC-Q101 qualified
- High speed
- Angle of half intensity: $\varphi = \pm 60^{\circ}$
- 0805 standard surface-mountable package
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Miniature light barrier
- Automotive sensors
- · Optical switch
- IR point source

PRODUCT SUMMARY					
$\label{eq:component} \begin{array}{c} \text{COMPONENT} & I_{e} \mbox{ (mW/sr) at } I_{F} = 100 \mbox{ mA} \end{array}$		φ (°)	λ _p (nm)	t _r (ns)	
VSMY4850X01	8	± 60	850	7	

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY4850X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805	

Note

· MOQ: minimum order quantity

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COMPLIANT

- HALOGEN FREE
- **GREEN** (5-2008)

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ISHAY

VSMY4850X01

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ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	100	mA	
Peak forward current	$t_p/T = 0.1, t_p = 100 \ \mu s$	I _{FM}	200	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	500	mA	
Power dissipation		Pv	210	mW	
Junction temperature		Тj	125	°C	
Operating temperature range		T _{amb}	-40 to +110	°C	
Storage temperature range		T _{stg}	-40 to +110	°C	
Soldering temperature	According to Fig. 7, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction-to-ambient	EIA / JESD51	R _{thJA}	280	K/W	

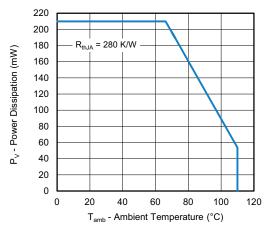


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

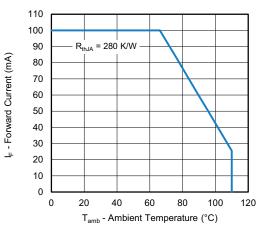


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	l _F = 100 mA, t _p = 20 ms	V _F	-	1.8	2.1	V
Temperature coefficient of V _F	I _F = 100 mA, t _p = 20 ms	TK _{VF}	-	-1.8	-	mV/K
Reverse current		I _R	Not designed for reverse operation μA			μA
Junction capacitance	$V_R = 0 V$, f = 1 MHz, E = 0 mW/cm ²	CJ	-	30	-	pF
Radiant intensity	I _F = 100 mA, t _p = 20 ms	le	5	8	11	mW/sr
Temperature coefficient of radiant power	I _F = 100 mA, t _p = 20 ms	ΤKφ _e	-	-0.16	-	%/K
Angle of half intensity		φ	-	± 60	-	0
Peak wavelength	I _F = 100 mA, t _p = 20 ms	λρ	-	850	-	nm
Spectral bandwidth	l _F = 100 mA, t _p = 20 ms	Δλ	-	35	-	nm
Temperature coefficient of λ_p	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	TK _{λp}	-	0.25	-	nm/K
Rise time	I _F = 100 mA, 10 % to 90 %	t _r	-	7	-	ns
Fall time	I _F = 100 mA, 10 % to 90 %	t _f	-	7	-	ns

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

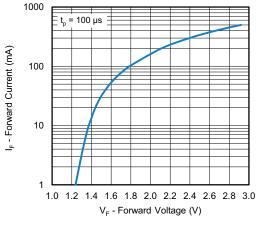


Fig. 3 - Forward Current vs. Forward Voltage

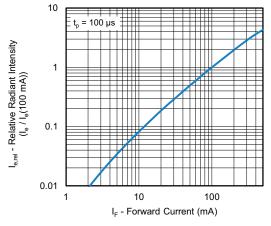


Fig. 4 - Relative Radiant Intensity vs. Forward Current

REFLOW SOLDER PROFILE

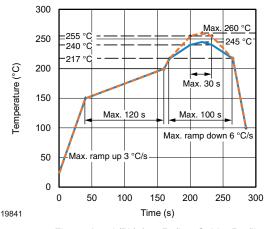


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

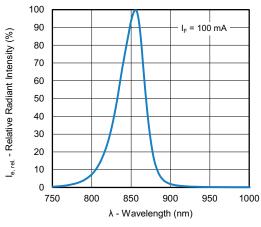


Fig. 5 - Relative Radiant Power vs. Wavelength

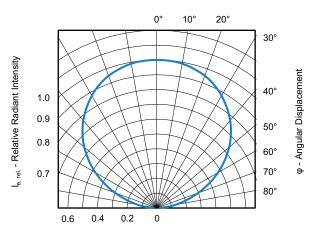


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020: Moisture sensitivity: level 3 Floor life: 168 h Conditions: $T_{amb} < 30$ °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

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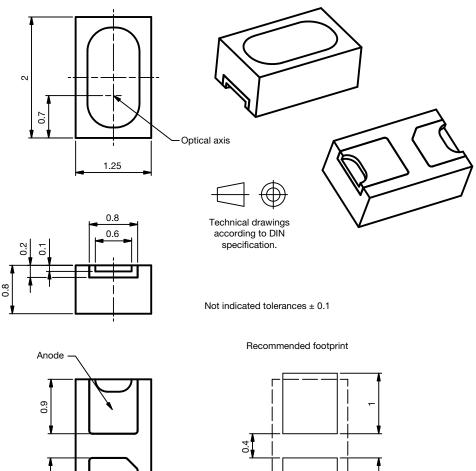
3 questions, contact: emittertechsupport@





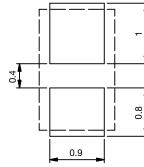
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PACKAGE DIMENSIONS in millimeters



Cathode

0.7



Drawing- No.: 6.550-5352.01-4 Issue: 1; 20.12.2016

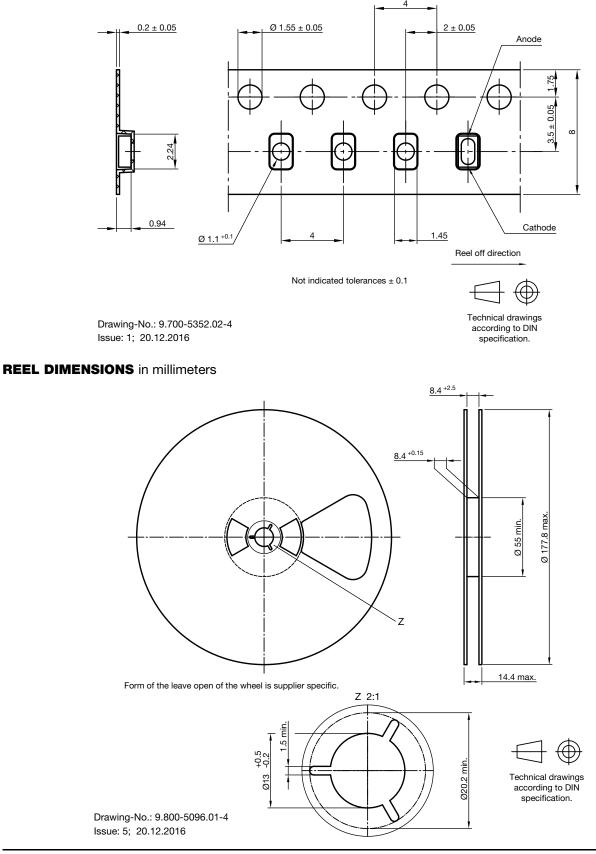
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BLISTER TAPE DIMENSIONS in millimeters



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