MMBD7000L, SMMBD7000L

Dual Switching Diode

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	100	V
Forward Current	IF	200	mA
Forward Surge Current (60 Hz @ 1 cycle)	I _{FSM}	1.6	А
Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%)	I _{FRM}	0.5	А

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1)T _A = 25°C	P _D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C	P _D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance,	$R_{\theta JA}$	447	°C/W
Junction-to-Ambient		417	
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

1

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

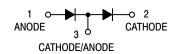


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SOT-23 (TO-236) CASE 318 STYLE 11



MARKING DIAGRAM



M5C = Specific Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)
*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBD7000LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SMMBD7000LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MMBD7000LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
SMMBD7000LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

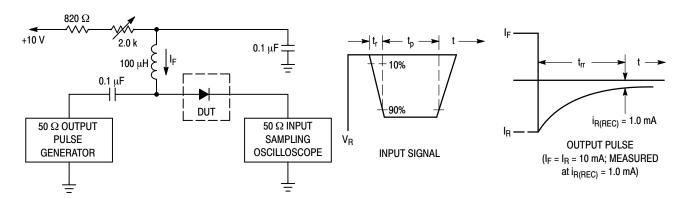
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBD7000L, SMMBD7000L

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	•			
Reverse Breakdown Voltage $(I_{(BR)} = 100 \mu Adc)$	V _(BR)	100	_	Vdc
Reverse Voltage Leakage Current $(V_R = 50 \text{ Vdc})$ $(V_R = 100 \text{ Vdc})$ $(V_R = 50 \text{ Vdc}, 125^{\circ}\text{C})$	I _R I _{R2} I _{R3}	- - -	1.0 3.0 100	μAdc
Forward Voltage (I _F = 1.0 mAdc) (I _F = 10 mAdc) (I _F = 100 mAdc)	V _F	0.55 0.67 0.75	0.7 0.82 1.1	Vdc
Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc})$ (Figure 1)	t _{rr}	_	4.0	ns
Capacitance (V _R = 0 V)	С	-	1.5	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



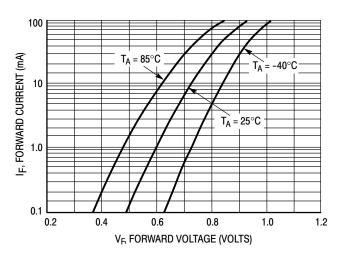
Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

MMBD7000L, SMMBD7000L

CURVES APPLICABLE TO EACH DIODE



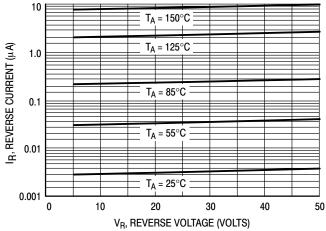
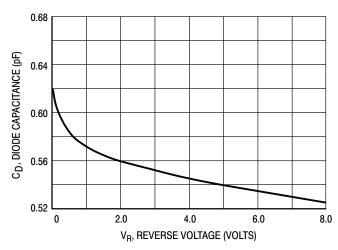


Figure 2. Forward Voltage

Figure 3. Leakage Current



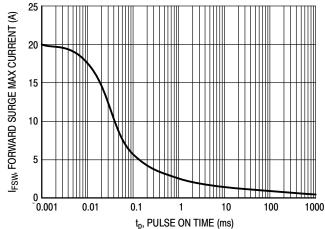


Figure 4. Capacitance

Figure 5. Forward Surge Current

MILLIMETERS

MIN

0.89

0.01

0.37

0.08

2.80

1.20

1.78

0.30

0.35

2.10

O°

NOM

1.00

0.06

0.44

0.14

2.90

1.30

1.90

0.43

0.54

2.40





SOT-23 (TO-236) 2.90x1.30x1.00 1.90P **CASE 318 ISSUE AU**

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MAX

1.11

0.10

0.50

0.20

3.04

1.40

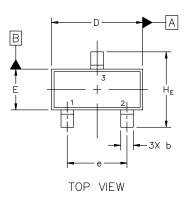
2.04

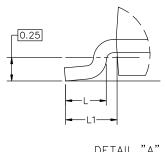
0.55

0.69

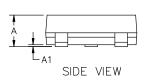
2.64

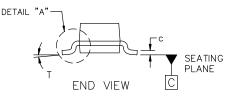
10°

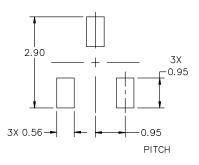




DETAIL "A" Scale 3:1







NOTES:

DIM

Α

Α1

b

С

D

Ε

е L

L1

HE

Τ

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P CASE 318 ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR		NODE D CONNECTION ATHODE	
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: STYLE 12: PIN 1. ANODE PIN 1. CA 2. CATHODE 2. CA 3. CATHODE-ANODE 3. AN	ATHODE PIN 1. SOURCE ATHODE 2. DRAIN	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE			STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: STYLE 24: PIN 1. ANODE PIN 1. GAT 2. ANODE 2. DR/ 3. CATHODE 3. SOU	TE PIN 1. ANODE AIN 2. CATHODE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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