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July. 2014

FPF1C2P5MF07AM F1 Module solution for PV-Application

General Description

Fairchild's brand-new DC-AC module is designed for a power stage that needs more compact design. And the Press-fit technology provides simple and reliable mounting. This module is optimized for the application such as solar inverter where a high efficiency and robust design are needed.

Electrical Features

- · High Efficiency
- · Low Conduction and Switching losses
- Low V_{CE(sat)}: 1.1 V typ. @ Ic = 30 A
- Low $R_{DS(ON)}$: 90 m Ω max.
- · Fast Recovery Body Diode

Mechanical Features

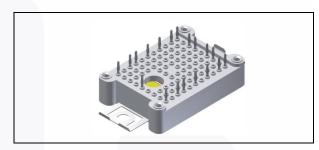
- · Compact size : F1 Package
- · Press-fit contact technology

Applications

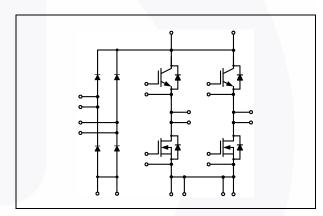
Solar Inverter

Certification

UL approved (E209204)



Package Code: F1



Internal Circuit Diagram

Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Description	Rating	Units	
Rectifier D	iode			
V _{RRM}	Peak Repetitive Reverse Voltage		620	V
I _{Fav}	Diode Continuous Forward Current	@ T _C = 80°C	27	Α
I _{FSM}	Diode Maximum Forward Surge Current	245	Α	
I ² t	I ² t value		300	A ² s
P _D	Maximum Power Dissipation @ T _C = 25°C		77	W
T _J	Operating Junction Temperature		-40 to +150	°C

Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted. (Continued)

Symbol	Description		Rating	Units	
High-side I	ЭВТ				
V _{CES}	Collector-Emitter Voltage		620	V	
V _{GES}	Gate-Emitter Voltage		± 20	V	
I _C	Collector Current	@ T _C = 80°C	39	Α	
I _{CM}	Pulsed Collector Current		90	Α	
I _F	Diode Continuous Forward Current	@ T _C = 80°C	22	Α	
I _{FM}	Diode Maximum Forward Current		90	А	
P_{D}	Maximum Power Dissipation	@ T _C = 25°C	231	W	
T _J	Operating Junction Temperature		-40 to +150	°C	
Low-side M	OSFET				
V _{DSS}	Drain-Source Voltage		620	V	
V _{GSS}	Gate-Source Voltage		± 20	V	
I _D	Continuous Drain Current	@ T _C = 25°C	36	Α	
		@ T _C = 80°C	27	Α	
I _{DM}	Pulsed Drain Current	Limited by T _J max.	156	А	
I _S	Continuous Source-Drain Forward Curren	ource-Drain Forward Current		А	
I _{SM}	Maximum Pulsed Source-Drain Forward Current		156	А	
P_D	Maximum Power Dissipation	@ T _C = 25°C	250	W	
T _J	Operating Junction Temperature		-40 to +150	°C	
Module				·	
T _{STG}	Storage Temperature		-40 to +125	°C	
V _{ISO}	Isolation Voltage	@ AC 1 _{MIN}	2500	V	
IsoMaterial			Al ₂ O ₃		
F _{MOUNT}	Mounting Force per Clamp		20 to 50	N	
Weight		Тур.	22	g	
Creepage	Terminal to Heatsink		11.5	mm	
	Terminal to Terminal		6.3	mm	
Clearance	Terminal to Heatsink		10.0	mm	
	Terminal to Terminal		5.0	mm	

Package Marking and Ordering Information

Device	Device Device Marking		Packing Type	Quantity / Tray	
FPF1C2P5MF07AM	FPF1C2P5MF07AM	F1	Tray	22	

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Rectifier	Diode					
V _F	Diode Forward Voltage	I _F = 30 A	-	-	1.9	V
		I _F = 30 A @T _C = 125°C	-	1.45	-	V
I _R	Reverse Leakage Current	V _R = 620 V	-	-	25	μΑ
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per Diode	-	-	1.62	°C/W
High-side	IGBT					
Off Charac	teristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0 V, I _C = 1 mA	620	-	-	V
I _{CES}	Collector Cut-off Current	$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	25	μΑ
I _{GES}	Gate-Emitter Leakage Current	$V_{GE} = V_{GES}, V_{CS} = 0 V$	-	-	2.5	μΑ
On Charac	teristics					
V _{GE(th)}	Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 30 \text{ mA}$	4	5.7	7	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 30 A, V _{GE} = 15 V	-	1.1	1.6	V
` '		I _C = 30 A, V _{GE} = 15 V @T _C = 125°C	-	1.0	-	V
		I _C = 60 A, V _{GE} = 15 V	-	1.4	-	V
Switching	Characteristics					
Q _g	Total Gate Charge	V _{DS} = 380 V, V _{GS} = 0V+15 V, I _D = 30 A	-	214	-	nC
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per IGBT	-	-	0.54	°C/W

^{*} Note : High-side IGBT is optimized for line frequency switching such as 50/60 Hz.

High-Sid	le FWD					
V_{FM}	Diode Forward Voltage	I _F = 15 A, V _{GS} = 0 V	-	1.75	2.25	V
t _{rr}	Reverse Recovery Time	I _F = 15 A	-	30	-	ns
Irr	Reverse Recovery Current	dI _F /dt = 1650 A/μs	-	27	-	Α
Q _{rr}	Reverse Recovery Charge		- /	405	-	nC
t _{rr}	Reverse Recovery Time	I _F = 15 A	-	43		ns
Irr	Reverse Recovery Current	dl _F /dt = 1500 A/μs @T _C = 125°C		38	-	Α
Q _{rr}	Reverse Recovery Charge		-	814	-	nC
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per Diode	-	-	1.61	°C/W

$\textbf{Electrical Characteristics} \ \, \textbf{T}_{\text{C}} = 25^{\circ} \text{C unless otherwise noted. (Continued)}$

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Low-Side	MOSFET					
Off Charac						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA	620	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 620 V, V _{GS} = 0 V	-	-	25	μΑ
I _{GSS}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			2.5	μА
On Charac	cteristics					
V _{GS(th)}	Gate-Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \text{ mA}$	2.7	3.8	5.3	V
R _{DS(ON)}	Static Drain-Source On-Resistance	I _D = 27 A, V _{GS} = 10 V	-	-	90	mΩ
D3(ON)		I _D = 27 A, V _{GS} = 10 V @T _C = 125°C	-	135	-	mΩ
		I _D = 47 A, V _{GS} = 10 V	_	76	-	mΩ
V _{SD}	Source-Drain Diode Forward Voltage	I _{SD} = 27 A, V _{GS} = 0 V		-	1.5	V
OD	3	I _{SD} = 47 A, V _{GS} = 0 V	-	1.3	-	V
				ı.		I
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{CC} = 380 V	-	57	-	ns
t _r	Rise Time	I _D = 27 A	-	14	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}$ $R_G = 10 \Omega$	-	240	-	ns
t _f	Fall Time	Inductive Load	-	20	-	ns
E _{ON}	Turn-On Switching Loss per Pulse	T _C = 25°C	-\	440	-	μJ
E _{OFF}	Turn-Off Switching Loss per Pulse		-	113	-	μJ
t _{d(on)}	Turn-On Delay Time	V _{CC} = 380 V	-	53	-	ns
t _r	Rise Time	I _D = 27 A	-	16	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10 V		257	-	ns
t _f	Fall Time	$R_G = 10 Ω$ Inductive Load	-	20	-	ns
E _{ON}	Turn-On Switching Loss per Pulse	T _C = 125°C		719	-	μJ
E _{OFF}	Turn-Off Switching Loss per Pulse			124	-	μJ
Qg	Total Gate Charge	V _{DS} = 380 V, V _{GS} = 0V+10 V, I _D = 27 A	- /	155	-	nC
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per Chip	-	-	0.5	°C/W

Typical Performance Characteristic

Fig 1. Typical Output Characteristics - IGBT

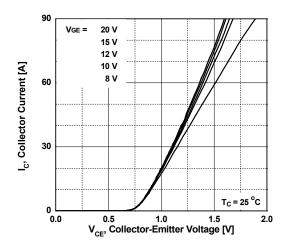


Fig 3. Typical Saturation Voltage Characteristics - IGBT

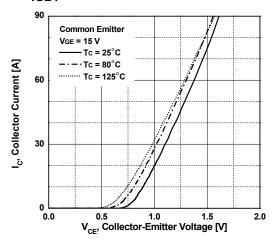


Fig 5. Typical Forward Voltage Drop vs. Forward Current - High-Side FWD

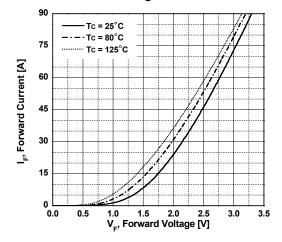


Fig 2. Typical Output Characteristics - IGBT

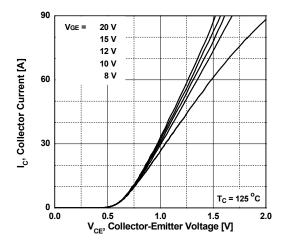


Fig 4. Transient Thermal Response Curve - IGBT

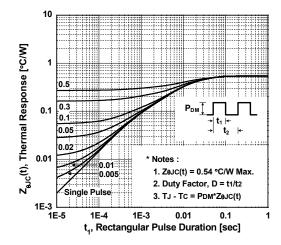
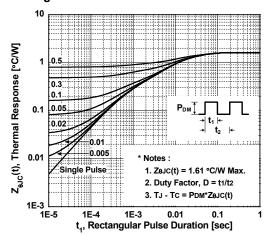


Fig 6. Transient Thermal Response Curve
- High-Side FWD



Typical Performance Characteristic (Continued)

Fig 7. On-Region Characteristics - MOSFET

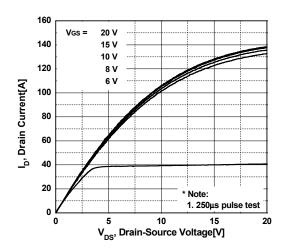


Fig 9. On-Resistance Variation vs. Temperature - MOSFET

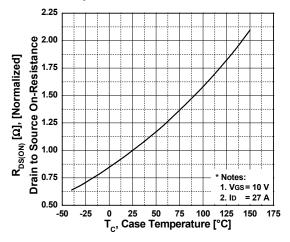


Fig 11. Turn-Off Loss vs. Gate Resistor Values
- MOSFET

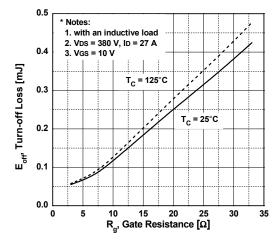


Fig 8. On-Resistance Variation



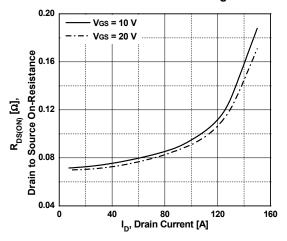


Fig 10. Body Diode Forward Voltage Variation vs. Source Current and Temperature - MOSFET

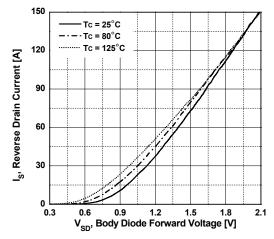
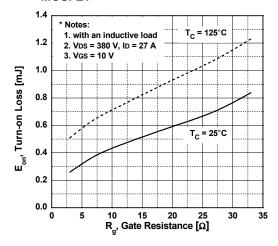


Fig 12. Turn-On Loss vs. Gate Resistor Values
- MOSFET



Typical Performance Characteristic (Continued)

Fig 13. Turn-Off Loss vs. Drain Current - MOSFET

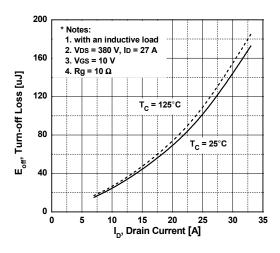


Fig 15. Transient Thermal Response Curve - MOSFET

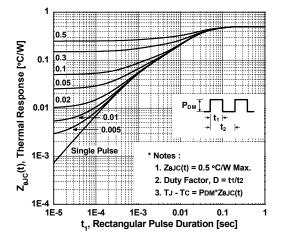


Fig 17. Transient Thermal Response Curve
- Rectifier Diode

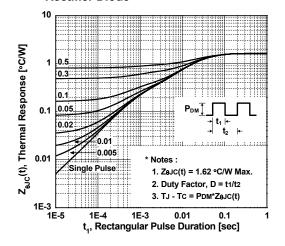


Fig 14. Turn-On Loss vs. Drain Current - MOSFET

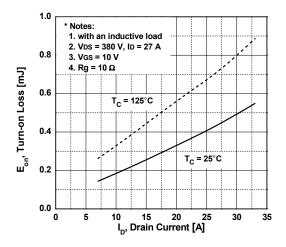
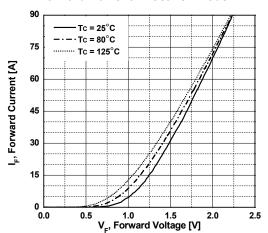
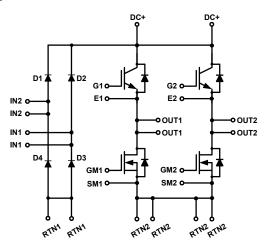


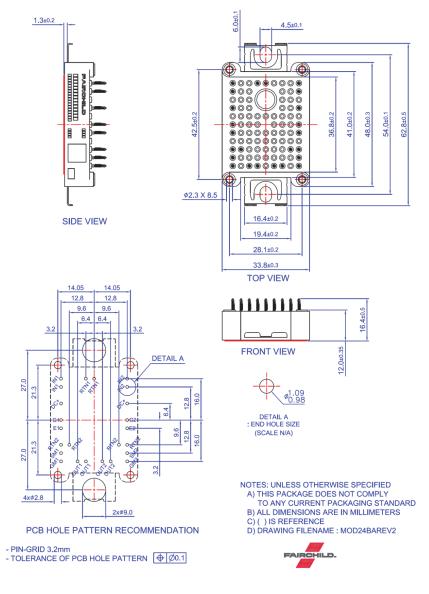
Fig 16. Typical Forward Voltage Drop vs.
Forward Current - Rectifier Diode



Internal Circuit Diagram



Package Outlines [mm]







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