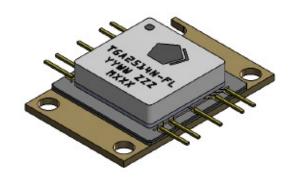
### **Applications**

- Ku Band VSAT Transmitter
- · Point to Point Radio



#### **Product Features**

Frequency Range: 13 – 16 GHzSaturated Output Power: 38 dBm

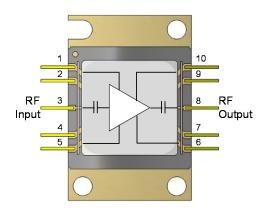
Small Signal Gain: 24 dB

Input and Output Return Loss: 14 dB
 Bias Condition (V<sub>D</sub>/I<sub>DQ</sub>): 8.0V/2.6 A

• Package Size: 0.448 x 0.682 x 0.120 inches

(11.379 x 17.323 x 3.048 mm)

### **Functional Block Diagram**



### **General Description**

Qorvo's TGA2514N-FL is a packaged Ku-band power amplifier operating from 13–16 GHz. Fabricated on Qorvo's production 0.25um GaAs pHEMT process (QPHT25), the TGA2514N-FL delivers 6.5 W of saturated output power with 24 dB of small signal gain. Performance is ideal for VSAT transmitters, data links and point to point radios.

To support easy system integration, the TGA2514N–FL is offered in a 10-pin flange–mounted package, offering robust handling and good thermal management. In addition, both RF ports have integrated DC blocking capacitors and are fully matched to 50 ohms.

Lead free and RoHS compliant.

Evaluation boards are available upon request.

### **Pad Configuration**

Pad Number	Symbol	
1, 5	V <sub>G</sub>	
2, 4, 7, 9	GND	
3	RF Input	
6, 10	V <sub>D</sub>	
8	RF Output	

### **Ordering Information**

Part	ECCN	Description
TGA2514N-FL	3A001.b.2.b	13-16 GHz Power Amplifier
TGA2514N-FL_EVB	EAR99	13-16 GHz Power Amplifier Evaluation Board

# TGA2514N-FL

13-16 GHz Power Amplifier

### **Absolute Maximum Ratings**

Parameter	Value
Drain Voltage (V <sub>D</sub> )	9.0 V
Drain Current (I <sub>D</sub> )	3.8 A
Gate Voltage Range (V <sub>G</sub> )	-5 to 0 V
Gate Current (I <sub>G</sub> )	-18 to +18 mA
Input Power, CW (P <sub>IN</sub> )	21 dBm
Power Dissipation (PDISS)	33.3 W
Operating Channel Temperature	200 ℃

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

### **Recommended Operating Conditions**

Parameter	Value
Drain Voltage (V <sub>G</sub> )	8.0 V
Gate Voltage (Vs) -0.65 V	
Drain Current (IDQ)	2.6 A

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

### **Electrical Specifications**

Test conditions, unless otherwise noted: Temp = 25  $^{\circ}$ C,  $V_D$  = 8.0 V,  $I_{DQ}$  = 2.6 A

Parameter	Min	Typical	Max	Units
Frequency Range	13		16	GHz
Small Signal Gain		24		dB
Saturated Output Power		38		dBm
Input Return Loss		14		dB
Output Return Loss		14		dB

# Specifications

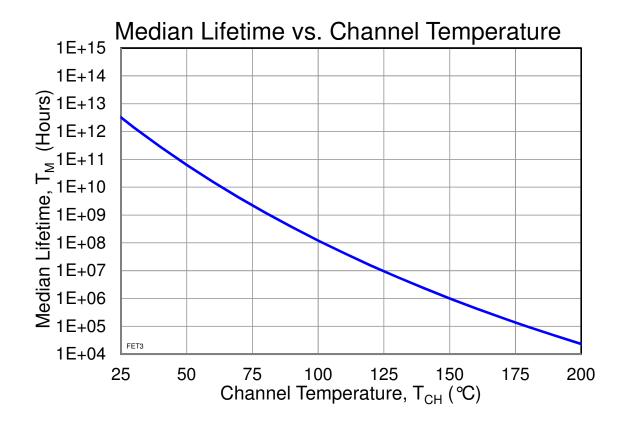
### **Thermal and Reliability Information**

Parameter	Conditions	Value	Units
Thermal Resistance (θ <sub>JC</sub> ) (1)	T 70.00 V 0.VI 0.0 A D	3.9	ºC/W
Channel Temperature (T <sub>CH</sub> ) <sup>(1)</sup>	$T_{BASE} = 70 \text{ °C}, V_{D} = 8 \text{ V } I_{DQ} = 2.6 \text{ A}, P_{DISS} = 20.8 \text{ W}$	151	∞
Median Lifetime (T <sub>M</sub> )	20.0 **	9.3E5	Hrs
Thermal Resistance (θ <sub>JC</sub> ) <sup>(1)</sup>	T 70.00 W 0.WL 0.0 A D	3.9	ºC/W
Channel Temperature (T <sub>CH</sub> ) (1)	$T_{BASE} = 70$ °C, $V_D = 8$ V $I_{DQ} = 2.6$ A, $P_{DISS} = 22.5$ W (Under RF Drive)	158	∞
Median Lifetime (T <sub>M</sub> )	22.5 W (Glidel III Blive)	5.2E5	Hrs

Note:

### **Median Lifetime**

Failure Criterion = 10% reduction in ID MAX

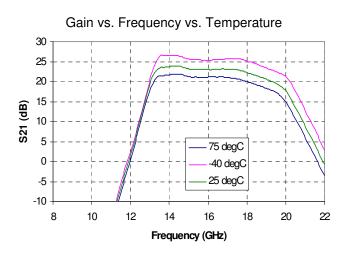


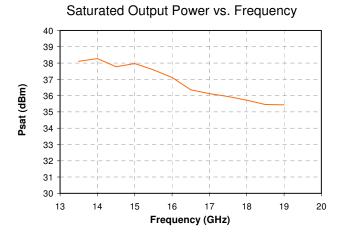
<sup>1.</sup> Package backside temperature fixed at 70 ℃.

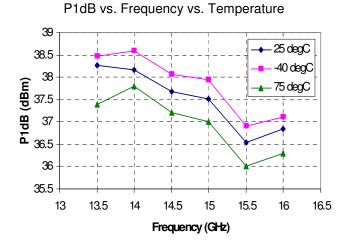
### Typical Performance – Small Signal and Power

Test conditions, unless otherwise noted: Temp = 25 °C, V<sub>D</sub> = 8.0 V, I<sub>DQ</sub> = 2.6 A

#### Gain and Return Loss vs. Frequency 28 24 20 16 Return Loss (dB) 12 Gain (dB) Output -20 -24 -28 9 11 13 15 17 19 21 23 Frequency (GHz)



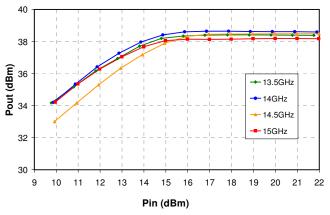




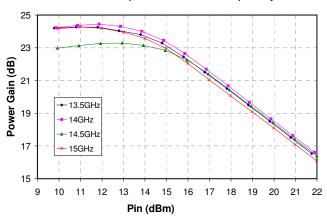
### Typical Performance – Power

Test conditions, unless otherwise noted: Temp = 25  $^{\circ}$ C,  $V_D$  = 8.0 V,  $I_{DQ}$  = 2.6 A

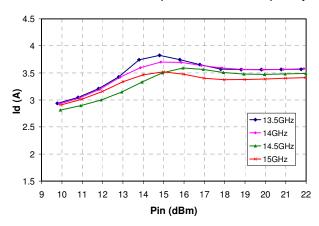
### Output Power vs. Input Power vs. Frequency



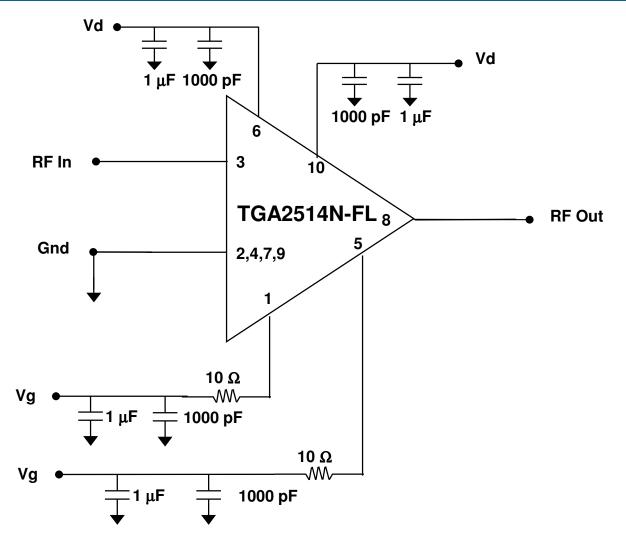
#### Gain vs. Input Power vs. Frequency



#### Drain Current vs. Input Power vs. Frequency



### **Application Circuit**



Note: Vg can be biased from either Pin 1 or Pin 5

#### **Bias-up Procedure**

- 1. Set I<sub>D</sub> limit to 3300 mA, I<sub>G</sub> limit to 18 mA
- 2. Set V<sub>G</sub> to -1.5 V
- 3. Set V<sub>D</sub> +8 V
- 4. Adjust  $V_G$  more positive until  $I_{DQ}$  = 2600 mA ( $V_G \sim -0.65$  V Typical)
- 5. Apply RF signal

#### **Bias-down Procedure**

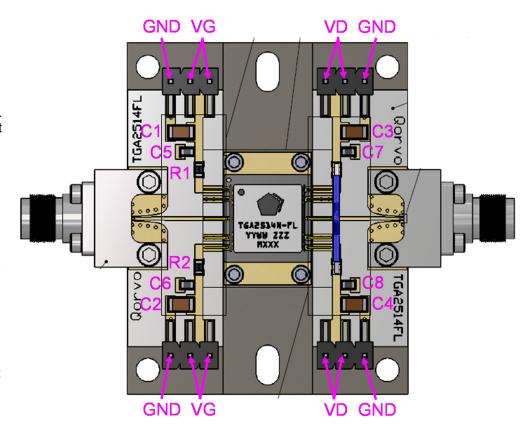
- 1. Turn off RF signal
- 2. Reduce  $V_G$  to -1.5V. Ensure  $I_{DQ} \sim 0 mA$
- 3. Set V<sub>D</sub> to 0V
- 4. Turn off V<sub>D</sub> supply
- 5. Turn off  $V_G$  supply

### **Applications Information**

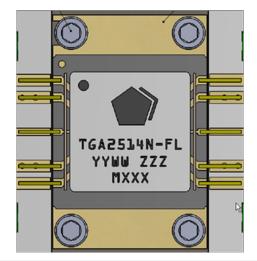
# **Evaluation Board Layout**

RF Layer is 0.010" thick Rogers Corp. RO4350, εr = 3.38. Metal layers are 0.5 oz. copper. The microstrip line at the connector interface is optimized for the Southwest Microwave end launch connector 1092-01A-5.

The pad pattern shown has been developed and tested for optimized assembly at TriQuint Semiconductor. The PCB land pattern has been developed to accommodate lead and package tolerances. Since surface mount processes vary from company to company, careful process development is recommended.



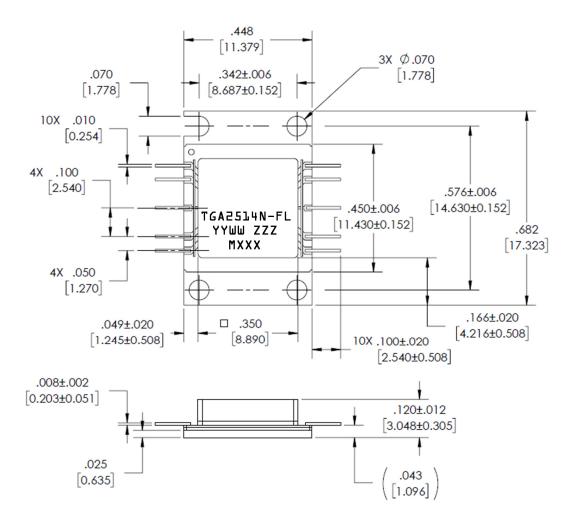
## **Package Mounting Detail**



Ref. Designation.	Value	Description	Manufacturer
C1 – C4	1 uF, 50V, 5%	CAP CER X7R 1206	Various
C5-C8	1000 pF, 50V, 5%	CAP CER NP0 0603	Various
R1 – R2	10 OHM, 1/10W	RES 0603, SMD	Various

### 13-16 GHz Power Amplifier

### **Mechanical Drawing**



(1) Units: inches [mm]

(2) Exposed metallization is Ni/Au plated

(3) Parts are non-hermetic

(4) Unless noted, tolerance is ±.005

(5) Drawing subject to change without notice

Marking:

TGA2514N-FL: Part number YY: Part Assembly year WW: Part Assembly week

ZZZ: Serial Number MXXX: Batch ID

Pin No.	Label	Description
1, 5	$V_{G}$	Gate Voltage; bias network is required, part can be biased from either pin
2, 4, 7, 9	GND	Ground
3	RF Input	RF Input; 50 Ω, AC coupled
6, 10	V <sub>D</sub>	Drain Voltage; bias network is required, part must be biased from both pins
8	RF Output	RF Output; 50 Ω, AC coupled



# TGA2514N-FL

13-16 GHz Power Amplifier

# **Assembly Notes**

- 1. Clean the board or module with alcohol. Allow it to dry fully.
- 2. Nylock screws are recommended for mounting the TGA2514N-FL to the neat sink carrier.
- 3. To improve the thermal and RF performance, we recommend applying thermal compound or a 4 mil thick indium shim between the package and the heat sink
- 4. Apply solder to each pin of the TGA2514N-FL.
- 5. Clean the assembly with alcohol.



### TGA2514N-FL

13-16 GHz Power Amplifier

### **Product Compliance Information**

### **ESD Sensitivity Ratings**



Caution! ESD-Sensitive Device

ESD Rating: 0B Value: ≤ 250 V

Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

### **MSL Rating**

Level 3 at +260 °C convection reflow.

The part is rated Moisture Sensitivity Level 3 at 260 ℃ per JEDEC standard IPC/JEDEC J-STD-020.

#### **ECCN**

US Department of Commerce: 3A001.b.2.b

### **Solderability**

Compatible with the latest version of J-STD-020, lead free solder, 260 ℃

#### **RoHS-Compliance**

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C15H12Br402) Free
- PFOS Free
- SVHC Free

#### **Contact Information**

For the latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

Web: <u>www.triquint.com</u> Tel: +1.972.994.8465

Email: <u>customer.support@qorvo.com</u>

For information about the merger of RFMD and TriQuint as Qorvo:

Web: www.qorvo.com

For technical questions and application information: **Email:** <u>info-products@tqs.com</u>

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