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Onsemi

Silicon Carbide (SiC) **MOSFET** – EliteSiC, 65 mohm, 1200 V, M3S, TO-247-3L NTHL070N120M3S

Features

- Typ. $R_{DS(on)} = 65 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge ($Q_{G(tot)} = 57 \text{ nC}$)
- High Speed Switching with Low Capacitance ($C_{oss} = 57 \text{ pF}$)
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb-Free 2LI (on second level interconnection)

Typical Applications

- Solar Inverters
- Electric Vehicle Charging Stations
- UPS (Uninterruptible Power Supplies)
- Energy Storage Systems
- SMPS (Switch Mode Power Supplies)

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	1200	V
Gate-to-Source Voltage			V _{GS}	-10/+22	V
Continuous Drain Current (Notes 1, 3)	Steady T _C =25°C State		Ι _D	34	А
Power Dissipation (Note 1)			PD	160	W
Continuous Drain Current (Notes 1, 3)	Steady State	T _C =100°C	۱ _D	24	А
Power Dissipation (Note 1)			PD	80	W
Pulsed Drain Current (Note 2)	$T_{\rm C} = 25^{\circ}{\rm C}$		I _{DM}	98	A
Operating Junction and Storage Temperature Range		T _J , T _{stg}	–55 to +175	°C	
Source Current (Body Diode) $T_C = 25^{\circ}C, V_{GS} = -3 V$		ا _S	31	A	
Single Pulse Drain-to-S Energy (Note 4)	gle Pulse Drain-to-Source Avalanche ergy (Note 4)		E _{AS}	91	mJ
Maximum Lead Tempera (1/25" from case for 10 s	aximum Lead Temperature for Soldering /25" from case for 10 s)		ΤL	270	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

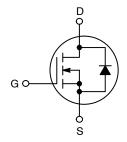
- 1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. The maximum current rating is based on typical $R_{DS(on)}$ performance. 4. EAS of 91 mJ is based on starting $T_J = 25^{\circ}$ C; L = 1 mH, I_{AS} = 13.5 A, $V_{DD} = 100 \text{ V}, V_{GS} = 18 \text{ V}.$

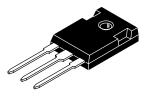
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DATA SHEET

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
1200 V	87 mΩ @ 18 V	34 A

N-CHANNEL MOSFET





TO-247-3L CASE 340CX

MARKING DIAGRAM



HL070N120M3S = Specific Device Code

- А = Assembly Location
- Y = Year
- WW = Work Week
- ΖZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NTHL070N120M3S	TO-247-3L	30 Units / Tube

Table 1. THERMAL CHARACTERISTICS

Parameter		Max	Unit
Junction-to-Case - Steady State (Note 1)		0.94	°C/W
Junction-to-Ambient - Steady State (Note 1)		40	

Table 2. RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Value	Unit
Operation Values of Gate-to-Source Voltage	V _{GSop}	-53 +18	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

Table 3. ELECTRICAL CHARACTERISTICS (T_J = $25^{\circ}C$ unless otherwise specified)

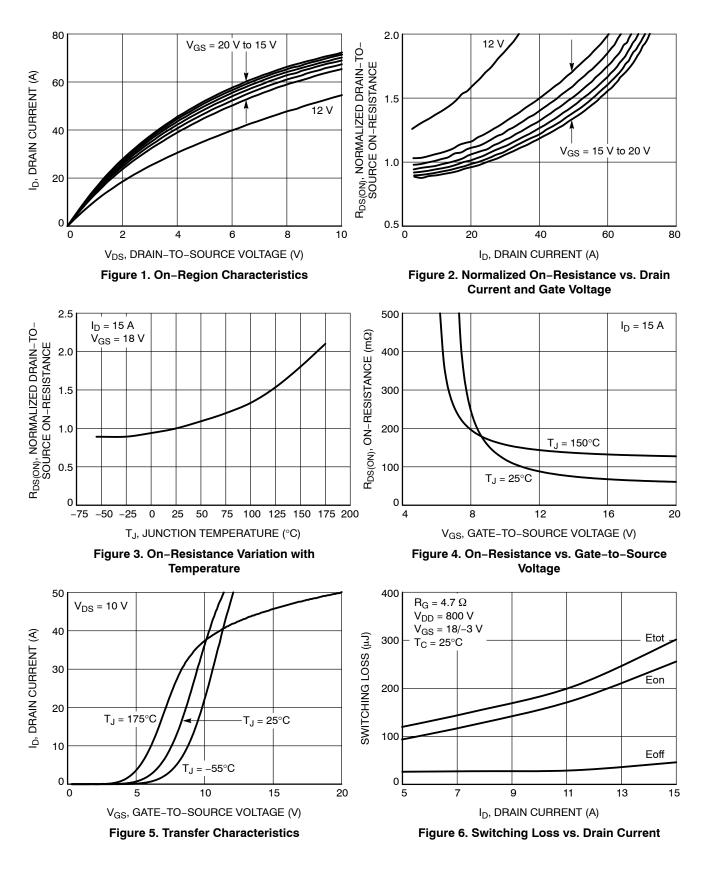
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF-STATE CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 1 mA	1200	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	$I_D = 1 \text{ mA}$, referenced to $25^{\circ}C$ (Note 6)	-	0.3	_	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V,$ $T_{J} = 25^{\circ}C$ $V_{DS} = 1200 V$	_	-	100	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +22/-10 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	-	-	±1	μA
ON-STATE CHARACTERISTICS (Note 2	2)					
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 7 \text{ mA}$	2.04	2.9	4.4	V
Drain-to-Source On Resistance	R _{DS(on)}	V_{GS} = 18 V, I_D = 15 A, T_J = 25°C	-	65	87	mΩ
		V _{GS} = 18 V, I _D = 15 A, T _J = 175°C (Note 6)	-	136	-	
Forward Transconductance	9 _{FS}	V _{DS} = 10 V, I _D = 15 A (Note 6)	-	12	-	S
CHARGES, CAPACITANCES & GATE R	ESISTANCE					
Input Capacitance	C _{ISS}	V_{GS} = 0 V, f = 1 MHz, V_{DS} = 800 V	-	1230	-	pF
Output Capacitance	C _{OSS}		-	57	-	
Reverse Transfer Capacitance	C _{RSS}		-	5	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -3/18 \text{ V}, \text{ V}_{DS} = 800 \text{ V},$ $I_D = 15 \text{ A}$	-	57	-	nC
Threshold Gate Charge	Q _{G(TH)}	1 _D = 15 A	-	3.2	-	-
Gate-to-Source Charge	Q _{GS}		-	9.6	-	
Gate-to-Drain Charge	Q _{GD}		-	17	-	
Gate-Resistance	R _G	f = 1 MHz	-	4.3	-	Ω
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$	-	10	-	ns
Rise Time	t _r	$I_D = 15 \text{ A}, R_G = 4.7 \Omega$ Inductive load (Notes 5, 6)	-	24	-	
Turn-Off Delay Time	t _{d(OFF)}	· · · · ·	-	29	-	
Fall Time	t _f		-	9.6	-	
Turn-On Switching Loss	E _{ON}		-	254	-	μJ
Turn–Off Switching Loss	E _{OFF}		-	46	-	1
Total Switching Loss	E _{tot}		-	300	-	

Table 3. ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified) (continued)

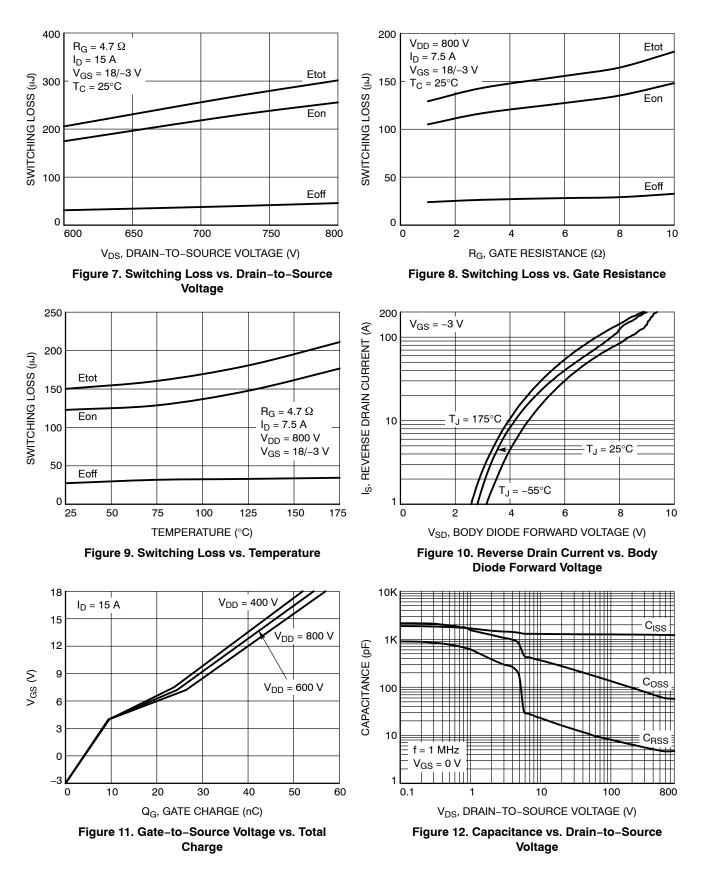
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
SOURCE-DRAIN DIODE CHARACTERIS	rics		-	-		
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -3 \text{ V}, \text{ T}_{C} = 25^{\circ}\text{C} \text{ (Note 6)}$	-	-	31	A
Pulsed Source-Drain Diode Forward Current (Note 2)	I _{SDM}		-	-	98	
Forward Diode Voltage	V _{SD}	$V_{GS} = -3 \text{ V}, \text{ I}_{SD} = 15 \text{ A}, \text{ T}_{J} = 25^{\circ}\text{C}$	-	4.7	-	V
Reverse Recovery Time	t _{RR}	$V_{GS} = -3/18 \text{ V}, I_{SD} = 15 \text{ A},$	-	14	-	ns
Reverse Recovery Charge	Q _{RR}	dl _S /dt = 1000 A/µs, V _{DS} = 800 V (Note 6)	-	57	-	nC
Reverse Recovery Energy	E _{REC}	1	-	3.1	-	μJ
Peak Reverse Recovery Current	I _{RRM}	1	-	8.2	-	А
Charge Time	T _A	1	-	7.7	-	ns
Discharge Time	Т _В	1	_	6.2	-	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
5. E_{ON}/E_{OFF} result is with body diode.
6. Defined by design, not subject to production test.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

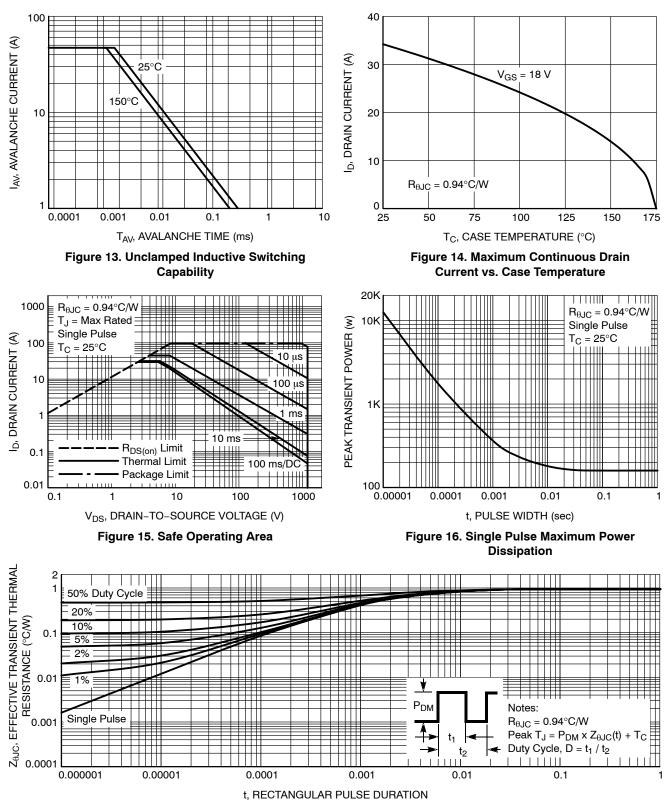
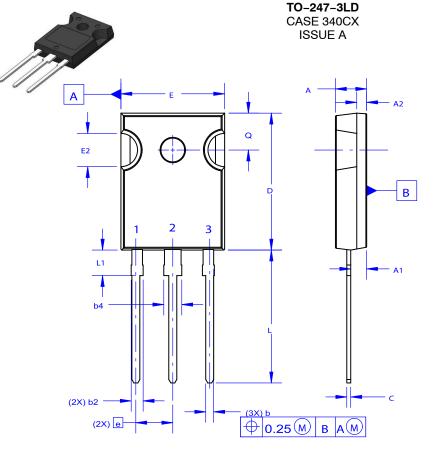


Figure 17. Junction-to-Case Transient Thermal Response





NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

γ

GENERIC **MARKING DIAGRAM*** Х



XXXXX	= Specific Device Code
Α	= Assembly Location

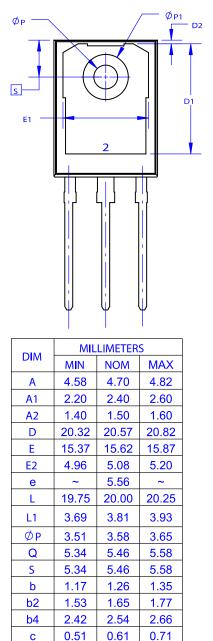
- = Assembly Location
- = Year
- ww = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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DATE 06 JUL 2020



D1

D2

E1

ØP1

13.08

0.51

12.81

6.60

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0.93

~

6.80

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1.35

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7.00

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