

# **RDBF4005 THRU RDBF410**

### SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

#### FEATURES

- $\cdot$  Fast recovery glass passivated chip
- $\cdot$  Low forward voltage drop
- · High surge current capability
- $\cdot$  Low reverse leakage current
- $\cdot$  Ideal for printed circuit board
- · High temperature soldering guaranteed:
- 260°C for 10 seconds

#### MECHANICAL DATA

Case: Molded plastic, DBF Epoxy: UL 94V-0 rate flame retardant Terminals: Pure tin plated, lead free, Leads solderable per MIL-STD-202, method 208 Mounting position: as Marking Weight: 0.213gram



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#### Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

	Symbols	RDBF4005	RDBF401	RDBF402	RDBF404	RDBF406	RDBF408	RDBF410	Units
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TC=115 $^{\circ}{\mathbb{C}}$	I(AV)				4.0				Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	IFSM	150							Amp
Maximum Instantaneous Forward Voltage at 25°C @IF=4.0A	$\mathbf{V}_{\mathbf{F}}$	1.3							Volts
Maximum Reverse Current at Rated DC Blocking Voltage @TA=25℃ @TA=125℃	IR	5.0 500							uAmp
Maximum Reverse Recovery Time (Note 1)	Trr		1:	50		250	5	00	nS
Typical Thermal Resistance between junction and lead	Rojl	15							°C/W
Operating and Storage Temperature Range	TJ, Tstg	-55 to +150							ů

#### NOTE:

1- Reverse Recovery Test Conditions: IF=.5A, IR=1A, IRR=.25A.



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#### **RATINGS AND CHARACTERISTIC CURVES**



FIG.5- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





version: 02