

General Description

The CMSC7423B combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDS(ON). This device is ideal for load switch and battery protection applications.

Product Summary

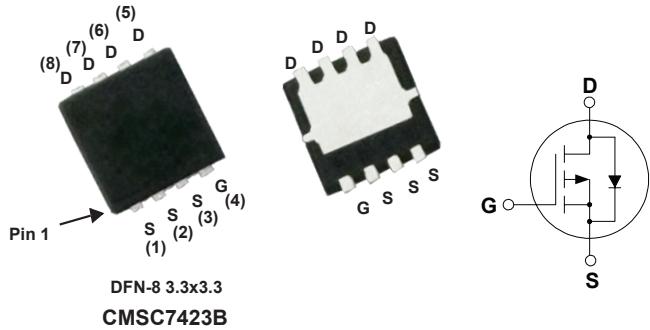
BVDSS	RDS(ON)	ID
-20V	6mΩ	-50A

Applications

- High side in DC - DC Buck Converters
- Notebook battery power management
- Load switch in Notebook

Features

- P-Channel MOSFET
- Low ON-resistance
- Surface Mount Package
- RoHS Compliant

DFN-8 3.3x3.3 Pin Configuration**Absolute Maximum Ratings (T_A=25 °C Unless Otherwise Noted)**

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D	Continuous Drain Current	-50	A
I _{DM}	Pulsed Drain Current	-150	A
EAS	Single Pulse Avalanche Energy ¹	90	mJ
P _D @T _C =25°C	Total Power Dissipation	85	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance, Junction-to-Ambient	---	55	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-20	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-4.5\text{V}$, $I_D=-20\text{A}$	---	---	6	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$, $I_D=-20\text{A}$	---	---	8.5	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-0.5	---	-1.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-16\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 12\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_D=-4\text{A}$	---	23	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	10	---	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=-10\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_D=-20\text{A}$	---	70	---	nC
Q_{gs}	Gate-Source Charge		---	10	---	
Q_{gd}	Gate-Drain Charge		---	20	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=-10\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $R_{\text{GEN}}=3\Omega$	---	18	---	ns
T_r	Rise Time		---	52	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	290	---	
T_f	Fall Time		---	125	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	5500	---	pF
C_{oss}	Output Capacitance		---	930	---	
C_{rss}	Reverse Transfer Capacitance		---	720	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{SD}}=-1\text{A}$	---	---	-1.2	V

Notes:

1. The test condition is $V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_D = 13\text{A}$.

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