

Features

- Trench LV MOSFET Technology
- ESD Protected up to 2KV (HBM)
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

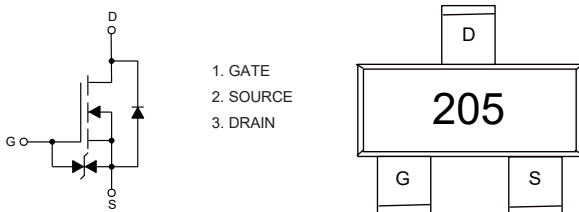
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 150°C/W Junction to Ambient (Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	50	V
Gate-Source Voltage	V_{GS}	±8	V
Continuous Drain Current	I_D	$T_A=25^\circ\text{C}$	0.2
		$T_A=100^\circ\text{C}$	0.13
Pulsed Drain Current (Note 3)	I_{DM}	0.8	A
Total Power Dissipation (Note 4)	P_D	0.83	W

Note:

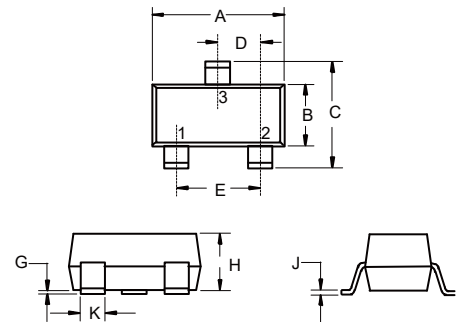
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-ambient thermal resistance.

Internal Structure and Marking Code



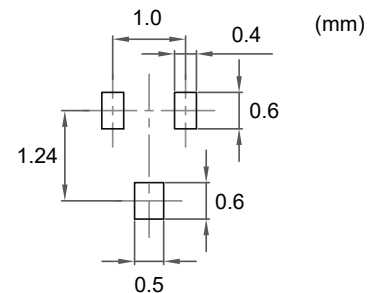
N-CHANNEL MOSFET

SOT-523



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.059	0.067	1.50	1.70	
B	0.030	0.033	0.75	0.85	
C	0.057	0.069	1.45	1.75	
D	0.020		0.50		TYP.
E	0.035	0.043	0.90	1.10	
G	0.000	0.004	0.00	0.10	
H	0.024	0.031	0.60	0.80	
J	0.004	0.008	0.10	0.20	
K	0.006	0.014	0.15	0.35	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	50			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 8V$			± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=40V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.3	0.5	0.8	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=0.2A$		2.5	4	Ω
		$V_{GS}=2.5V, I_D=0.2A$		2.6	4.3	
Gate Resistance	R_g	f=1 MHz, Open drain		110		Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=0.2A$		0.9		S
Diode Characteristics						
Continuous Body Diode Current	I_S				0.2	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=0.2A$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F=0.2A, di/dt=100A/\mu s$		10		ns
Reverse Recovery Charge	Q_{rr}			1.8		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		33		μF
Output Capacitance	C_{oss}			4.8		
Reverse Transfer Capacitance	C_{rss}			3.6		
Total Gate Charge	Q_g	$V_{DS}=25V, V_{GS}=4.5V, I_D=0.2A$		0.7		nC
Gate-Source Charge	Q_{gs}			0.1		
Gate-Drain Charge	Q_{gd}			0.2		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=25V, V_{GS}=4.5V, R_G=6\Omega, I_D=0.2A$		4.5		ns
Turn-On Rise Time	t_r			4.3		
Turn-Off Delay Time	$t_{d(off)}$			23		
Turn-Off Fall Time	t_f			14		

Curve Characteristics

Fig.1 - Typical Output Characteristics

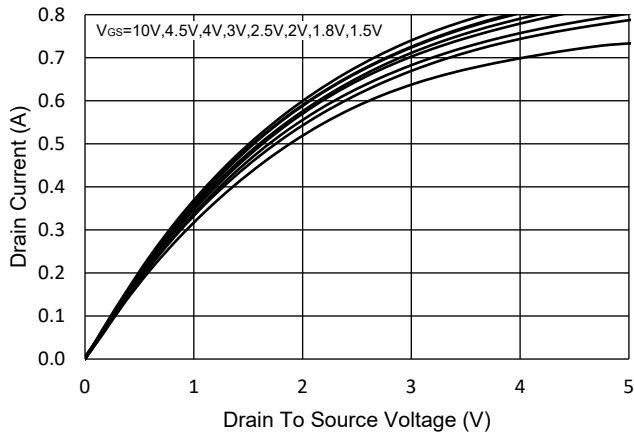


Fig.2 - Transfer Characteristic

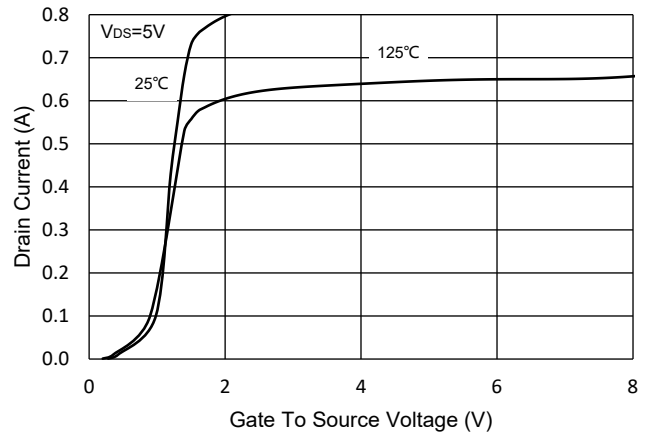


Fig.3 - $R_{DS(ON)}$ - V_{GS}

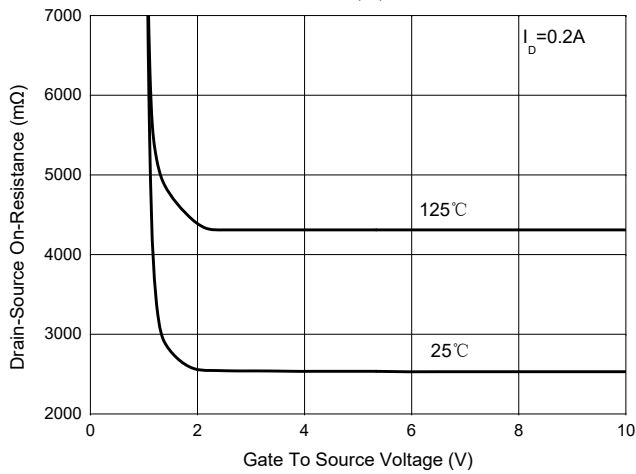


Fig.4 - $R_{DS(ON)}$ - I_D

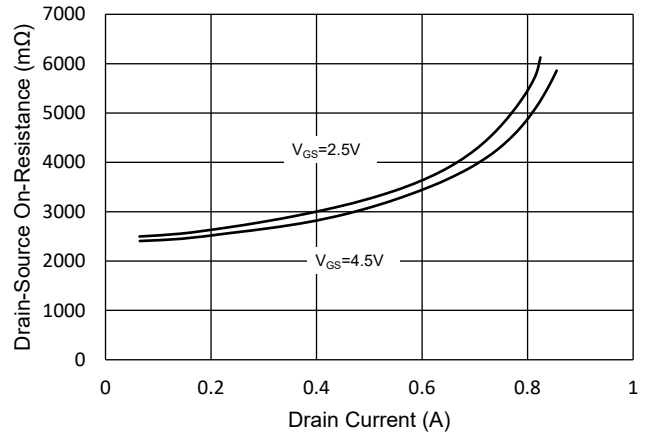


Fig.5 - Capacitance Characteristics

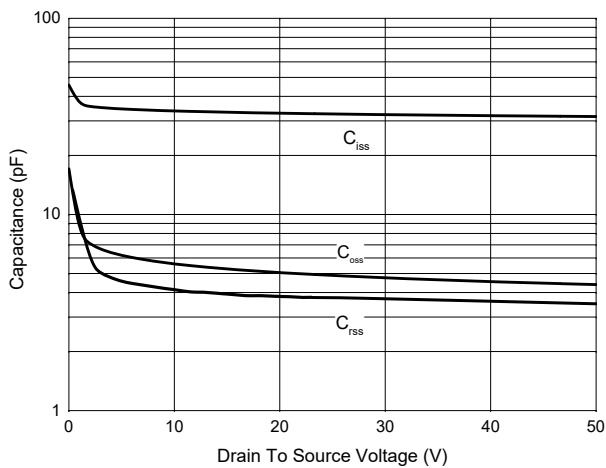
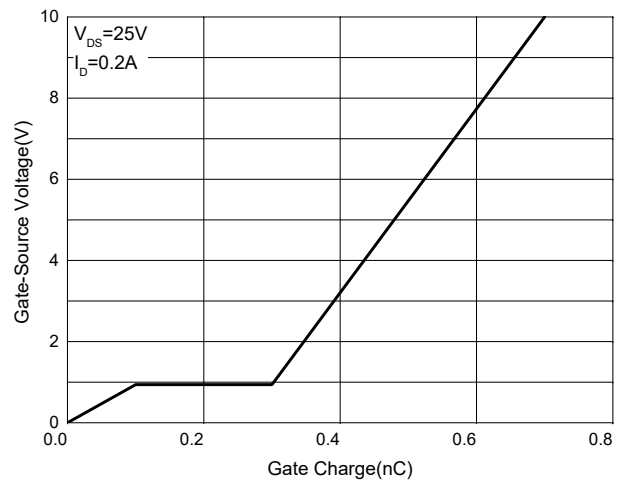


Fig.6 - Gate Charge



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

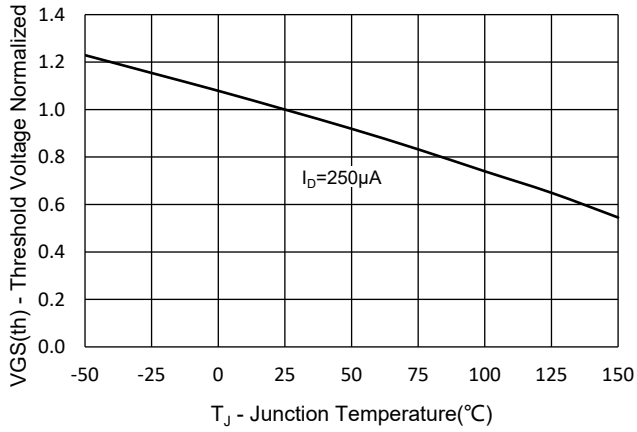


Fig.8 - Normalized On Resistance Characteristics

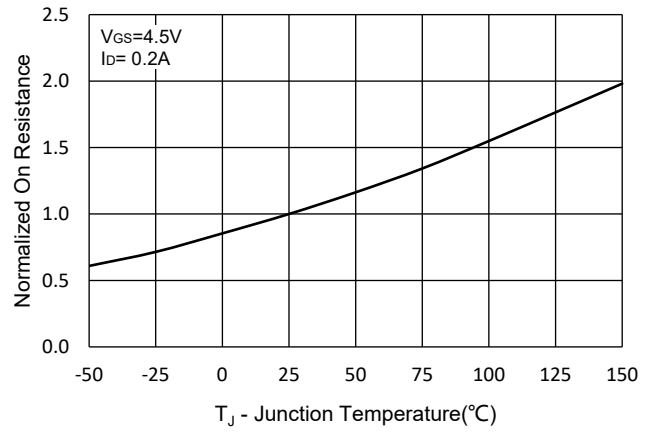


Fig.9 - $I_S - V_{SD}$

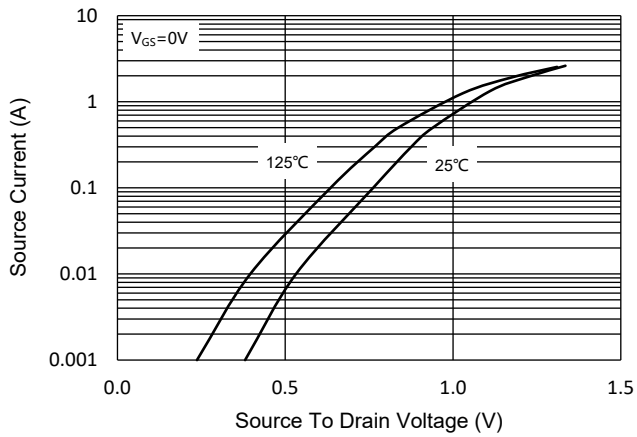


Fig.10 - Drain Current

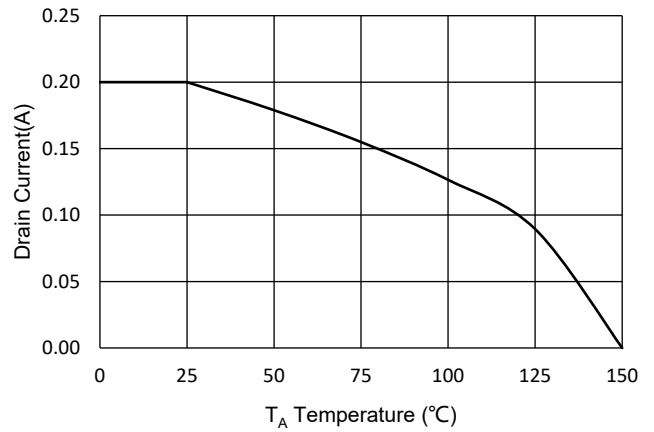
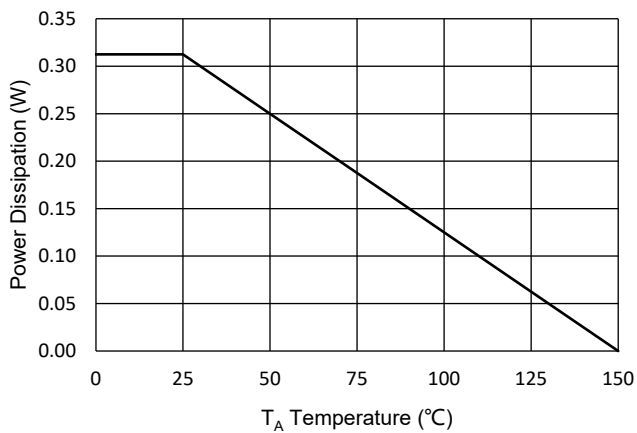


Fig.11 - PD Dissipation



Curve Characteristics

Fig.12 - Safe Operation Area

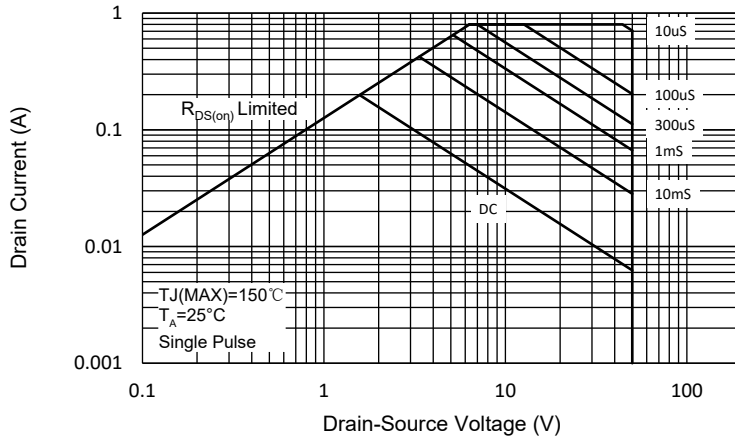
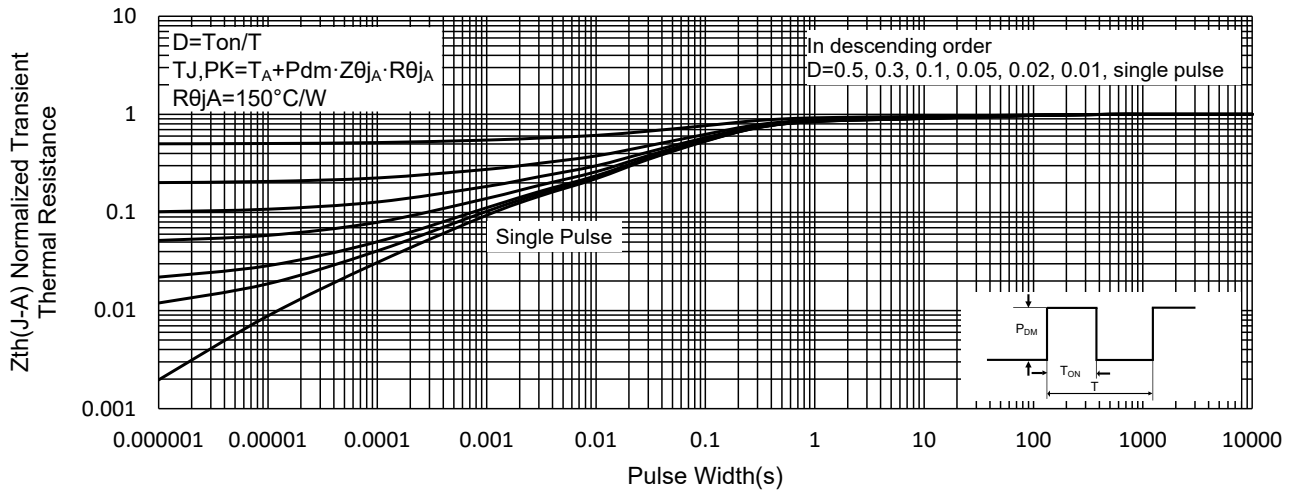


Fig.13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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