

HY5012W-VB Datasheet N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY	
V _{DS} (V)	100
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.002
I _D (A) ^a	320
Configuration	Single

FEATURES

- TrenchFET[®] Power MOSFET
- Package with Low Thermal Resistance
- 100 % Rg and UIS Tested





Top View

G

N-Channel MOSFET

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PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C ^a		320		
Continuous Drain Current	T _C = 125 °C	I _D	240		
Continuous Source Current (Diode Conduction) ^a		I _S	320	А	
Pulsed Drain Current ^b		I _{DM}	1220		
Single Pulse Avalanche Current	Current		123		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	366	mJ	
Maximum Revuer Dissignation	T _C = 25 °C	6	650	W	
Maximum Power Dissipation ^b	T _C = 125 °C	P _D	183	vv	
Operating Junction and Storage Temperatu	re Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.6	C/W

Notes

a. Base on Tc = 25° C.

b. Pulse test; pulse width $\leq 300~\mu\text{s},~\text{duty}~\text{cycle} \leq 2~\%.$

c. When mounted on 1" square PCB (FR-4 material).

d. Parametric verification ongoing.

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	-1						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0, I_D = 250 \ \mu A$		100	-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μΑ	2.5	3.0	3.5	v
Gate-Source Leakage	I _{GSS}	V _{DS} =	0 V, V _{GS} = ± 20 V	-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 100 V	-	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 100 V, T _J = 125 °C	-	-	50	μA
		$V_{GS} = 0 V$	V _{DS} = 100 V, T _J = 175 °C	-	-	500	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 20 A	-	0.0020	-	Ω
	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C		0.0054	-	
		V _{GS} = 10 V	I _D = 30 A, T _J = 175 °C	-	0.0080	-	
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		-	82	-	S
Dynamic ^b	•						•
Input Capacitance	C _{iss}			-	9780	12230	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = 25 \text{ V}, \text{ f} = 1 \text{ MHz}$	-	3070	3840	pF
Reverse Transfer Capacitance	C _{rss}	1		-	305	385	
Total Gate Charge ^c	Qg			-	125	190	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 50 \text{ V}, \text{ I}_{D} = 70 \text{ A}$	-	28	-	nC
Gate-Drain Charge ^c	Q _{gd}			-	46	-	1
Gate Resistance	R _g	f = 1 MHz		1.6	3.3	5	Ω
Turn-On Delay Time ^c	t _{d(on)}	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 0.7 \Omega$ $\text{I}_{D} \cong 70 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$		-	16	25	- ns
Rise Time ^c	t _r			-	110	165	
Turn-Off Delay Time ^c	t _{d(off)}			-	40	60	
Fall Time ^c	t _f			-	12	20	
Source-Drain Diode Ratings and Char	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	480	Α
Forward Voltage	V _{SD}	IF =	= 100 A, V _{GS} = 0	-	0.9	1.5	V

Notes

a. Pulse test; pulse width $\leq 300~\mu\text{s},$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

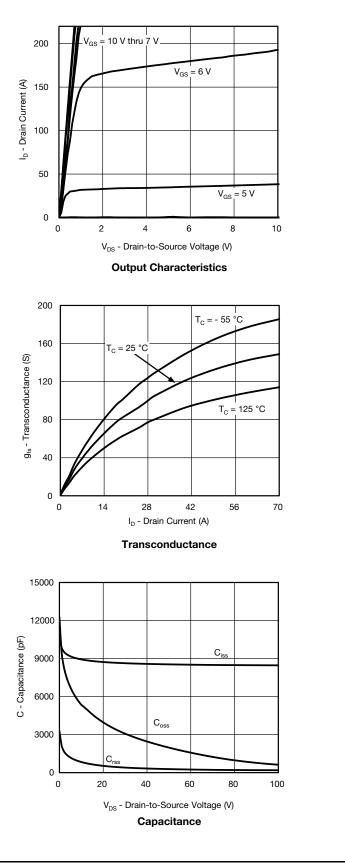
c. Independent of operating temperature.

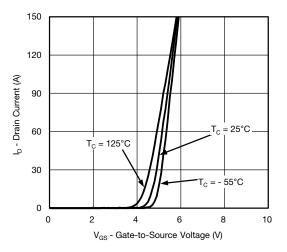
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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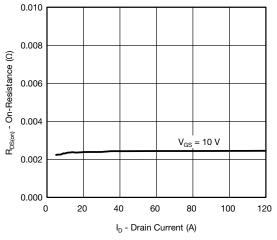


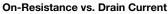
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

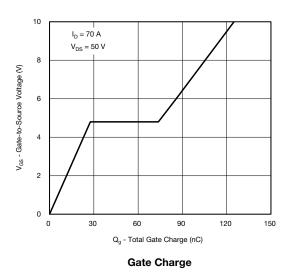




Transfer Characteristics

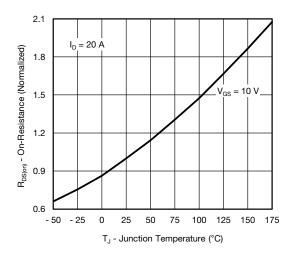




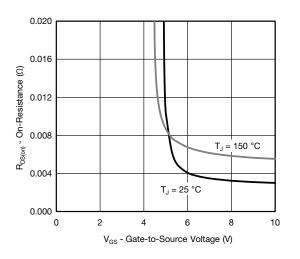




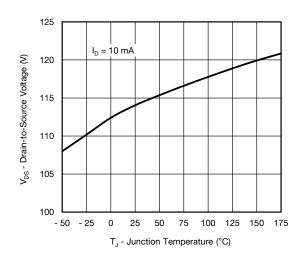
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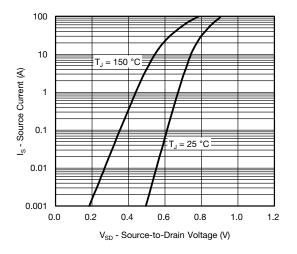
On-Resistance vs. Junction Temperature



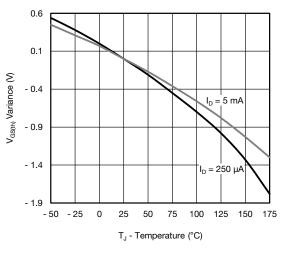
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



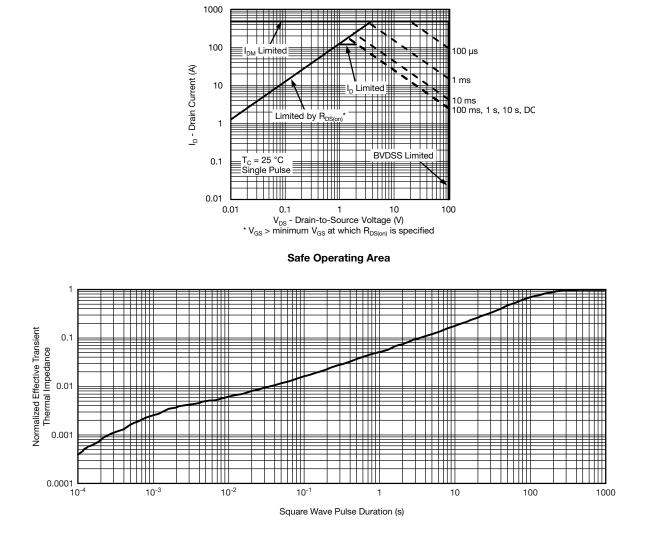
Source Drain Diode Forward Voltage



Threshold Voltage



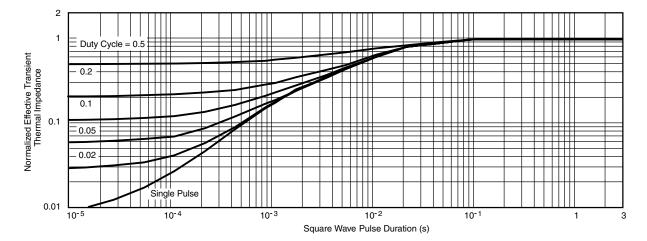
THERMAL RATINGS ($T_A = 25 \ ^{\circ}C$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

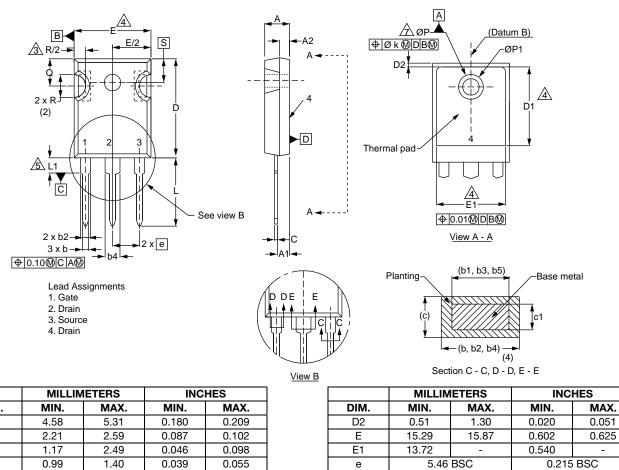
- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-247AC



DIM.	MIN.	MAX.	MIN.	MAX.
А	4.58	5.31	0.180	0.209
A1	2.21	2.59	0.087	0.102
A2	1.17	2.49	0.046	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.53	2.39	0.060	0.094
b3	1.65	2.37	0.065	0.093
b4	2.42	3.43	0.095	0.135
b5	2.59	3.38	0.102	0.133
С	0.38	0.86	0.015	0.034
c1	0.38	0.76	0.015	0.030
D	19.71	20.82	0.776	0.820
D1	13.08	-	0.515	-

	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	
E1	13.72	-	0.540	-	
е	5.46 BSC		0.215	BSC	
Øk	0.2	0.254		010	
L	14.20	16.25	0.559	0.640	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	7.62 BSC		BSC	
ØР	3.51	3.66	0.138	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	



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