

# N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>e</sup>	Q <sub>g</sub> (Typ.)			
	0.028 at V <sub>GS</sub> = 4.5 V	6 <sup>a</sup>				
20	0.042 at V <sub>GS</sub> = 2.5 V	at $V_{GS} = 2.5 \text{ V} \qquad 6^{\text{a}} \qquad 8$				
	0.050 at V <sub>GS</sub> = 1.8 V	5.6				



- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 %  $\rm R_g$  Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- DC/DC Converters
- Load Switch for Portable Applications

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20	V	
Gate-Source Voltage		V <sub>GS</sub>	± 12	V	
	T <sub>C</sub> = 25 °C		6 <sup>a</sup>		
Continuous Drain Current (T 150 °C)	T <sub>C</sub> = 70 °C		5.1		
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C		5 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		4 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	20		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		1.75		
Commuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.04 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		2.1		
Maximum Bower Dissinction	T <sub>C</sub> = 70 °C		1.3	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.25 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera	Ŭ	260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	80	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	40	60	- C/ W	

Notes:

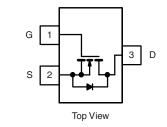
a. Package limited

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 125 °C/W.

e. Based on T\_C = 25 °C.



SOT-23

FREE

$\begin{array}{ c c c c c c } \hline Parameter & Symbol & Test Conditions & Min. Typ. Max. Unit \\ \hline State & \\ \hline State & \\ \hline State & \\ \hline Drain-Source Breakdown Voltage & V_{DS} & V_{QS} = 0 V, I_D = 250 \ \mu A & 20 & 25 & V & V \\ V_{DS} Temparature Coefficient & AV_{DS}/T_J & I_D = 250 \ \mu A & -2.6 & 0 & VV \\ \hline Caste-Source Threshold Voltage & V_{OS}(h) & V_{DS} = 0.7, I_D = 250 \ \mu A & 0.45 & 1.0 & V \\ \hline Caste-Source Leakage & I_{DSS} & V_{DS} = 0.7, V_{OS} = 4.8 & V & 100 & nA \\ \hline Caste-Source Leakage & I_{DSS} & V_{DS} = 0.7, V_{OS} = 4.5 & 20 & -1 & 10 \\ \hline V_{DS} = 20.7, V_{OS} = 0.7, J_D = 70 \ C & 0.1 & 1 & U \\ \hline V_{DS} = 20.7, V_{OS} = 0.7, J_D = 70 \ C & 0.042 & 0.45 & 0.028 & -1 & 0 \\ \hline V_{DS} = 20.7, V_{OS} = 4.5 \ V, I_D = 5.0 \ A & 0.028 & -1 & 0 \\ \hline V_{DS} = 20.7, V_{OS} = 4.5 \ V, I_D = 5.0 \ A & 0.042 & 0.060 & -1 & 0 \\ \hline V_{DS} = 10 \ V, V_{OS} = 10 \ V, V_{OS} = 0 \ V, I_D = 5.0 \ A & 0.042 & 0.060 & -1 & 0 \\ \hline Porward Transconductance^8 & Q_{S} & V_{DS} = 10 \ V, V_{OS} = 5 \ V, I_D = 5.0 \ A & 0.042 & 0.050 & -1 & 0 \\ \hline Portard Capacitance & C_{res} & V_{DS} = 10 \ V, V_{OS} = 5 \ V, I_D = 5.0 \ A & 0.042 & 0.050 & -1 & 0 \\ \hline Portard Gate Charge & Q_{g} & V_{DS} = 10 \ V, V_{OS} = 5 \ V, I_D = 5.0 \ A & 0.042 & 0.050 & -1 & 0 \\ \hline Total Gate Charge & Q_{g} & V_{DS} = 10 \ V, V_{OS} = 5 \ V, I_D = 5.0 \ A & 1.1 & -1 & 0 \\ \hline Portard Capacitance & C_{res} & V_{DS} = 10 \ V, V_{OS} = 5 \ V, I_D = 5.0 \ A & 1.1 & -1 & 0 \\ \hline Caste Resistance & R_{g} & f = 1 \ MHz & 0.5 \ A & 0.050 & -1 & 1.1 & 0 \\ \hline Caste Resistance & R_{g} & f = 1 \ MHz & 0.5 \ A & 0.050 & -1 & 1.1 & 0 \\ \hline Caste Resistance & R_{g} & f = 1 \ MHz & 0.5 \ A & 0.050 & -1 & 0.1 & 0 \\ \hline Caste Resistance & R_{g} & I_D \ V, V_{OS} = 5 \ V, I_D = 5.0 \ A & 1.1 & -1 & 0 \\ \hline Caste Resistance & R_{g} & I_D \ V, V_{DD} = 10 \ V, R_{L} = 2.2 \ A & 1.1 & 0.5 \ A & 1.1 & 0.5 & 100 \\ \hline Caste Resistance & R_{g} & I_D \ V_{DD} = 10 \ V, R_{L} = 2.2 \ A & 1.1 & 2.0 & 0.5 \ A & 1.1 & 0 \\ \hline Caste Resistance & R_{g} & I_D \ V_{DD} = 10 \ V, R_{L} = 2.2 \ A & 1.1 & 0 & 0 \\$	<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted							
		1 1		Min.	Typ.	Max.	Unit	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Static			<u> </u>				
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	20			V	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L _ 250 uA		25		m)//0C	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 2.6		mv/°C	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.45		1.0	V	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		1	$V_{DS} = 20 V, V_{GS} = 0 V$			1		
$ \begin{array}{ c c c c c } & V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 0.028 \\ \hline V_{GS} = 2.5 \ V, \ I_D = 4.7 \ A & 0.042 \\ \hline V_{GS} = 1.8 \ V, \ I_D = 4.3 \ A & 0.050 \\ \hline V_{GS} = 1.8 \ V, \ I_D = 4.3 \ A & 0.050 \\ \hline V_{GS} = 1.8 \ V, \ I_D = 4.3 \ A & 0.050 \\ \hline V_{GS} = 1.8 \ V, \ I_D = 5.0 \ A & 0.042 \\ \hline V_{GS} = 1.8 \ V, \ I_D = 5.0 \ A & 0.028 \\ \hline V_{GS} = 10 \ V, \ I_D = 5.0 \ A & 0.042 \\ \hline V_{GS} = 10 \ V, \ I_D = 5.0 \ A & 0.042 \\ \hline V_{GS} = 10 \ V, \ I_D = 5.0 \ A & 0.050 \\ \hline V_{DS} = 10 \ V, \ I_D = 5.0 \ A & 0.050 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 0 \ V, \ I_D = 5.0 \ A & 0.050 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 0 \ V, \ I_D = 5.0 \ A & 0.050 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_D = 5.0 \ A & 0.05 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_D = 5.0 \ A & 1.1 \\ \hline I_D \ A & A \ A \$	Zero Gate Voltage Drain Current	DSS	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			10	μΑ	
$\begin{array}{ c c c c c } \hline \mbox{Drain-Source On-State Resistance}^a & \mbox{P}_{GS} & & \mbox{V}_{GS} = 2.5 \ V, \ \mbox{P}_{GS} = 1.8 \ V, \ \mbox{P}_{GS} = 1.8 \ V, \ \mbox{P}_{GS} = 1.8 \ V, \ \mbox{P}_{DS} = 10 \ V, \ \mbox{P}_{DS} = 10 \ V, \ \mbox{P}_{DS} = 10 \ V, \ \mbox{P}_{DS} = 0 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 5 \ V, \ \mbox{P}_{S} = 0 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 5 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = 5 \ V, \ \mbox{P}_{S} = 5 \ V, \ \mbox{P}_{S} = 10 \ V, \ \mbox{P}_{S} = $	On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}{\leq}5$ V, $V_{GS}{=}4.5$ V	20			Α	
$ \begin{array}{ c c c c c } \hline \begin{tabular}{ c c c c } \hline \hline \begin{tabular}{ c c c c c } \hline \hline \begin{tabular}{ c c c c c c } \hline \hline \begin{tabular}{ c c c c c c c } \hline \hline \begin{tabular}{ c c c c c c } \hline \hline \begin{tabular}{ c c c c c c } \hline \hline \begin{tabular}{ c c c c c c c } \hline \hline \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$			$V_{GS} = 4.5 \text{ V}, I_D = 5.0 \text{ A}$		0.028			
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 4.7 \text{ A}$		0.042		Ω	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			$V_{GS} = 1.8 \text{ V}, I_D = 4.3 \text{ A}$		0.050		1	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		24		S	
$ \begin{array}{ c c c c c } \hline \mbox{Output Capacitance} & \mbox{C}_{Oss} & \mbox{V}_{DS} = 10 \ V, \ V_{GS} = 0 \ V, \ f = 1 \ MHz & \mbox{MHZ} & \mbox{105} & \mbox{MHZ} & \mbox{S} & \mbox{MHZ} & \mbox{S} & \$	Dynamic <sup>b</sup>				•	•	•	
$ \begin{array}{c c c c c c c c } \hline Reverse Transfer Capacitance & C_{rss} & & & & & & & & & & & & & & & & & & $	Input Capacitance	C <sub>iss</sub>			865			
$ \begin{array}{c c c c c c c c } \hline Reverse Transfer Capacitance & C_{rss} & & & & & & & & & & & & & & & & & & $	Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 1 MHz		105		pF	
$ \begin{array}{ c c c c c } \mbox{Idate Charge} & Q_g \\ \hline Gate-Source Charge & Q_{gd} \\ \hline Gate-Drain Charge & Q_{gd} \\ \hline Q_{gd} \\ \hline$	Reverse Transfer Capacitance				55		-	
$ \begin{array}{ c c c c c } \hline \begin{tabular}{ c c c c c } \hline \begin{tabular}{ c c c c c } \hline \begin{tabular}{ c c c c c c } \hline \begin{tabular}{ c c c c c c c } \hline \begin{tabular}{ c c c c c c c c } \hline \begin{tabular}{ c c c c c c c c } \hline \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	Total Cata Charge	0	$V_{DS}$ = 10 V, $V_{GS}$ = 5 V, $I_{D}$ = 5.0 A		12	18		
$ \begin{array}{ c c c c c c c } \hline Gate-Source Charge & G_{gs} & V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 1.1 & 0.7 &$	Iolai Gale Charge	Qg			8.8	14		
$ \begin{array}{c c c c c c c c c c } \hline Gate Resistance & F_g & f = 1  \text{MHz} & 0.5 & 2.4 & 4.8 & \Omega \\ \hline \mbox{Turn-On Delay Time} & t_{d(on)} & & & & & & & & & & & & & & & & & & &$	Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 4.5 V, $I_D$ = 5.0 A		1.1		nc	
$ \begin{array}{c c c c c c c c c c } \hline Turn-On Delay Time & t_{d(on)} & & & & & & & & & & & & & & & & & & &$	Gate-Drain Charge	Q <sub>gd</sub>			0.7			
$ \begin{array}{c c c c c c c c c } \hline Rise Time & t_r & t_r$	Gate Resistance	Rg	f = 1 MHz	0.5	2.4	4.8	Ω	
$\begin{array}{ c c c c } \hline \mbox{Hub} & \$	Turn-On Delay Time	t <sub>d(on)</sub>			8	16		
$\begin{array}{c c c c c c c } \hline Idin Grin Dolay Time & $	Rise Time	t <sub>r</sub>			17	26		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 4 \text{ A}, V_{\text{GEN}} = 4.5 \text{ V}, R_g = 1 \Omega$		31	47		
$\begin{tabular}{ c c c c c } \hline Turn-On Delay Time & $t_d(on)$ \\ \hline Rise Time & $t_r$ \\ \hline Turn-Off Delay Time & $t_d(off)$ \\ \hline Turn-Off Delay Time & $t_d(off)$ \\ \hline Turn-Off Delay Time & $t_f$ \\ \hline Turn-Off Delay Time & $t_s$ \\ \hline Turn-Off Delay Time & $t_r$ \\ \hline Turn-Off Delay Time & $t_r$ \\ \hline Turn-Off Delay Time & $t_r$ \\ \hline Turn-Off Delay Time & $t_s$	Fall Time	t <sub>f</sub>			8	16	ns	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Turn-On Delay Time	t <sub>d(on)</sub>			5	10		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Rise Time	t <sub>r</sub>			13	20		
$\begin{tabular}{ c c c c c } \hline Drain-Source Body Diode Characteristics & & & & & & & & & & & & & & & & & & &$	Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 4 A$ , $V_{GEN} = 5 V$ , $R_g = 1 \Omega$		21	32		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Fall Time	t <sub>f</sub>			6	12		
Pulse Diode Forward Current $I_{SM}$ 20Body Diode Voltage $V_{SD}$ $I_S = 4 \text{ A}, V_{GS} = 0 \text{ V}$ 0.751.2VBody Diode Reverse Recovery Time $t_{rr}$ 1220nsBody Diode Reverse Recovery Charge $Q_{rr}$ $I_F = 4 \text{ A}, dI/dt = 100 \text{ A}/\mus, T_J = 25 \text{ °C}$ 510nCReverse Recovery Fall Time $t_a$ $r_f$ $r_f$ $r_f$ $r_f$ $r_f$	Drain-Source Body Diode Characteristic	s						
Pulse Diode Forward CurrentI SMI SM20Body Diode VoltageV SDI SI S $0.75$ 1.2VBody Diode Reverse Recovery Time $t_{rr}$ 1220nsBody Diode Reverse Recovery Charge $Q_{rr}$ I F $12$ 20nsReverse Recovery Fall Time $t_a$ $T_F = 4 A, dI/dt = 100 A/\mu s, T_J = 25 °C$ $7$ $7$ ns	Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			1.75	Δ	
Body Diode Reverse Recovery Time $t_{rr}$ 1220nsBody Diode Reverse Recovery Charge $Q_{rr}$ Reverse Recovery Fall Time $t_a$	Pulse Diode Forward Current	I <sub>SM</sub>				20	~	
Body Diode Reverse Recovery Charge $Q_{rr}$ Reverse Recovery Fall Time $t_a$	Body Diode Voltage	$V_{SD}$	$I_{S} = 4 A, V_{GS} = 0 V$		0.75	1.2	V	
Reverse Recovery Fall Time $t_a$ $r_F = 4 A, di/dt = 100 A/\mu s, r_J = 25 C$ 7 ns	Body Diode Reverse Recovery Time				12	20	ns	
Reverse Recovery Fall Time t <sub>a</sub> 7	Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$l_{\rm r} = 4$ A dl/dt = 100 A/us T = 25 °C		5	10	nC	
Reverse Recovery Rise Time t <sub>b</sub>	Reverse Recovery Fall Time	t <sub>a</sub>	$ F = 4 A, u/u = 100 A/\mu s, T = 25 C$		7		ne	
	Reverse Recovery Rise Time	t <sub>b</sub>			5		115	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 % b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Bsemi

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- 55 °C

1.5

20

T<sub>C</sub> =

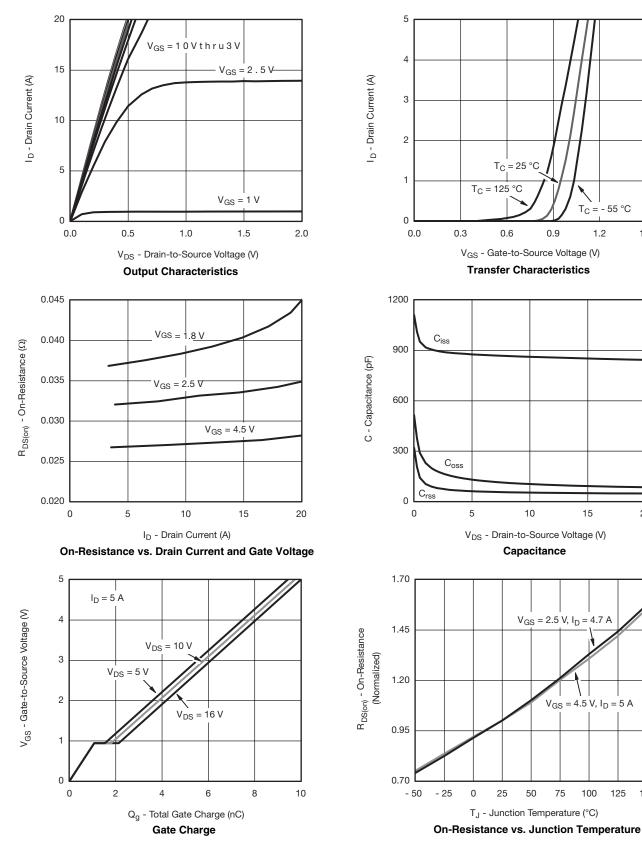
15

100

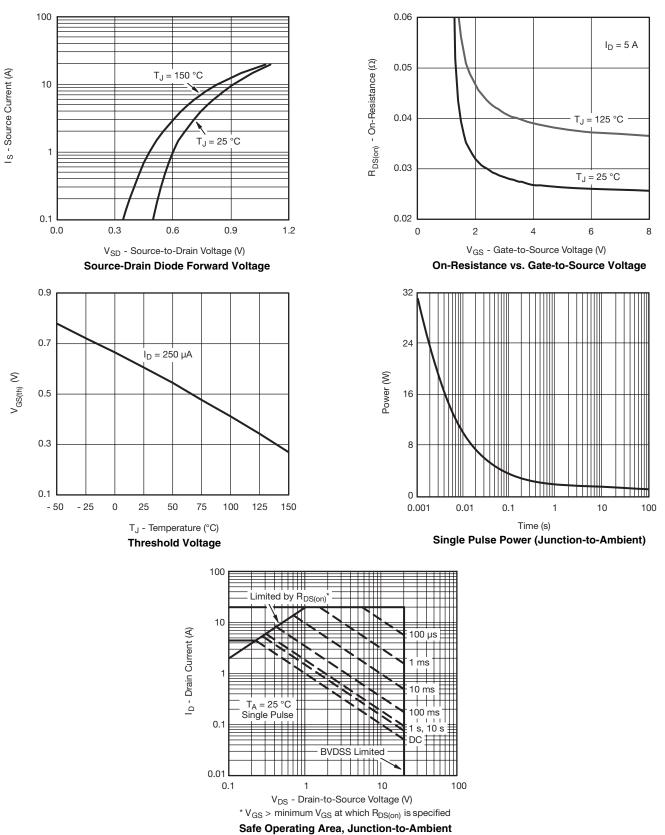
125 150

1.2

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



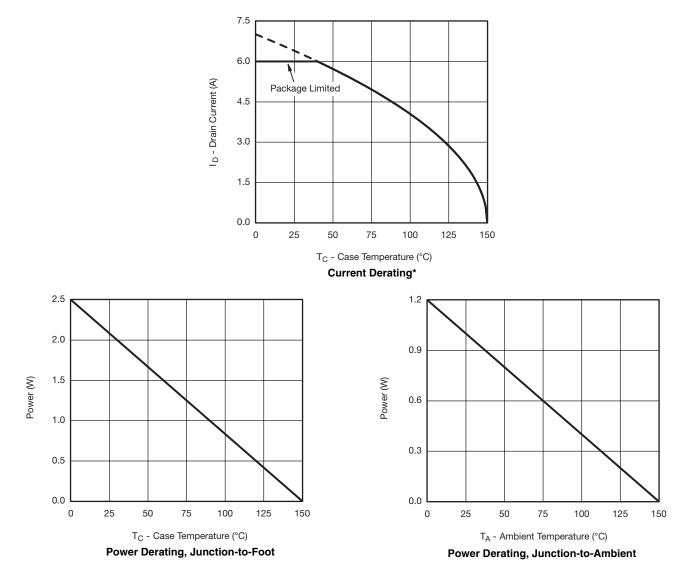




### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



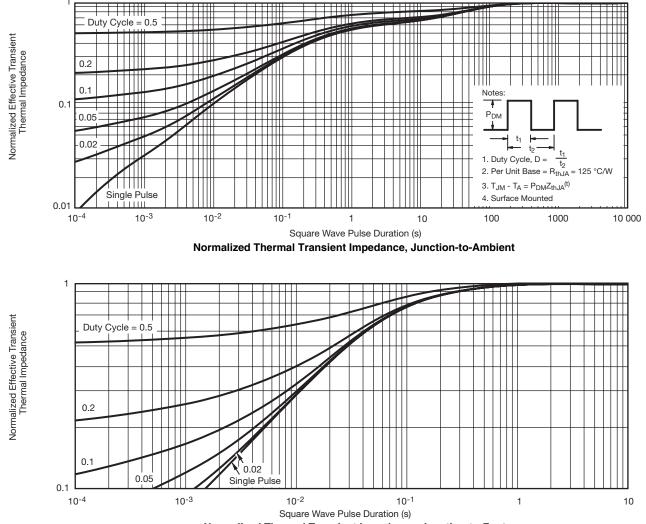
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max.)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



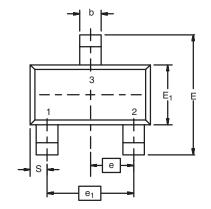
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

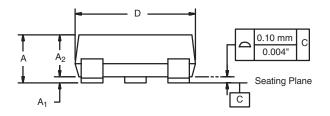


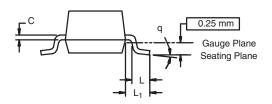
Normalized Thermal Transient Impedance, Junction-to-Foot



# SOT-23 (TO-236): 3-LEAD



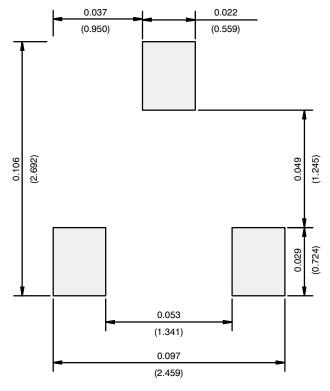




Dim	MILLIN	IETERS	INCHES			
	Min	Мах	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A <sub>1</sub>	0.01	0.10	0.0004	0.004		
A <sub>2</sub>	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
C	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E <sub>1</sub>	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e <sub>1</sub>	1.90 BSC		0.0748	0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L <sub>1</sub>	0.64 Ref		0.025	Ref		
S	0.50 Ref		0.020	Ref		
q	3°	8°	3°	8°		
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01					



## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



# Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

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Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.