

Discription

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The HESDNC24VB1AF-A protects sensitive semiconductor	
components from damage or upset due to electrostatic	2~
discharge (ESD) and other voltage induced transient events.	
Excellent clamping capability, low leakage, low capacitance,	HXY
and fast response time provide best in class	
protection on designs that are exposed to ESD.	
It gives designer the flexibility to protect one bi-directional	
line in applications where arrays are not practical.	DFN100

Features

- ★ Small Body Outline Dimensions: 1.00 mm x 0.60 mm
- ★ Low Body Height: 0.50 mm
- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 per Human Body Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ We declare that the material of product compliance with RoHS requirements and Halogen Free.

Ordering information



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Circuit Diagram

Product ID	Pack	Qty(PCS)
HESDNC24VB1AF-A	DFN1006-2L	10000

Absolute Ratings (T_{amb}=25°C)

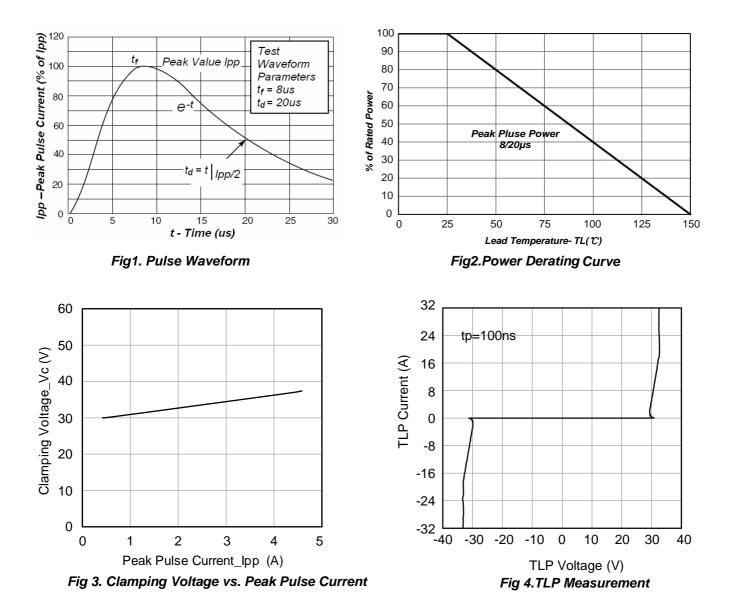
Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (t _p = 8/20µs)	300	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _{op}	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discha contact discha	±30 ±30	ΚV



Device	V _{RWM} (V)	I _R (μA) @V _{RWM}	V _{BR} (V) * @ I _T = 1mA		I _{PP} (A)**	V _C (V) ** @ I _{PP} =5A	Р _{РК} (W)**	C(pF) VR=0V, f=1MHz;
	Max	Max	Min	Max	Max	Max	Max	Тур
HESDNC24VB1AF-A	24	0.1	25	2995	5	60	300	15

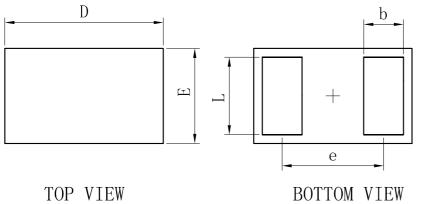
* V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C.

** Surge current waveform per Figure 1.



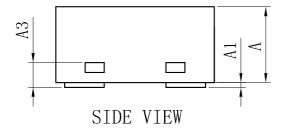


OUTLINE AND DIMENSIONS

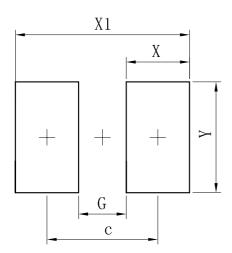


BOTTOM	VI

DFN1006-2L			
Dim	Min Typ		Max
D	0.95	1.00	1.05
Е	0.55	0.60	0.65
е	-	0.64	_
L	0.44	0.49	0.54
b	0.20	0.25	0.30
А	0.43	0.48	0.53
A1	0	-	0.05
A3	0.127REF.		
All Dimensions in mm			



SOLDERING FOOTPRINT



Dimensions	(mm)
С	0.70
G	0.30
Х	0.40
X1	1.10
Y	0.70



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