

## 600V HALF-BRIDGE GATE DRIVER

### Features

- High side fully operational to +600V
- Floating channel designed for bootstrap operation
- Output source/sink current capability 2.5A
- Gate drive supply range from 10V to 20V
- Common-mode dv/dt noise canceling circuit
- Under-voltage lockout for both channels
- 3.3V and 5V input logic compatible
- Logic and power ground  $\pm 5V$  offset
- CMOS Schmitt-triggered inputs with pull-down
- Cross-conduction prevention logic
- Matched propagation delay for both channels

### Applications

- Motor Drivers
- Full/Half Bridge Converters
- Two Switch forward Converter

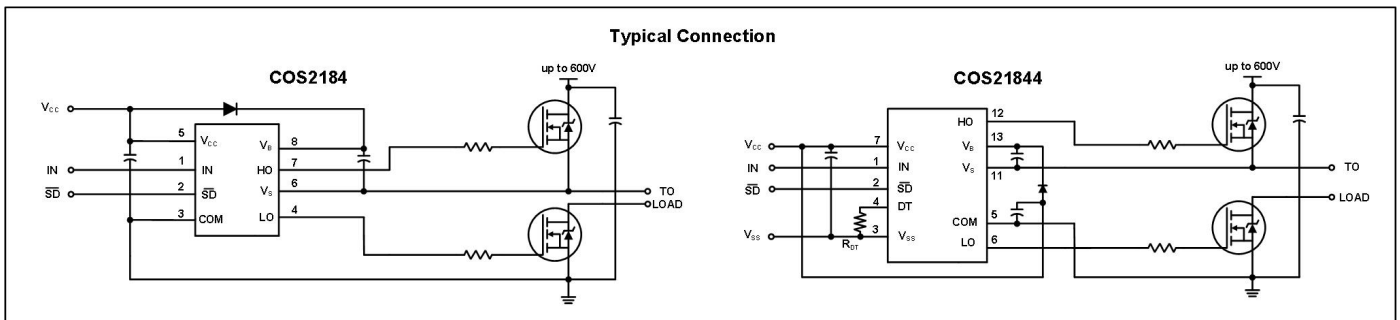
### General Description

The COS2184/COS21844 are high voltage, high speed half-bridge power MOSFET and IGBT drivers with dependent high and low side referenced output channels. The output drivers feature a high pulse current buffer stage designed for minimum driver cross conduction. Propagation delays are matched to simplify use in high frequency applications. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high-side configuration which operates up to 600V. The Logic inputs of COS2184/COS21844 are compatible with standard CMOS or TTL output, down to 3.3V logic. COS2184 is available in Green SOP8 and DIP8 Packages. COS21844 is available in Green SOP14 and DIP14 Packages.

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## 1. Pin Configuration and Functions

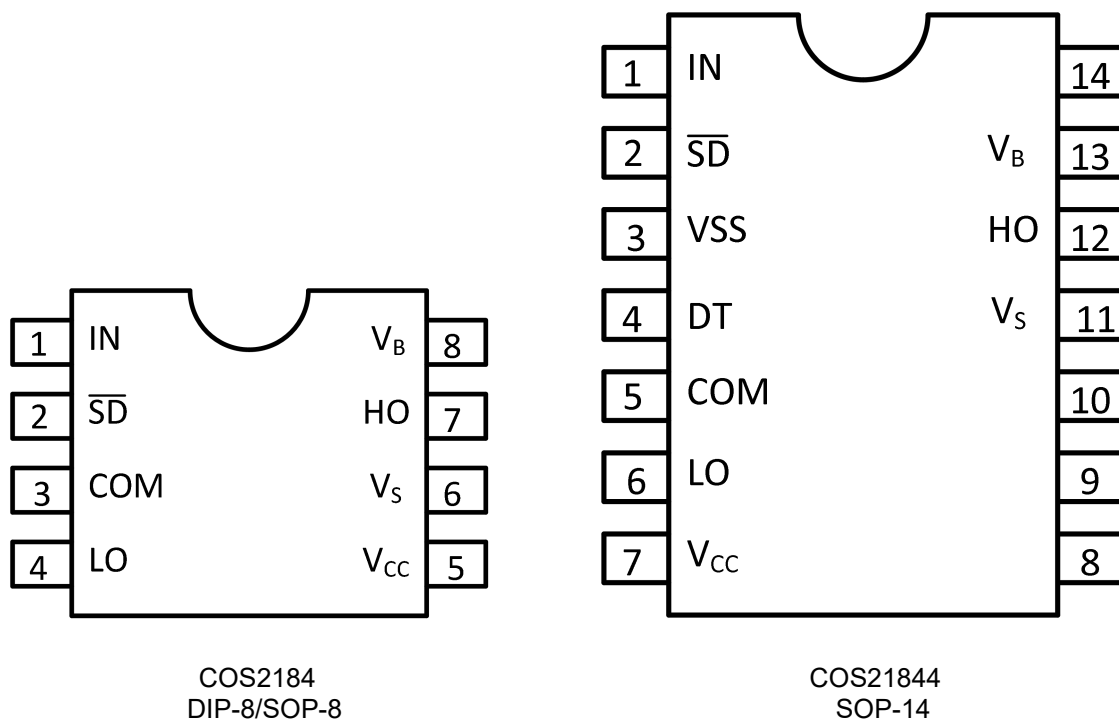


Figure 1. Pin Diagram

### Pin Description

Pin Name	Pin No. (COS2184)	Pin No. (COS21844)	Description
IN	1	1	Logic input for high and low side gate driver outputs (HO and LO), in phase with HO (referenced to COM)
$\overline{SD}$	2	2	Logic input for shutdown (referenced to COM for COS2184 and VSS for COS21844)
VSS	-	3	Logic ground (COS21844 only)
COM	3	5	Low-side return
LO	4	6	Low-side gate drive output
VCC	5	7	Low-side and logic fixed supply
VS	6	11	High-side floating supply return
HO	7	12	High-side gate drive output
VB	8	13	High-side floating supply
DT	-	4	Programmable deadtime lead, referenced to VSS (COS21844 only)

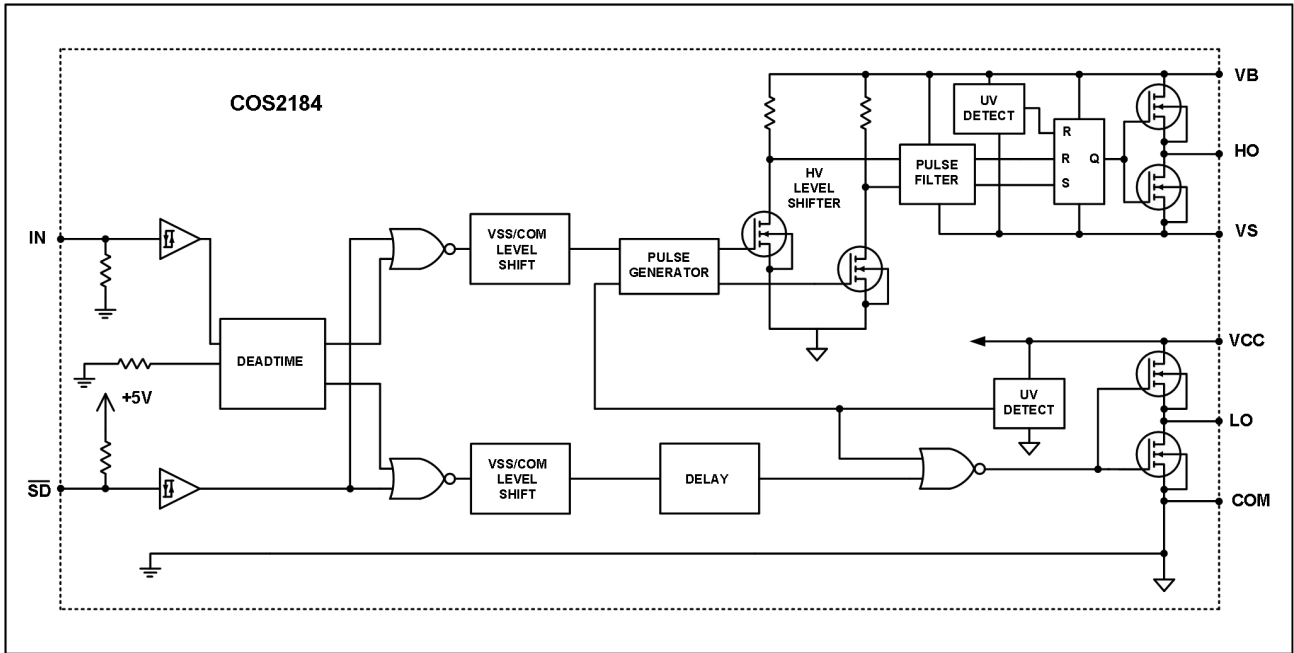


Figure 2. Functional Block Diagram of COS2184

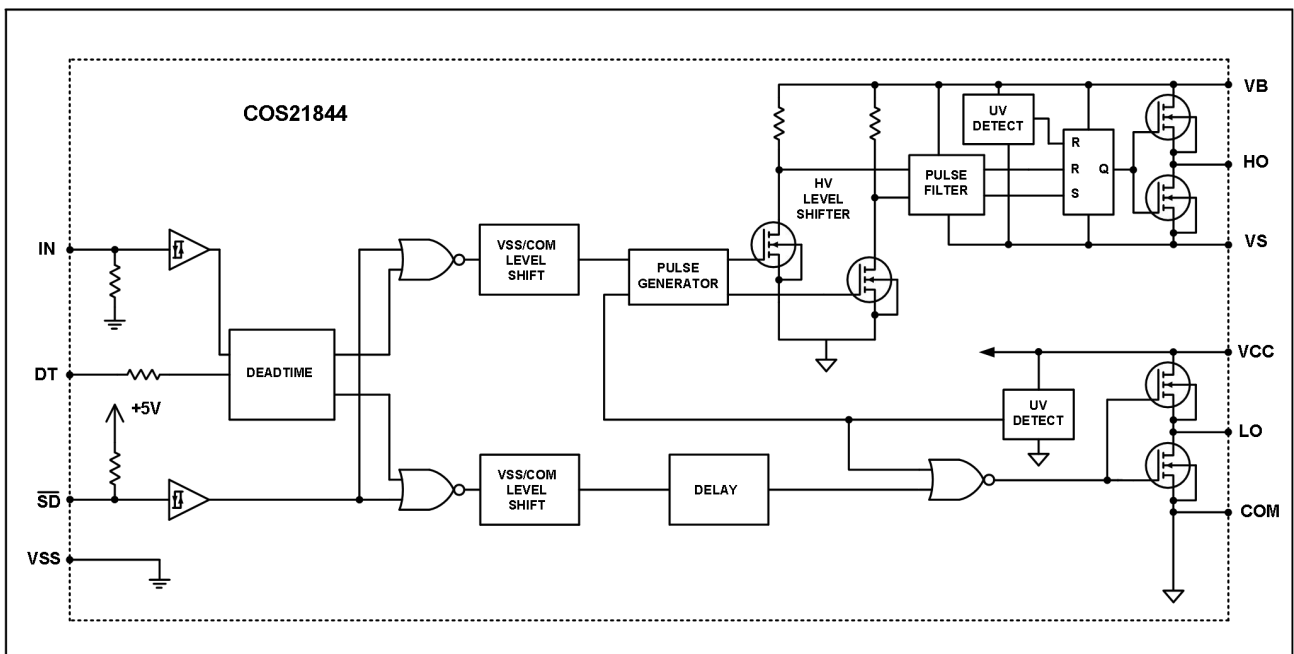


Figure 3. Functional Block Diagram of COS21844

## 2. Product Specification

### 2.1 Absolute Maximum Ratings <sup>(1)</sup>

Parameter	Symbol	Min	Max	Unit
High-side floating supply voltage	VB	-0.3	625	V
High-side floating supply offset voltage	VS	VB - 25	VB + 0.3	
High-side floating output voltage	VHO	VS - 0.3	VB + 0.3	
Low-side fixed supply voltage	VCC	-0.3	25	
Low-side output voltage	VLO	-0.3	VCC + 0.3	
Programmable deadtime pin voltage (COS21834 only)	DT	VSS-0.3	VCC + 0.3	
Logic input voltage (HIN, LIN)	VIN	VSS-0.3	VCC + 0.3	
Logic supply offset voltage (COS21834 only)	VSS	VCC-25	VSS + 0.3	
Allowable offset supply voltage transient	dVs/dt	-	50	V/ns
Junction temperature	TJ	-	150	°C
Storage temperature	TS	- 50	150	
Lead temperature (soldering, 10 seconds)	TL	-	300	

(1) Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

### 2.2 Thermal Data

Parameter	Rating	Unit
Package Thermal Resistance, R <sub>θJA</sub> (Junction-to-ambient)	155 (SOP8) 82 (SOP14) 125 (DIP8) 75 (DIP14)	°C/W

### 2.3 Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
High-side floating supply absolute voltage	VB	VS+10	VS+20	V
High-side floating supply offset voltage	VS	-5	600	
High-side floating output voltage	VHO	VS	VB	
Low-side and logic fixed supply voltage	VCC	10	20	
Low-side output voltage	VLO	0	VCC	

Logic input voltage (HIN, SD)	VIN	VSS	VCC	V
Logic supply offset voltage	VSS	-5	5	
Programmable deadtime pin voltage (COS21844 only)	DT	VSS	VCC	
Operation temperature	TA	-40	125	°C

## 2.4 Electrical Characteristics

VBIAS (VCC, VBS) = 15 V,  $T_A = 25\text{ }^\circ\text{C}$ ,  $C_L = 1000\text{pF}$  and VSS = COM, unless otherwise specified.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>INPUT</b>						
Input signal high threshold	$V_{IH}$		2.5	-		V
Input signal low threshold	$V_{IL}$			-	1.9	V
Logic "1" input bias current	$I_{IN+}$	$V_{IN} = 5\text{V}$		10	20	$\mu\text{A}$
Logic "0" input bias current	$I_{IN-}$	$V_{IN} = 0\text{V}$		-	2.0	$\mu\text{A}$
<b>OUTPUT</b>						
High level output voltage, $V_{BIAS} - V_O$	$V_{OH}$	$I_O = 20\text{mA}$		-	1.9	V
Low level output voltage	$V_{OL}$			-	0.15	V
Output high short circuit pulsed current	$I_{O+}$	$V_O = 0\text{V}$ , $V_{IN} = \text{VCC}$ $PW \leq 10\text{ }\mu\text{s}$	2.0	2.5	-	A
Output low short circuit pulsed current	$I_{O-}$	$V_O = 15\text{V}$ , $V_{IN} = 0$ $PW \leq 10\text{ }\mu\text{s}$	2.0	2.5	-	A
<b>POWER SUPPLY</b>						
Quiescent VBS supply current	$I_{QBS}$	$V_{IN} = 0\text{V}$ or VCC	-	38	100	$\mu\text{A}$
Quiescent VCC supply current	$I_{QCC}$	$V_{IN} = 0\text{V}$ or VCC, DT=VSS	-	2	-	mA
Offset supply leakage current	$I_{LK}$	$V_B = V_S = 600\text{V}$	-	-	50	$\mu\text{A}$
VCC and VBS supply under voltage positive going threshold	$V_{BSUV+}$ $V_{CCUV+}$		-	8.8	-	V
VCC and VBS supply under voltage negative going threshold	$V_{BSUV-}$ $V_{CCUV-}$		-	8.2	-	V
VCC and VBS supply under-voltage lockout hysteresis	$V_{BSHY}$ $V_{CCHY}$		-	0.6	-	V

SWITCHING CHARACTERISTICS						
Turn-on rise time	$t_R$	VS=0, See Figure 6	-	20	-	ns
Turn-off fall time	$t_F$		-	15	-	
Turn-on propagation delay	$t_{on}$		-	420	-	
Turn-off propagation delay	$t_{off}$		-	130	-	
Shutdown propagation delay	$t_{sd}$	See Figure 7	-	140	-	
Delay matching, HS & LS turn-on	$MT_{on}$	See Figure 8	-	0	90	
Delay matching, HS & LS turn-off	$MT_{off}$		-	0	40	
Deadtime (see Figure 9)	DT	RDT=0 (COS2184)	100	300	500	ns
		RDT=200k $\Omega$ (COS21844)	5	6	7	$\mu$ s
Deadtime matching	MDT	RDT=0 (COS2184)	-	0	50	ns
		RDT=200k $\Omega$ (COS21844)	-	0	600	

### 3 Application Information

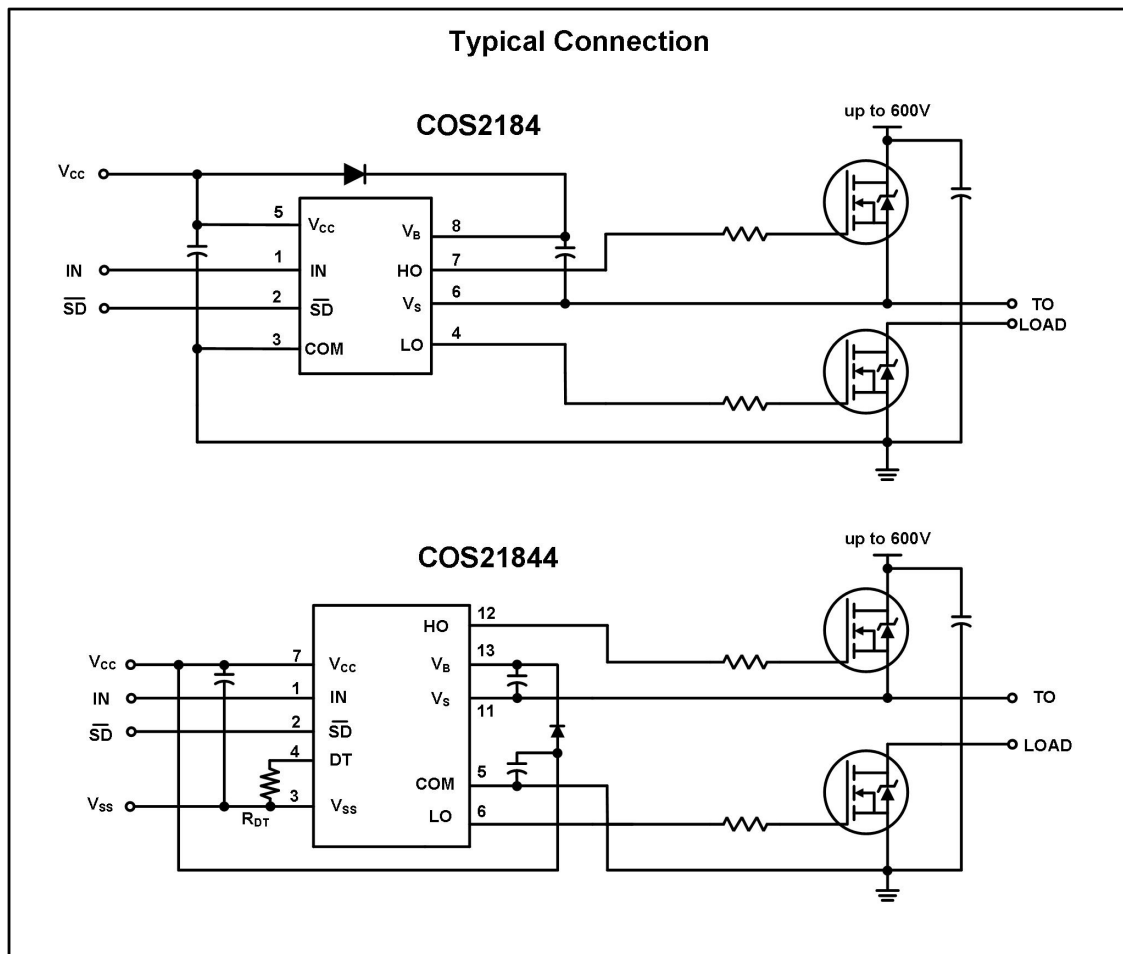


Figure 4. Typical Connection

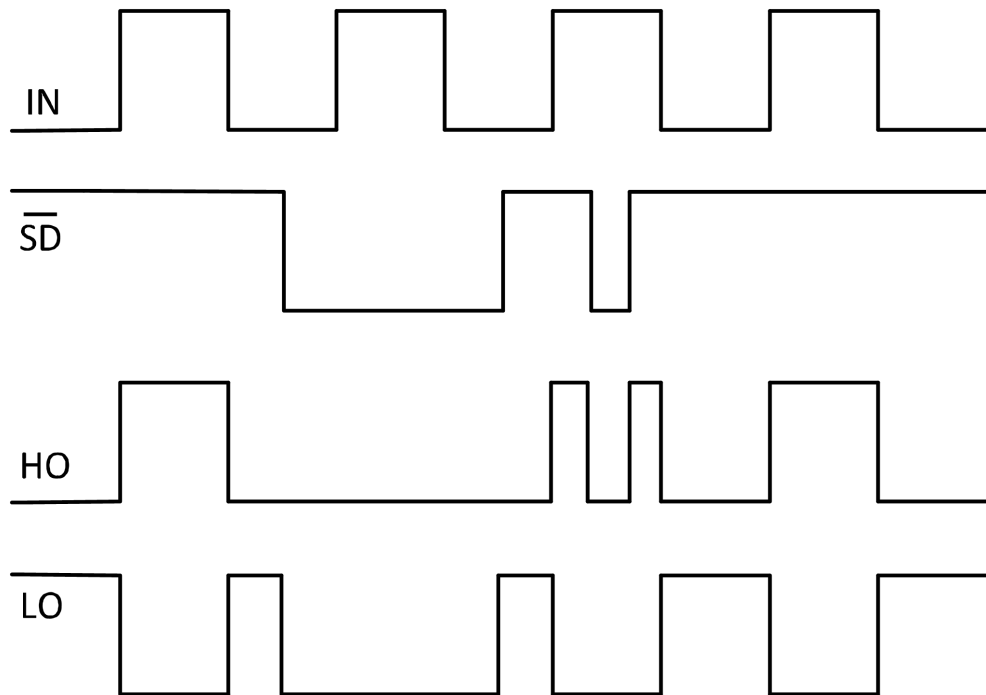


Figure 5. Input/Output Timing Diagram

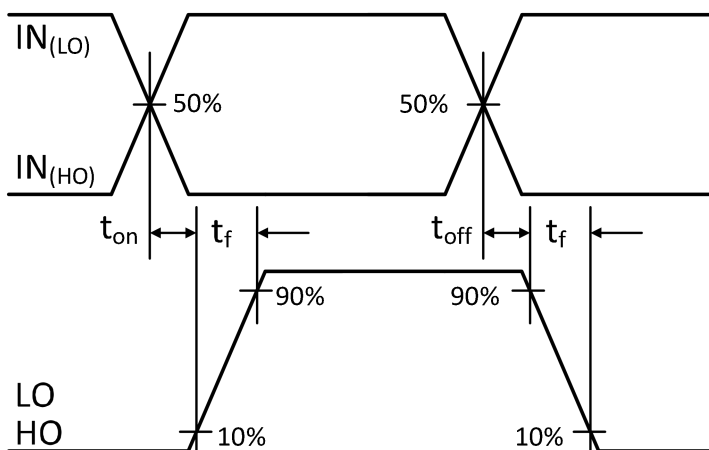


Figure 6. Switching Time Waveform Definitions

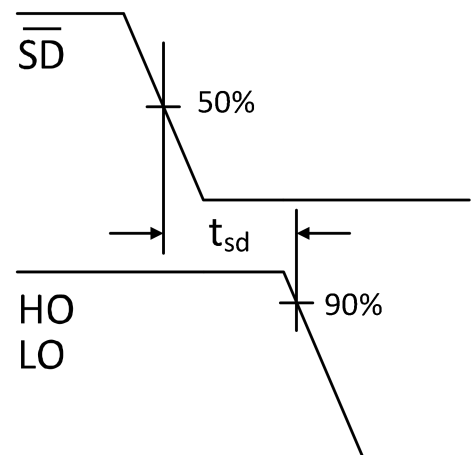


Figure 7. Shutdown Waveform Definition

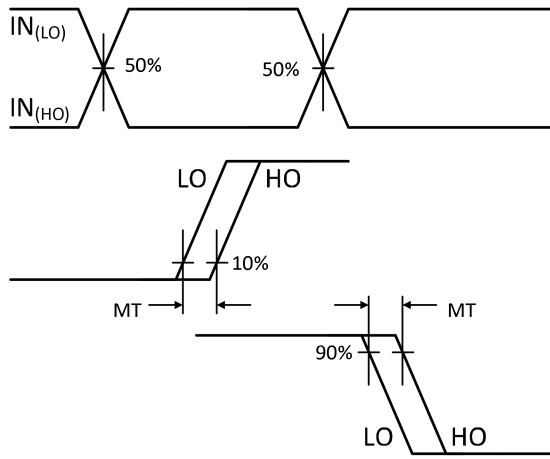


Figure 8. Delay Matching Waveform Definitions

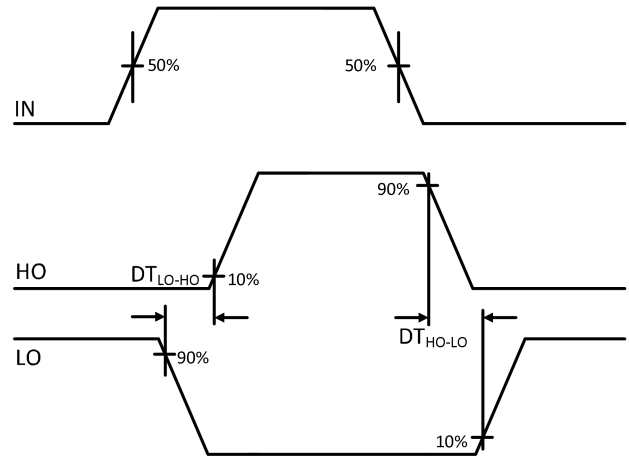


Figure 9. Deadtime Waveform Definitions

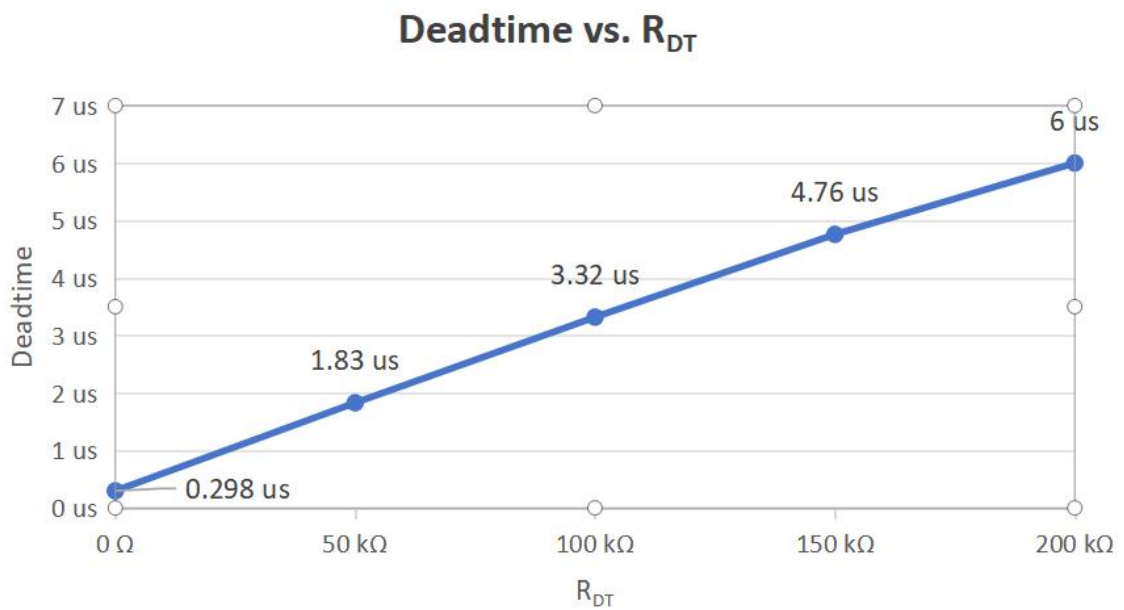
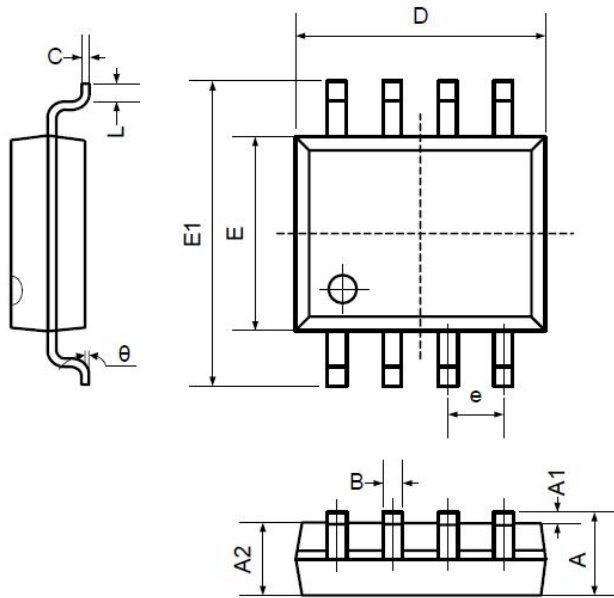


Figure 10. Deadtime vs.  $R_{DT}$



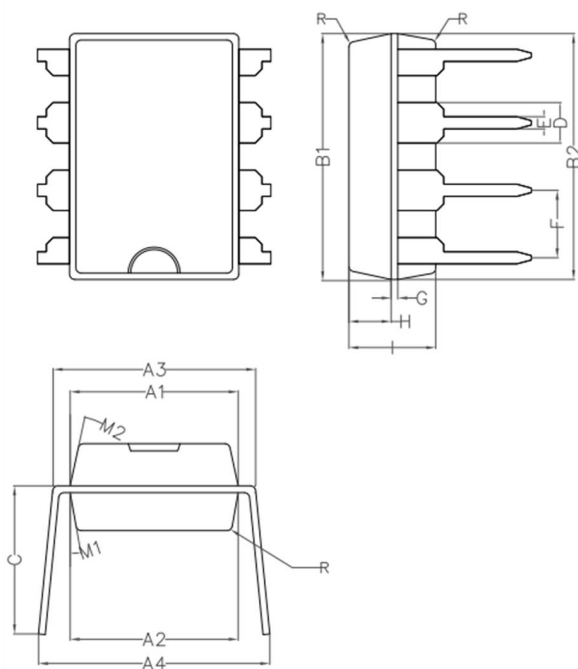
## 4. Package Information

### 4.1 SOP8 (Package Outline Dimensions)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
B	0.330	0.510	0.013	0.020
C	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
e	1.270TYP		0.050TYP	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°

### 4.2 DIP8 (Package Outline Dimensions)



Symbol	Min	Non	Max
A1	6.28	6.33	6.38
A2	6.33	6.38	6.43
A3	7.52	7.62	7.72
A4	7.80	8.40	9.00
B1	9.15	9.20	9.25
B2	9.20	9.25	9.30
C		5.57	
D		1.52	
E	0.43	0.45	0.47
F		2.54	
G		0.25	
H	1.54	1.59	1.64
I	3.22	3.27	3.32
R		0.20	
M1	9°	10°	11°
M2	11°	12°	13°



## 5. Package and Ordering Information

<b>Model</b>	<b>Order Number</b>	<b>Package</b>	<b>Package Option</b>	<b>Marking Information</b>
COS2184	COS2184S	SOP-8	Tape and Reel, 4000	COS2184S
	COS2184D	DIP-8	Tube 50	COS2184
COS21844	COS21844S	SOP-14	Tape and Reel, 3000	COS21844S
	COS21844D	DIP-14	Tube 50	COS21844