

Description

The HXY4402S uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

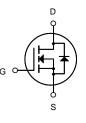
 $V_{DS} = 20V$ $I_D = 20 \text{ A}$ $R_{DS(ON)} < 5.5 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$

Application

Battery protection Load switch Uninterruptible power supply







N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY4402S	SOP-8	4402 XXX YYYY	3000

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Parameter Rating			
Vds	Drain-Source Voltage	n-Source Voltage 20			
Vgs	Gate-Source Voltage	Gate-Source Voltage ± 12			
	Drain Current – Continuous (T $_{c}$ =25 $^{\circ}$ C)	20	А		
lo	Drain Current – Continuous (Tc=70 \degree C)	16	А		
Ідм	Drain Current – Pulsed ¹	140	А		
EAS	Single Pulse Avalanche Energy ²	162	mJ		
IAS	Single Pulse Avalanche Current ²	57	А		
PD	Power Dissipation (T _c =25 $^{\circ}$ C)	3.1	W		
Тѕтс	Storage Temperature Range	-55 to 150	°C		
TJ	Operating Junction Temperature Range	Operating Junction Temperature Range -55 to 150			
R _θ ja	Thermal Resistance Junction to ambient 40		°C/W		



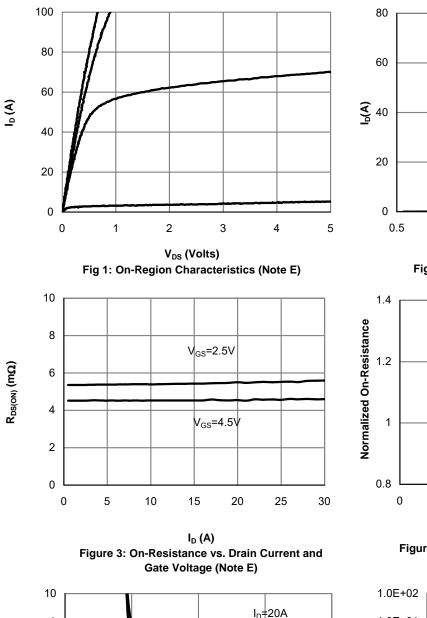
Electrical Characteristics Ta = 25° C

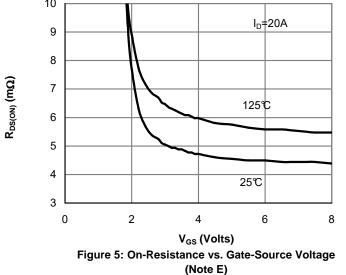
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Drain-Source Breakdown Voltage	Vdss	ID=250 uA, VGs=0V	20			V	
Zero Gate Voltage Drain Current	Idss	VDS=20V, VGS=0V			1	uA	
Zero Gale Voltage Drain Current		VDs=20V, VGs=0V, TJ=55°C			5	uA	
Gate-Body Leakage Current	Igss	VDS=0V, VGS=±12V			±100	nA	
Gate Threshold Voltage	VGS(th)	Vbs=Vgs , Ib=250uA	0.5		1.6	V	
		Vgs=4.5V, ID=20A			5.5	mΩ	
Static Drain-Source On-Resistance	RDS(ON)	Vgs=4.5V, ID=20A TJ=125℃			7		
		Vgs=2.5V, ID=18A			7		
On State Drain Current	ID(ON)	Vgs=10V, Vds=5V	140			А	
Forward Transconductance	gfs	VDS=5V, ID=20A		105		S	
Input Capacitance	Ciss		3080		4630	pF	
Output Capacitance	Coss	Vgs=0V, Vds=10V, f=1MHz	520		960		
Reverse Transfer Capacitance	Crss	1	350		810		
Gate Resistance	Rg	VGS=0V, VDS=0V, f=1MHz	0.6		2.1	Ω	
Total Gate Charge	Qg		28		43	nC	
Gate Source Charge	Qgs	VGS=10V, VDS=10V, ID=20A	7		11		
Gate Drain Charge	Qgd		7		17		
Turn-On DelayTime	td(on)			7			
Turn-On Rise Time	tr	Vgs=10V, Vds=10V, RL=0.5Ω,		8		ns	
Turn-Off DelayTime	td(off)	Rgen=3Ω		70			
Turn-Off Fall Time	tr			18			
Body Diode Reverse Recovery Time	trr		13		20		
Body Diode Reverse Recovery Charge	Qrr	IF= 20A, dı/dt= 500A/us	29		43	nC	
Maximum Body-Diode Continuous Current	ls				4	А	
Diode Forward Voltage	Vsd	Is=1A,Vgs=0V			1	V	

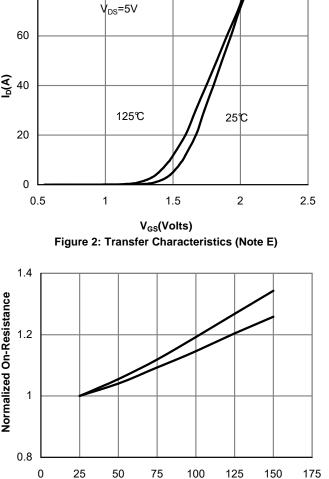
Note : The static characteristics in Figures 1 to 6 are obtained using <300 µs pulses, duty cycle 0.5% max.



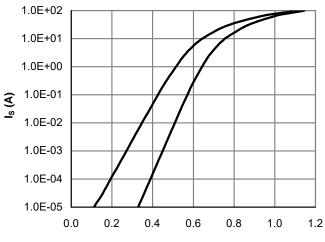
Typical Characterisitics





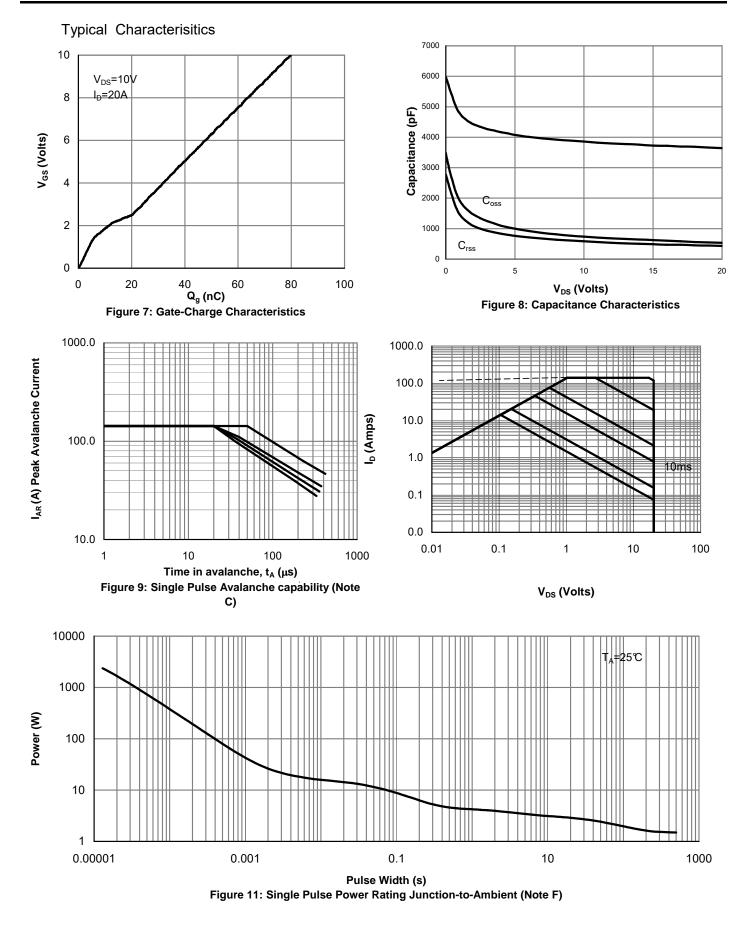


Temperature (℃) Figure 4: On-Resistance vs. Junction Temperature (Note E)

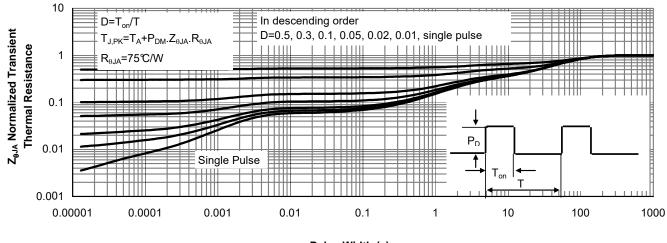


V_{SD} (Volts) Figure 6: Body-Diode Characteristics (Note E)







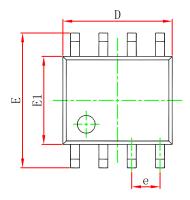


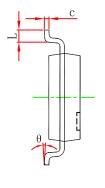
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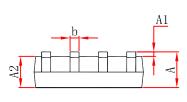




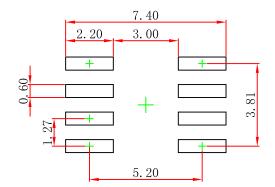
SOP-8 Package Outline Dimensions







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.007	0.010	
D	4.800	5.000	0.189	0.197	
e	1.270 (BSC)		0.050 (BSC)		
E	5.800	6.200	0.228	0.244	
E1	3.800	4.000	0.150	0.157	
L	0.400	1.270	0.016	0.050	
θ	0 °	8°	0 °	8°	



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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