

4805 (3/13)

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Γ	DIMENSIONS:	
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	$\psi =$	
┝	MATERIAL	
	SEE BOM	
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		500	1000	) + 1 0 / - C	23432	88-2
		250	500	+ 1 0 / - 0	23432	88-1
		(" <b>M</b> ")		"L"	TE I	>/N
A CONTROLLED DO	DCUMENT.	DWN 10DEC2018 T.SMITH снк 10DEC2018 GLEN GUTGOLD		<b>E</b> TE	TE Connecti	vity
0 PLC ±- 1 PLC ±- 2 PLC ±- 2 PLC ±-	UNLESS PECIFIED:	APVD 10DEC2018 GLEN GUTGOLD PRODUCT SPEC -		2.0 4C 33 _ TO LATER		
3 PLC ±- 4 PLC ±- ANGLES FINISH	±-	APPLICATION SPEC - WEIGHT _	size cage code	drawing no C=234328	\$8	RESTRICTED TO
-		CUSTOMER DRAWING	<u>, , , </u>	SCALE	3:2 SHEET 1	of 3 Rev B

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		REVISIONS			
Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	В	REVISED PER ECN-24-247533	16JAN2024	ΜY	DZ

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			DESIGNATION	WIRE TYPE			
ſ	- A 1	PAIR NO.	GROUND	30AWG DISCRETE	<b>DESIGNATION</b> GROUND	PAIR NO. PIN NO.   A 1 -	
	— <u>A 2</u>		GROUND	30AWG DISCRETE	GROUND	A 2	
COMMONED —	— <u>A 3</u> — <u>A 4</u>		G R O U N D G R O U N D	30AWG DISCRETE 30AWG DISCRETE	GROUND GROUND	A 3 - A 4 -	COMMONED
	— <u>A 5</u> — A 6		GROUND GROUND	30AWG DISCRETE 30AWG DISCRETE	GROUND GROUND	A 5	
	A 7		SIDE BAND	30AWG DISCRETE	SIDE BAND	A 0	
	A 8 A 9		SIDE BAND	30AWG DISCRETE 30AWG DISCRETE	SIDE BAND SIDE BAND	A 8 A 9	
	A 1 0		SIDE BAND	30AWG DISCRETE	SIDE BAND	A 1 0	
	A 1 1 A 1 2		SIDE BAND SIDE BAND	30AWG DISCRETE 30AWG DISCRETE	SIDE BAND SIDE BAND	A 1 1 A 1 2	
	A 1 3		GND		GND	A 1 3	
	A 1 4 A 1 5		CLK CLK	33AWG DIFF PAIR 33AWG DIFF PAIR	C L K C L K	A 1 4 A 1 5	
	A 1 6		GND		GND	A 1 6	
	A 1 7 A 1 8		PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	1 A 1 7 1 A 1 8	
	A 1 9		GND		GND	A 1 9	
	A 2 0 A 2 1	2	PCIe TX PCIe TX	33AWGDIFFPAIR33AWGDIFFPAIR	PCIe TX PCIe TX	2 A 2 0 2 A 2 1	
	A 2 2		GND		GND	A 2 2	
	A 2 3 A 2 4	3	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	3 A 2 3 3 A 2 4	
	A 2 5		GND		GND	A 2 5	
	A 2 6 A 2 7	4	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	4 A 2 6 4 A 2 7	
	A 2 8		GND		GND	A 2 8	
	A 2 9		K E Y G N D		K E Y G N D	A 2 9	
	A 3 0	5	PCIe TX	33AWG DIFF PAIR	PCIe TX PCIe TX	5 A 3 0	
	A 3 1 A 3 2	<u> </u>	PCIe TX GND	33AWG DIFF PAIR	PCIe TX GND	5 A 3 1 A 3 2	
	A 3 3	6	PCIe TX	33AWG DIFF PAIR	PCIe TX PCIe TX	6 A 3 3	
	A 3 4 A 3 5	0	PCIe TX GND	33AWG DIFF PAIR	PCIe TX GND	6 A 3 4 A 3 5	
	A 3 6 A 3 7	7 7	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	7 A 3 6 7 A 3 7	
	A 3 7 A 3 8	1	GND	JJAWU DIFF MAIK	G N D	A 3 7 A 3 8	
	A 3 9 A 4 0	8	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	8 A 3 9 8 A 4 0	
	A 4 1		GND		GND	A 4 1	
	A 4 2		SIDE BAND KEY	30AWG DISCRETE	SIDE BAND KEY	A 4 2	
	A 4 3		GND		GND	A 4 3	
	A 4 4 A 4 5	9	PCIe TX PCIe TX	33AWGDIFFPAIR33AWGDIFFPAIR	PCIe TX PCIe TX	9 A 4 4 9 A 4 5	
	A 4 6		GND		GND	A 4 6	
	A 4 7 A 4 8	10	PCIe TX PCIe TX	33AWGDIFFPAIR33AWGDIFFPAIR	PCIe TX PCIe TX	10 A 4 7 10 A 4 8	
	A 4 9		GND		GND	A 4 9	
	A 5 0 A 5 1	1 1	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	1 1 A 5 0 1 1 A 5 1	
	A 5 2		GND		GND	A 5 2	
	A 5 3 A 5 4	12	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	12 A 5 3 12 A 5 4	
	A 5 5		GND		GND	A 5 5	
	A 5 6 A 5 7	1 3 1 3	PCIe TX PCIe TX	33AWGDIFFPAIR33AWGDIFFPAIR		13 A56 13 A57	
	A 5 8		GND		GND	A 5 8	
	A 5 9 A 6 0	14	PCIe TX PCIe TX	33AWG DIFF PAIR 33AWG DIFF PAIR	PCIe TX PCIe TX	14 A59 14 A60	
	A 6 1		GND		GND	A 6 1	
	A 6 2 A 6 3	15	PCIe TX PCIe TX	33AWGDIFFPAIR33AWGDIFFPAIR		15 A62 15 A63	
	A 6 4		GND		GND	A 6 4	THIS DRAWING IS A CONTROLLED DOCUMENT. DWN 1 T.SMITH
	A 6 5 A 6 6	16	PCIe TX PCIe TX	33AWGDIFFPAIR33AWGDIFFPAIR	PCIe TX PCIe TX	16 A65 16 A66	DIMENSIONS: TOLERANCES UNLESS ADVID
	A 6 7		GND		GND	A 6 7	mm  0  PLC  ±-    0  PLC  ±-    1  PLC  ±-    2  PLC  ±-    3  PLC  ±-
	A 6 8 A 6 9		RESERVED RESERVED	30AWG DISCRETE 30AWG DISCRETE	RESERVED RESERVED	A 6 8 A 6 9	A PLC ±- ANGLES ±- MATERIAL FINISH WEIGHT
	A 7 0		RESERVED	30AWG DISCRETE	RESERVED	A 7 0	SEE BOM -

		REVISIONS			
Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

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10DEC2018 S DRAWING IS A CONTROLLED DOCUMENT. T.SMITH TE Connectivity 10DEC2018 GLEN GUTGOLD APVD 10DEC2018 GLEN GUTGOLD PRODUCT SPEC TOLERANCES UNLESS OTHERWISE SPECIFIED: M<sup>e</sup>SLIVER 2.0 4C 33 AWG LATERAL TO LATERAL 0 PLC 1 PLC 2 PLC 3 PLC 4 PLC ±-±-±-±--APPLICATION SPEC size cage code drawing no A 1 0 0 7 7 9 C = 2 3 4 3 2 8 8 RESTRICTED TO ±-WEIGHT FINISH --SCALE 1:2 SHEET 2 OF 3 REV B CUSTOMER DRAWING -

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				P1	WIRE TYPE		P2	
		PIN NO.	PAIR NO.	DESIGNATION		DESIGNATION	PAIR NO. PIN NO.	
		- B1 - B2		12V POWER 12V POWER	30AWG DISCRETE 30AWG DISCRETE	12V POWER 12V POWER	B 1 B 2	
		- B3		12V POWER	30AWG DISCRETE	12V POWER	B 3	
	COMMONED —	- B 4		12V POWER	30AWG DISCRETE	12V POWER	B 4	
	_	- B 5		12V POWER	30AWG DISCRETE	12V POWER	В 5	
		- B6		12V POWER	30AWG DISCRETE	12V POWER	B 6	
		B 7		SIDE BAND	30AWG DISCRETE	SIDE BAND	B 7	
		B8		SIDE BAND	30 AWG DISCRETE	SIDE BAND	B8	
		B 9 B 1 0		SIDE BAND SIDE BAND	30AWG DISCRETE 30AWG DISCRETE	SIDE BAND SIDE BAND	B 9 B 1 0	
		B11		3.3 AUX POWER	30AWG DISCRETE	3.3 AUX POWER		
		B12		SIDE BAND	30AWG DISCRETE	SIDE BAND	B12	
		B13		GND		GND	B13	
		B14		CLK	33AWG DIFF PAIR	CLK	B14	
		B 1 5		CLK	33AWG DIFF PAIR	CLK	B 1 5	
		B16	4 7	GND		GND	B16	
		B17	17	PCIe RX	33AWG DIFF PAIR		17 B17	
		B18 B19	1 7	PCIeRX GND	33AWG DIFF PAIR	PCIeRX GND	17 B18 B19	
		B20	1 8	PCIe RX	33AWG DIFF PAIR		18 B20	
		B 2 1	18	PCIe RX	33AWG DIFF PAIR		18 B21	
		B 2 2		GND		GND	B22	
		B 2 3	19	PCIe RX	33AWG DIFF PAIR		19 B23	
		B 2 4	19	PCIe RX	33AWG DIFF PAIR		19 B24	
		B25		G N D		GND DCL: DV	B25	
		B26 B27	20	PCIe RX PCIe RX	33AWG DIFF PAIR 33AWG DIFF PAIR		20 B26 20 B27	
		B28		GND	JJAWO DIFF FAIN	GND	B28	
				KEY		KEY		
		B 2 9		GND		GND	B29	
		B30	2 1	PCIe RX	33AWG DIFF PAIR		21 B30	
		B 3 1	21	PCIe RX	33AWG DIFF PAIR		21 B31	
		B 3 2		GND		GND	B32	
		B33 B34	22	PCIe RX PCIe RX	33AWG DIFF PAIR 33AWG DIFF PAIR		22 B33 22 B34	
		B 3 5		GND	JJAWO DINI TAIN	GND	B35	
		B 3 6	23	PCIe RX	33AWG DIFF PAIR		23 B36	
		B 3 7	23	PCIe RX	33AWG DIFF PAIR	PCIe RX	23 B37	
		B 3 8		GND		GND	B38	
		B 3 9	24	PCIe RX	33AWG DIFF PAIR		24 B39	
		B 4 0 B 4 1	2 4	PCIe RX	33AWG DIFF PAIR		24 B40	
		B 4 1		GND PRSNT DETECT	30AWG DISCRETE	GND PRSNT DETECT	B 4 1 B 4 2	
				KEY		KEY		
		B 4 3		GND		GND	B 4 3	
		B 4 4	25	PCIe RX	33AWG DIFF PAIR		25 B44	
		B 4 5	2 5	PCIe RX	33AWG DIFF PAIR		25 B45	
		B 4 6 B 4 7	2 6	GND PCIeRX	33AWG DIFF PAIR	GND PCIeRX	B46        26      B47	
		B 4 8	26	PCIe RX	33AWG DIFF PAIR		26 B48	
		B 4 9	_ ~	GND		GND	B 4 9	
		B 5 0	27	PCIe RX	33AWG DIFF PAIR	PCIe RX	27 B50	
		B 5 1	27	PCIe RX	33AWG DIFF PAIR	PCIe RX	27 B51	
		B 5 2		GND		GND	B 5 2	
		B53	28	PCIe RX	33AWG DIFF PAIR	PCIe RX	28 B53	
		B54 B55	28	PCIeRX GND	33AWG DIFF PAIR	PCIeRX GND	28 B54 B55	
		B 5 6	29	PCIe RX	33AWG DIFF PAIR		29 B56	
		B 5 7	29	PCIe RX	33AWG DIFF PAIR		29 B57	
		B 5 8		GND		GND	B 5 8	
		B 5 9	30	PCIe RX	33AWG DIFF PAIR		30 B59	
		B60	30	PCIe RX	33AWG DIFF PAIR		30 B60	
		B 6 1		G N D		GND DCL DV	B 6 1	
		B62 B63	31	PCIe RX PCIe RX	33AWG DIFF PAIR 33AWG DIFF PAIR		31 B62 31 B63	
		B63 B64		GND	JJAWU VIEF FAIK	GND	B64	
		B65	32	PCIe RX	33AWG DIFF PAIR		32 B65	
		B66	32	PCIe RX	33AWG DIFF PAIR		32 B66	
		B 6 7		GND		GND	B 6 7	
		B68		RESERVED	30AWG DISCRETE	RESERVED	B68	
		B69		RESERVED	30AWG DISCRETE	RESERVED	B 6 9	
		B70		PRSNT DETECT	30AWG DISCRETE	PRSNT DETECT	B70	

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DIMENSIONS: mm MATERIAL SEE BOM -

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		REVISIONS			
Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

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10DEC2018 THIS DRAWING IS A CONTROLLED DOCUMENT. DWN T.SMITH TE Connectivity 10DEC2018 CHK TOUDEC2018 TOLERANCES UNLESS OTHERWISE SPECIFIED: APVD TODEC2018 APVD TODEC2018 APVD TODEC2018 APVD TODEC2018 APVD TODEC2018 APVD SPEC PRODUCT SPEC M<sup>e</sup>SLIVER 2.0 4C 33 AWG LATERAL TO LATERAL 0 PLC 1 PLC 2 PLC 3 PLC 4 PLC ±-±-±--APPLICATION SPEC SIZE CAGE CODE DRAWING NO A 1 0 0 7 7 9  $\bigcirc$  2 3 4 3 2 8 8 RESTRICTED TO ±-NGLE WEIGHT \_ FINISH -SCALE 3:2 SHEET 3 OF 3 REV B CUSTOMER DRAWING -