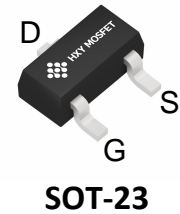




Description

The HI5121 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.



General Features

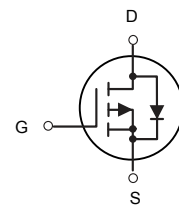
$V_{DS} = -20V, I_D = -3A$

$R_{DS(ON)} < 130m\Omega @ V_{GS} = -4.5V$

$R_{DS(ON)} < 148m\Omega @ V_{GS} = -2.5V$

Application

PWM applications
Load switch



P-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HI5121	SOT-23	A19T	3000

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Drain Current-Continuous	-3	A
I_{DM}	Drain Current-Pulsed (Note 1)	-10	A
P_D	Maximum Power Dissipation	0.7	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 2)	178	°C/W



Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-15V, V_{GS}=0V, T_C=25^{\circ}C$			-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}= \pm 10V, V_{DS}=0V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}= V_{GS}, I_D=-250\mu A$	-0.4	-0.62	-1.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}= -4.5V, I_D=-2A$		100	130	m Ω
		$V_{GS}= -2.5V, I_D=-1.6A$		132	148	
Diode Forward Voltage	V_{SD}	$I_S=-2A, V_{GS}=0V$		-0.8	-1.2	V
Maximum Body-Diode Continuous Current	I_S				-2.3	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=-10V, V_{GS}=0V, f=1MHZ$		260		pF
Output Capacitance	C_{oss}			44		
Reverse Transfer Capacitance	C_{rss}			29		
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=-4.5V, V_{DS}=-10V, I_D=-2A$		3.9		nC
Gate Source Charge	Q_{gs}			0.7		
Gate Drain Charge	Q_{gd}			0.9		
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=-4.5V, V_{DD}=-10V, I_D=-1A, R_{GEN}=2.5\Omega$		12		ns
Turn-on Rise Time	t_r			54		
Turn-off Delay Time	$t_{D(off)}$			15		
Turn-off Fall Time	t_f			9		

A. Pulse Test: Pulse Width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.



Typical Performance Characteristics

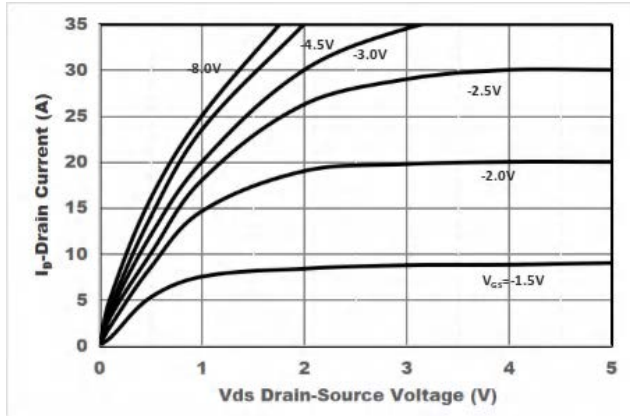


Figure1. Output Characteristics

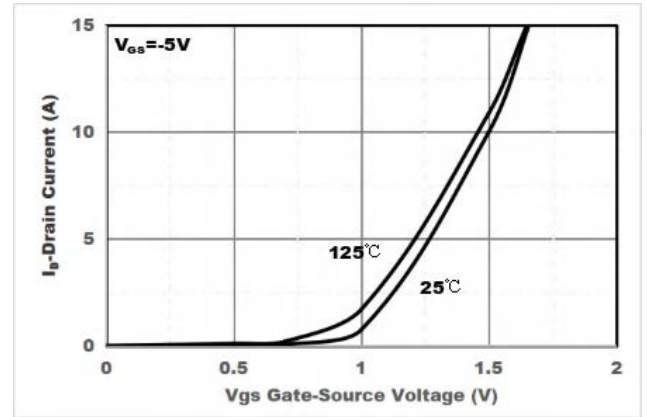


Figure2. Transfer Characteristics

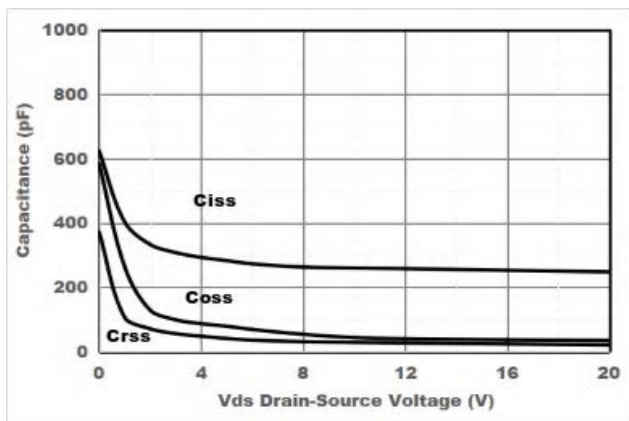


Figure3. Capacitance Characteristics

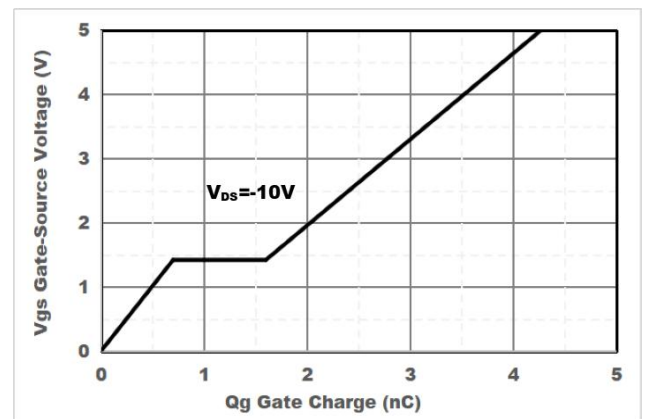


Figure4. Gate Charge

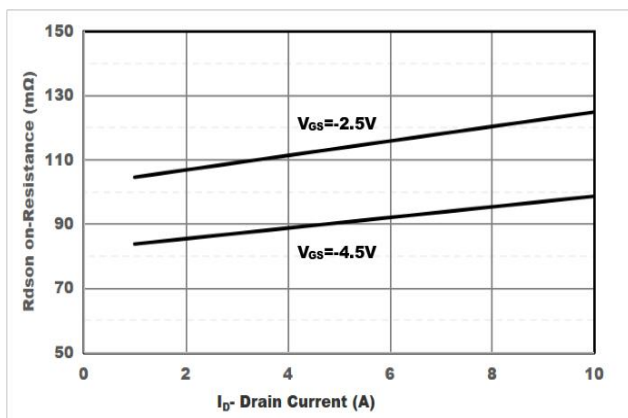


Figure5. Drain-Source on Resistance

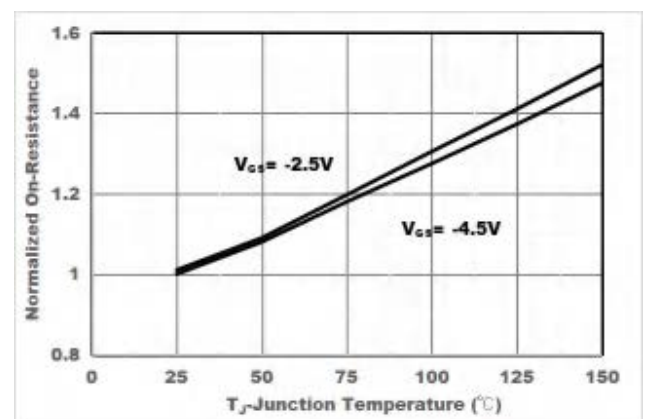


Figure6. Drain-Source on Resistance

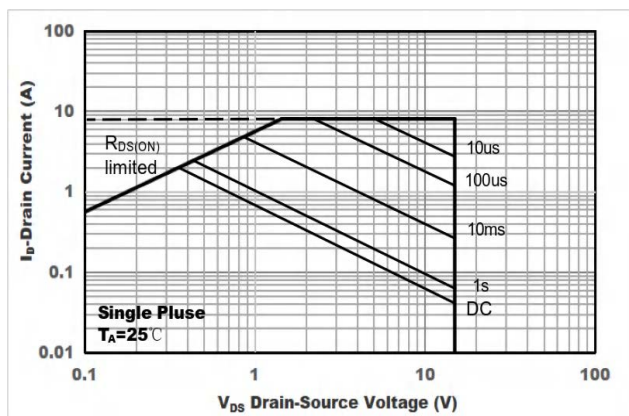


Figure7. Safe Operation Area

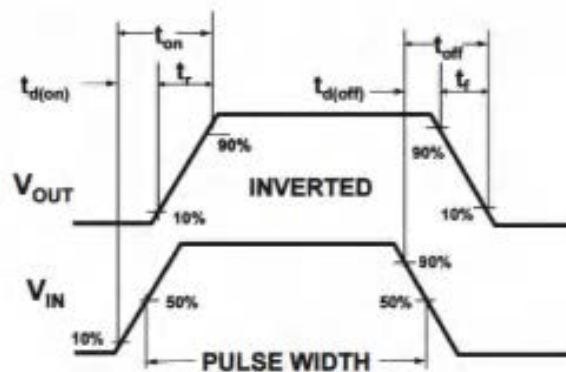
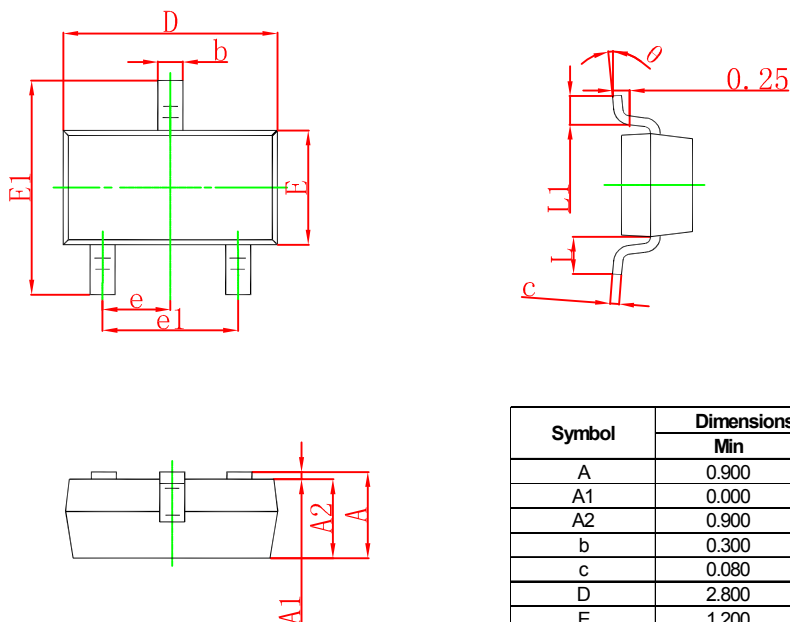


Figure8. Switching wave

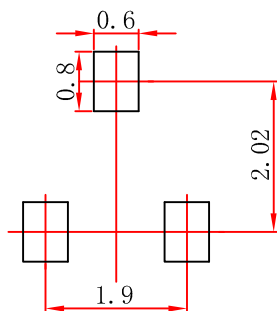


SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



Note:
1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.



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