

## P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)			
- 30	0.033 at V <sub>GS</sub> = - 10 V	- 38	19 nC			
- 30	0.046 at V <sub>GS</sub> = - 4.5 V	- 25	13110			

#### **FEATURES**

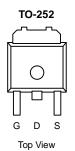
- Halogen-free
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

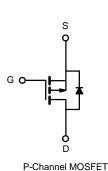


RoHS

### **APPLICATIONS**

- Load Switch
- · Notebook Adaptor Switch





Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 30	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
	T <sub>C</sub> = 25 °C		- 38		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	1 , [	- 25		
Continuous Diain Curient (1) = 130°C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 14.9 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		- 13.6 <sup>a, b</sup>		
Pulsed Drain Current	I <sub>DM</sub>	- 112	Α		
Continuous Source Drain Diada Current	T <sub>C</sub> = 25 °C	1	- 4.1		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	ls =	- 2.2 <sup>a, b</sup>		
Avalanche Current	1 04 11	I <sub>AS</sub>	- 20		
Single-Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	20	mJ	
	T <sub>C</sub> = 25 °C		25		
Marianaa Darraa Dissination	T <sub>C</sub> = 70 °C		20	W	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.7 <sup>a, b</sup>	vv	
	T <sub>A</sub> = 70 °C		1.7 <sup>a, b</sup>		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	46	°C/W	
Maximum Junction-to-Foot	Steady State	R <sub>th,IF</sub>	20	25	C/VV	

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 85 °C/W.
- d. Based on  $T_C$  = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						•
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L = 250 uA		- 34		mV/
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = -250 \mu\text{A}$		5.3		°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1.0		- 2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 100	nA
Zoro Coto Voltogo Droin Current		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	μΑ
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 30			Α
D : 0	_ ` ′	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		0.033		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8 A		0.046		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 10 A		28		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			1350		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		255		
Reverse Transfer Capacitance	$C_{rss}$			190		
Total Oata Obanna	$Q_g$ $V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -10 \text{ A}$	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A}$		27	43	
Total Gate Charge			19	25		
Gate-Source Charge	$Q_gs$	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -10 \text{ A}$	0 A	6		- nC
Gate-Drain Charge	Q <sub>gd</sub>			12		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.2	4.4	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			13	25	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		12	24	
Turn-Off DelayTime	Time $t_{d(off)}$ $I_D \cong -10 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = -10 \text{ A}$			40	70	
Fall Time	t <sub>f</sub>			9	18	
Turn-On Delay Time	t <sub>d(on)</sub>			48	80	ns
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		92	160	
Turn-Off DelayTime	n-Off DelayTime $t_{d(off)}$ $I_D \cong -10 \text{ A}, V_{GEN} = -4.5 \text{ V},$			34	60	
Fall Time	t <sub>f</sub>			19	35	
<b>Drain-Source Body Diode Characteris</b>	tics					
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 4.1	۸
Pulse Diode Forward Current	I <sub>SM</sub>				- 40	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A, V <sub>GS</sub> = 0 V		- 0.75	- 1.2	V
Body Diode Reverse Recovery Time				27	45	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 40 A dl/dt = 400 A/vs T = 25 °C		16	27	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		12		ns
Reverse Recovery Rise Time	t <sub>b</sub>	1		15		

#### Notes:

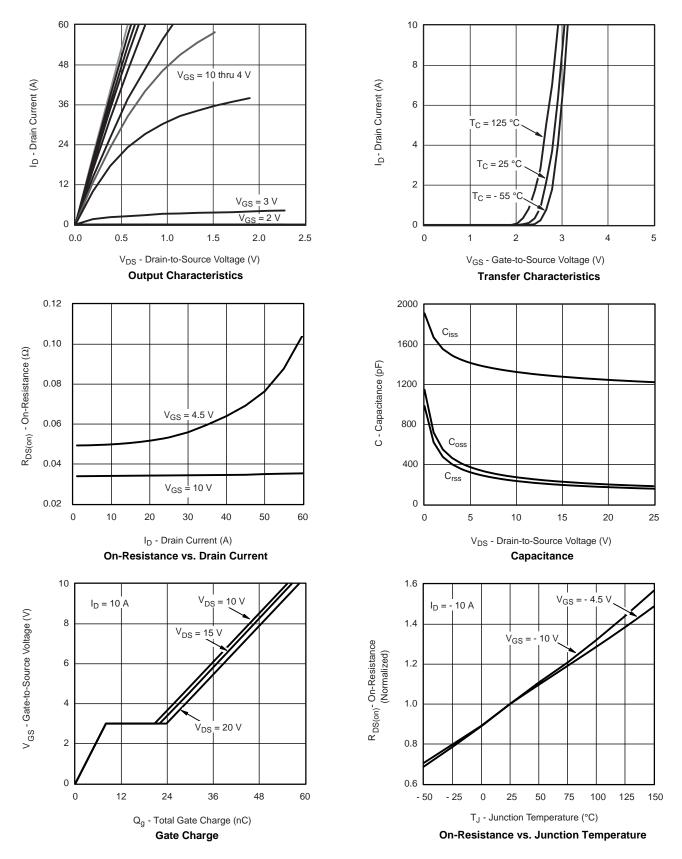
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

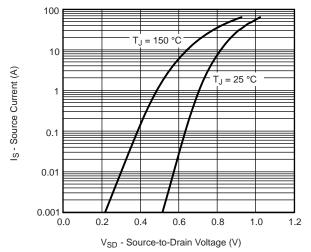


## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

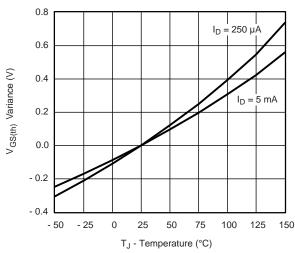




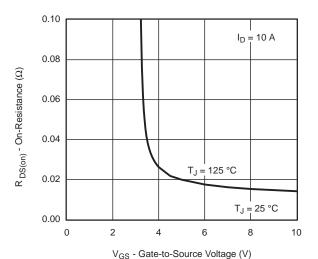
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



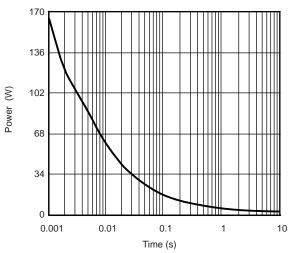
#### Source-Drain Diode Forward Voltage



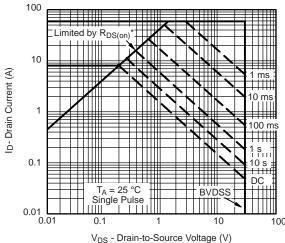
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

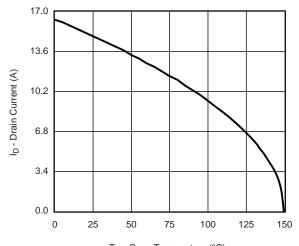


\* V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

Safe Operating Area

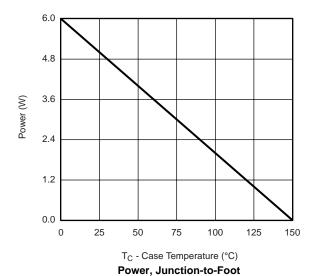


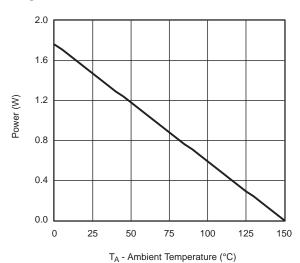
## MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



 $T_{\mbox{\scriptsize C}}$  - Case Temperature (°C)

## **Current Derating\***



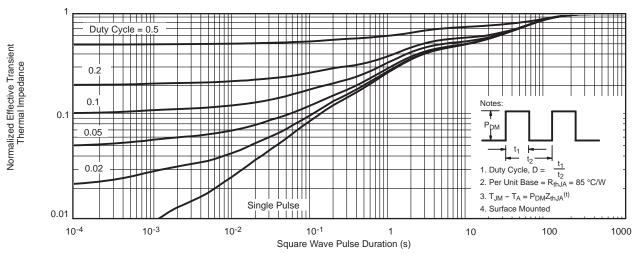


Power Derating, Junction-to-Ambient

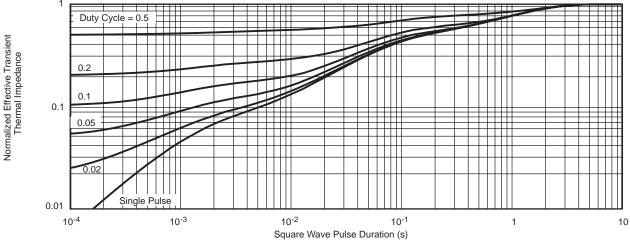
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



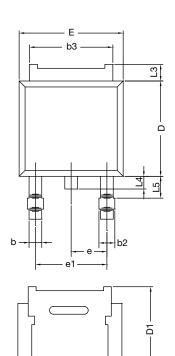
Normalized Thermal Transient Impedance, Junction-to-Ambient



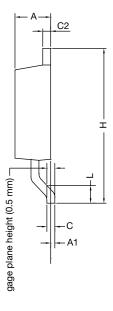
Normalized Thermal Transient Impedance, Junction-to-Foot



## **TO-252AA CASE OUTLINE**



E1



	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56	BSC 0.180 E		BSC	
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12					

ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347

### Note

• Dimension L3 is for reference only.



## **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)



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