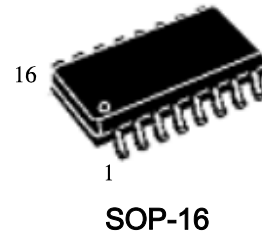




DESCRIPTION

The ST3232BDR is a 3 V powered EIA/TIA-232 and V.28/V.24 communication interfaces with low power requirements, high data-rate capabilities and enhanced electrostatic discharge (ESD) protection to ± 8 kV using IEC1000-4-2 contact discharge and ± 15 kV using the human body model. The ST3232E has a proprietary low-dropout transmitter output stage providing true RS-232 performance from 3 to 5 V supplies with a dual charge pump.

The device is guaranteed to run at data rates of 250 kbps while maintaining RS-232 output levels.



FEATURES

- ESD protection for RS-232 I/O pins
- ± 15 kV human body model
- ± 8 kV IEC 1000-4-2 contact discharge
- 300 μ A supply current
- 250 kbps minimum guaranteed data rate
- 6 V/ μ s minimum guaranteed slew rate
- Meet EIA/TIA-232 specifications down to 3 V
- Available in SOP-16

APPLICATIONS

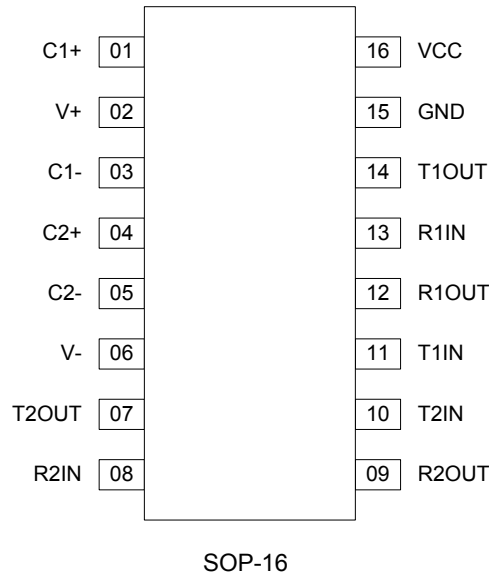
- Notebook, subnotebook and palmtop computers
- Battery-powered equipment
- Hand-held equipment
- Peripherals and printers

ORDERING INFORMATION

Package	Oder No.	Compliance	Supplied As
SOP-16	ST3232BDR	RoHS, Green	Tube



PIN CONFIGURATION



PIN DESCRIPTION

Pin No.	Pin Name	Pin Description
1	C1+	Positive terminal for the first charge pump capacitor
2	V+	Doubled voltage terminal
3	C1-	Negative Terminal for the first charge pump capacitor
4	C2+	Positive terminal for the second charge pump capacitor
5	C2-	Negative terminal for the second charge pump capacitor
6	V-	Inverted voltage terminal
7	T2OUT	Second transmitter output voltage
8	R2IN	Second receiver input voltage
9	R2OUT	Second receiver output voltage
10	T2IN	Second transmitter input voltage
11	T1IN	First transmitter input voltage
12	R1OUT	First receiver output voltage
13	R1IN	First receiver input voltage
14	T1OUT	First transmitter output voltage
15	GND	Ground
16	VCC	Supply voltage



SPECIFICATIONS

Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V_{CC}	-0.3	6.0	V
Transmitter High Output Voltage	V_+	$V_{CC}-0.3$	7.0	V
Transmitter Low Output Voltage	V_-	-0.3	-14	V
Transmitter Input Voltage	V_{TIN}	-0.3	V_++6	V
Receiver Input Voltage	V_{RIN}	-25	25	V
Voltage Applied to Transmitter Output	V_{TOUT}	$V_- -13.2$	$V_+ +12.2$	V
Voltage Applied to Receiver Output	V_{ROUT}	-0.3	$V_{CC}+0.3$	V
Storage Temperature Range	T_{STG}	-65	150	°C
Thermal resistance junction-to-case ^{(1) (2)}	R_{th-jc}	30		°C/W
Thermal resistance junction-to-ambient ^{(1) (2)}	R_{th-ja}	95		

- Short-circuits can cause excessive heating and destructive dissipation.
- R_{th} are typical values.

ESD protection

PIN	TEST CONDITIONS	TYP	UNIT
D _{OUT} , R _{IN}	Human-Body Model	±15	kV
D _{OUT} , R _{IN}	IEC-1000-4-2	±8	kV

Electrical Characteristics

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP(1)	MAX	UNIT
I _{SUPPLY}	V_{CC} Power supply current	No load, $V_{CC} = 3V$ or $5V$		0.3	1	mA

(1) All typical values are at $T_A = 25^\circ\text{C}$.

Logic input

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{TIL}	Input logic threshold low	T-IN			0.8	V
V_{HYS}	Transmitter input hysteresis			0.25		V
I_{IL}	Input leakage current T-IN			±0.01	±1	V
V_{TIH}	Input logic threshold high	$V_{CC} = 3.3\text{ V}$	2			V
		$V_{CC} = 5\text{ V}$	2.4			



Transmitter

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{TOUT}	Output voltage swing	All transmitter outputs are loaded with 3 kΩ to GND	±5	±5.4		V
R _{TOUT}	Transmitter output resistance	Driver high-level input voltage (D _{IN})	300	10M		Ω
I _{SC}	Output short-circuit current			±60		mA
I _{TOL}	Output leakage current	V _{CC} = 0 V or 3.3 V or 5.5 V, V _{CC} = ±12 V Transmitters disable			±25	μA

Timing characteristics

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
D _R	Data transfer rate	R _L = 3 kΩ, C _{L2} = 1000 pF one transmitter switching	250			kbps
t _{PHLR} t _{PLHR}	Propagation delay input to output	R _{XIN} = R _{XOUT} , C _L = 150 pF		0.15		μs
t _{OER}	Receiver output enable time	Normal operation		50		ns
t _{ODR}	Receiver output disable time	Normal operation		50		ns
t _{PHLT} - t _{THL}	Transmitter propagation delay difference	(1)		200		ns
t _{PHLR} - t _{THR}	Receiver propagation delay difference			50		ns
S _{RT}	Transition slew rate	T _A = 25 °C R _L = 3 kΩ to 7 kΩ V _{CC} = 3.3 V measured from +3 V to -3 V or -3 V to +3 V C _L = 150 pF to 1000 pF C _L = 150 pF to 2500 pF	6 4		30 30	V/μs V/μs

(1) Transmitter skew is measured at the transmitter zero-cross points.

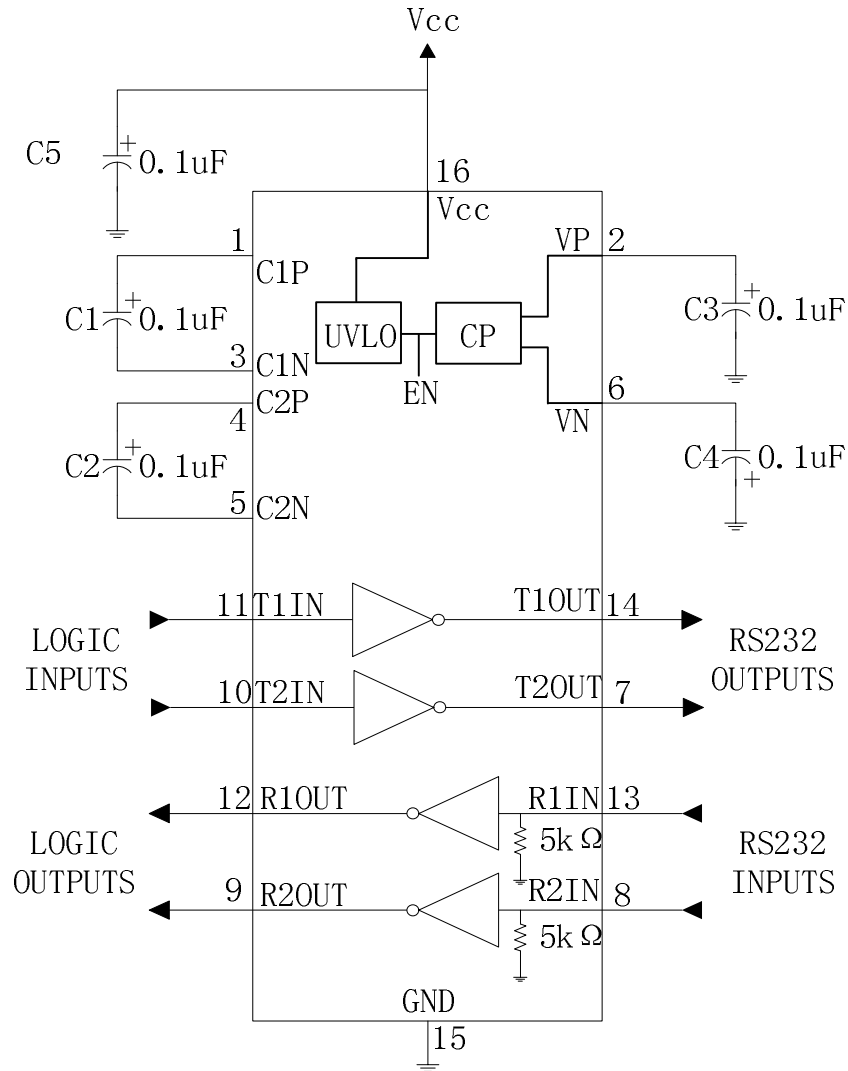
Receiver

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{RIN}	Receiver input voltage operating range		-25		25	V
V _{RIL}	RS-232 input threshold low	T _A = 25 °C, V _{CC} = 3.3 V	0.6	1.1		V
		T _A = 25 °C, V _{CC} = 5 V	0.8	1.5		
V _{RIH}	RS-232 input threshold high	T _A = 25 °C, V _{CC} = 3.3 V		1.4	2.4	V
		T _A = 25 °C, V _{CC} = 5 V		1.8	2.4	
V _{RIHYS}	Input hysteresis			0.5		V
R _{RIN}	Input resistance	T _A = 25 °C	3	5	7	kΩ
V _{ROL}	TTL/CMOS Output voltage low	I _{OUT} = 1.6 mA			0.4	V
V _{ROH}	TTL/CMOS Output voltage high	I _{OUT} = -1 mA	V _{CC} -0.6	V _{CC} -0.1		V



APPLICATION

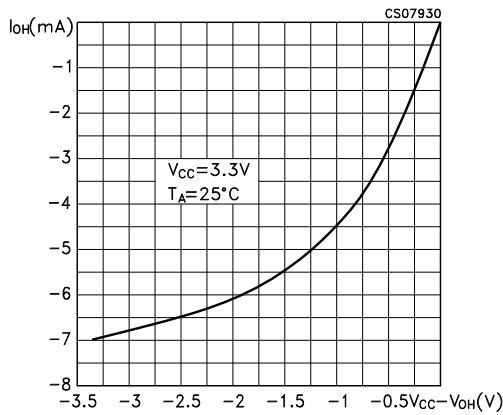
Application circuits



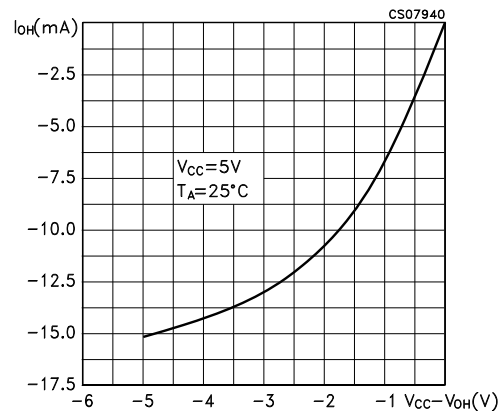


TYPICAL PERFORMANCE CHARACTERISTICS

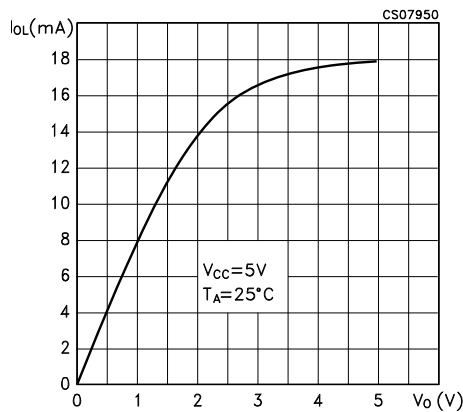
Output current vs. output high voltage



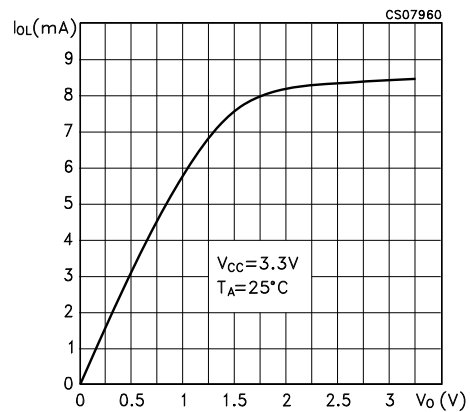
Output current vs. output high voltage



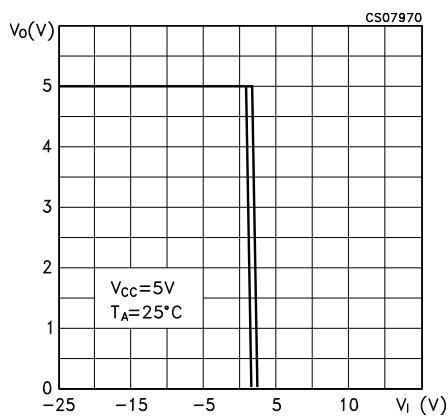
Output current vs. output low voltage



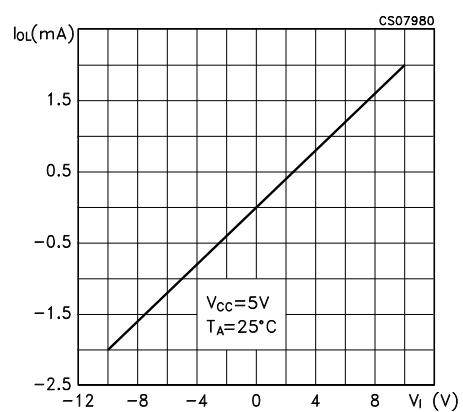
Output current vs. output low voltage



Voltage transfer characteristics for transmitter inputs

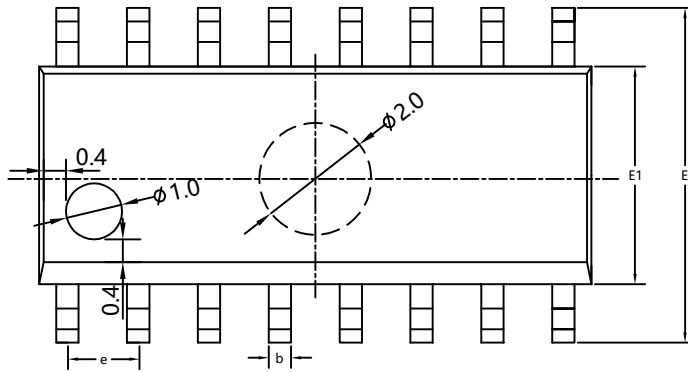
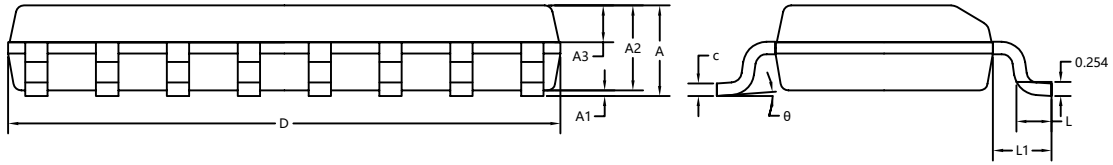


Receiver input resistance





PACKAGE OUTLINE DIMENSIONS
SOP-16



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	1.50	1.60	1.70
A1	0.10	0.15	0.25
A2	1.40	1.45	1.50
A3	0.60	0.65	0.70
b	0.30	0.40	0.50
c	0.15	0.20	0.25
D	9.80	9.90	10.00
E	5.80	6.00	6.20
E1	3.85	3.90	3.95
e	1.27BSC		
L	0.50	0.60	0.70
L1	1.05BSC		
theta	0°	4°	8°



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