

**isc P-Channel MOSFET Transistor**
**IXTP32P05T**
**• FEATURES**

- Static drain-source on-resistance:  
 $R_{DS(on)} \leq 39m\Omega$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• APPLICATION**

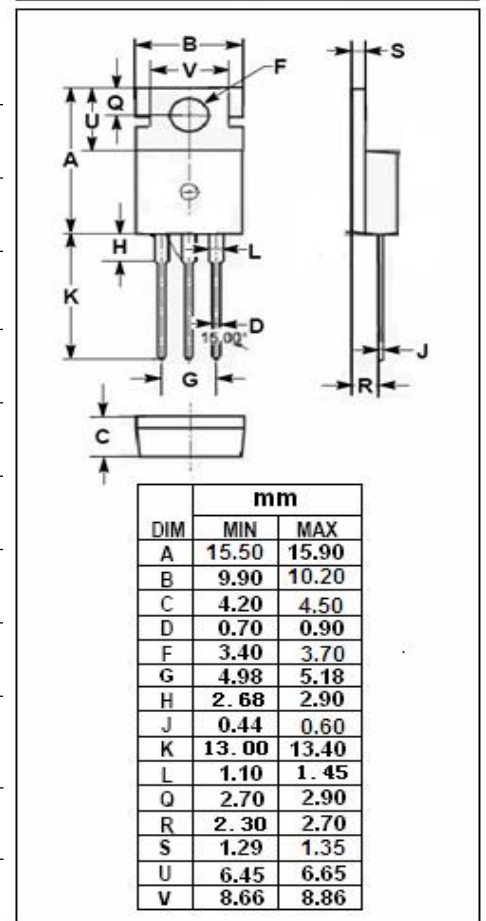
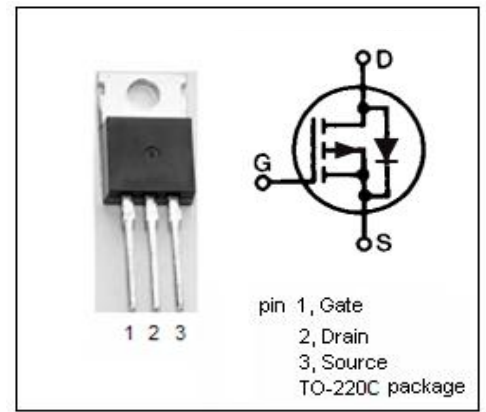
- High side switching
- Current regulators
- Automatic test equipment

**• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	-50	V
$V_{GS}$	Gate-Source Voltage	$\pm 15$	V
$I_D$	Drain Current-Continuous	-32	A
$I_{DM}$	Drain Current-Single Pulsed	-110	A
$P_D$	Total Dissipation @ $T_c=25^\circ C$	83	W
$T_j$	Operating Junction Temperature	-55~150	$^\circ C$
$T_{stg}$	Storage Temperature	-55~150	$^\circ C$

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	1.5	$^\circ C/W$



## isc P-Channel MOSFET Transistor

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## ELECTRICAL CHARACTERISTICS

T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = -250 μ A	-50			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; I <sub>D</sub> = -250 μ A	-2.5		-4.5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = -10V; I <sub>D</sub> = -16A			39	m Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±15V; V <sub>DS</sub> =0			±50	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V			-3	μ A
		V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V; T <sub>J</sub> =125°C			-100	
V <sub>SD</sub>	Diode forward voltage	I <sub>F</sub> = -32A; V <sub>GS</sub> = 0V			-1.5	V

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