

isc Silicon PNP Power Transistor

KSA1220

DESCRIPTION

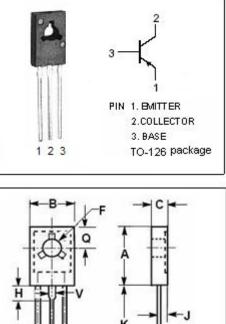
- Low Collector Saturation Voltage
- High voltage
- High speed switching
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

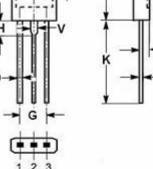
APPLICATIONS

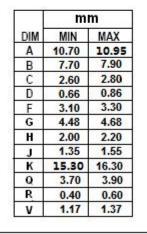
- High voltage switching
- · Low power switching regulator
- DC-DC converter

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-120	V
V _{CEO}	Collector-Emitter Voltage	-120	V
V_{EBO}	Emitter-Base Voltage	-5	V
lc	Collector Current-Continuous	-1.2	A
Pc	Total Power Dissipation @ Ta=25℃	1.2	w
	Total Power Dissipation @ T _c =25℃	20	W
TJ	Junction Temperature 150		°C
T _{stg}	Storage Temperature Range	-55~150	°C









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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -1A; I _B = -200mA			-0.7	V
V _{BE(sat)}	Collector-Emitter Saturation Voltage	I _C = -1A; I _B = -200mA			-1.3	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -120V; I _E = 0			-1	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -3V; I _C = 0			-1	μA
h _{FE}	DC Current Gain	I _C = -300mA ; V _{CE} = -5V	60		320	

• h_{FE} Classifications

R	О	Y
60-120	100-200	160-320

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