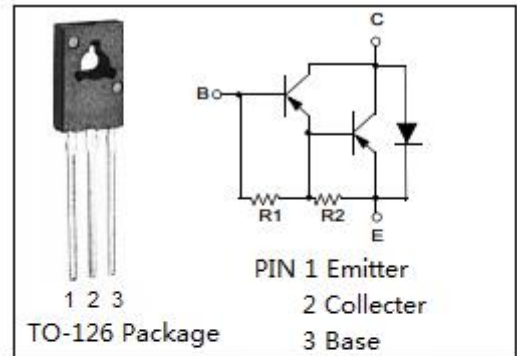


**isc Silicon PNP Power Transistor**

**KSE700**

**DESCRIPTION**

- High DC current gain
- Low Collector Saturation Voltage
- Complement to Type KSE800
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

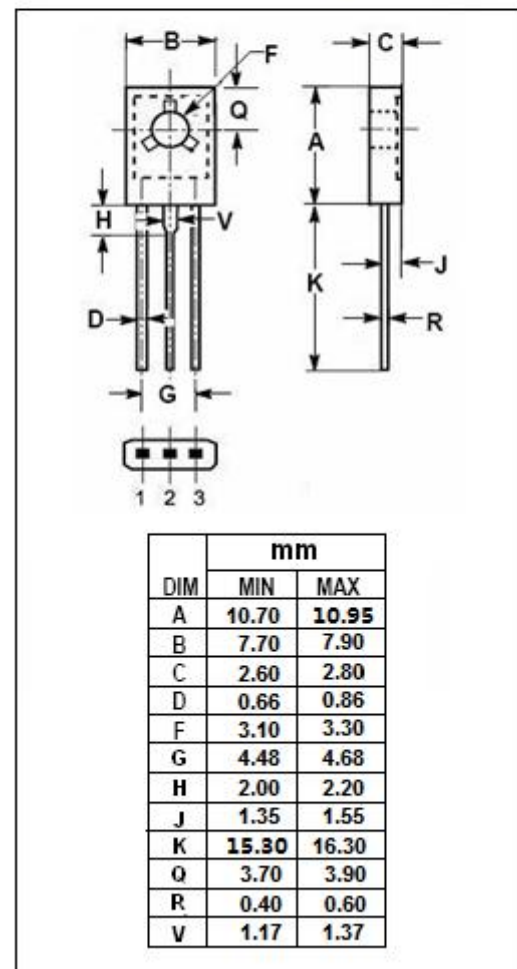


**APPLICATIONS**

- Monolithic construction with built-in-Base-Emitter resistor

**ABSOLUTE MAXIMUM RATINGS(T<sub>a</sub>=25°C)**

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	-60	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-60	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
I <sub>c</sub>	Collector Current-Continuous	-4	A
P <sub>c</sub>	Collector Power Dissipation @ T <sub>c</sub> =25°C	40	W
	Collector Power Dissipation @ T <sub>a</sub> =25°C	2	
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature Range	-55~150	°C



**ELECTRICAL CHARACTERISTICS**

 T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I <sub>CB0</sub>	Collector Cutoff Current	V <sub>CB</sub> = -60V; I <sub>E</sub> = 0			-100	μ A
I <sub>EB0</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-2	mA
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> =- 1.5A; V <sub>CE</sub> = -3V	750			
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> =- 4A; V <sub>CE</sub> = -3V	100			
V <sub>CE(sat)-1</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -1.5A; I <sub>B</sub> =- 30mA			-2.5	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -4A; I <sub>B</sub> = -40mA			-3	V
V <sub>BE(ON)-1</sub>	Base-Emitter On Voltage	I <sub>C</sub> =- 1.5A; V <sub>CE</sub> = -3V			-1.2	V
V <sub>BE(ON)-2</sub>	Base-Emitter On Voltage	I <sub>C</sub> =- 4A; V <sub>CE</sub> = -3V			-3	V

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