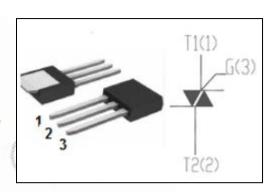


isc Thyristors T835-600H

APPLICATIONS

- With TO-251(IPAK) package.
- Be suitable for general purpose AC switching, they can be used as an ON/OFF function in applications.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.



ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER			UNIT
V_{DRM}	Repetitive peak off-state voltage			V
V_{RRM}	Repetitive peak reverse voltage			V
I _{T(RMS)}	RMS on-state current @T₀=110°C			Α
I _{TSM}	Surge non-repetitive on-state current	F=50HZ; t=20ms F=60HZ;16.7ms	80 84	А
$P_{G(AV)}$	Average gate power dissipation	@T _j =125℃ t _P =20 μ s	1	W
Tj	Operating junction temperature	South Mills	-40~125	$^{\circ}$
T _{stg}	Storage temperature		-40~150	$^{\circ}$

ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RRM}	Tj=25℃		5	μ A
			Tj=125℃		1	mA
I _{DRM}	Repetitive peak off-state current	V _{DM} =V _{DRM}	Tj=25℃		5	μ А
			T _j =125℃		1	mA
V _{TM}	On-state voltage	I _{TM} = 11A; t _p =380 μ s			1.55	V
I _{GT}	Gate-trigger current (minimum IGT is guaranted at 5% of IGT max) Quadrant(I - II - III)	V _D = 12 V;R _L =30 Ω			35	mA
V _{GT}	Gate-trigger voltage Quadrant (I - II - III)	$V_D = 12 \text{ V;R}_L = 30 \Omega$			1.3	V
R _{th(j-c)}	Thermal resistance	Junction to case			1.6	°C/W
R _{th(j-a)}	Thermal resistance	Junction to case			100	°C/W



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