

# NCE N-Channel Enhancement Mode Power MOSFET

# **Description**

The NCE2304 uses advanced trench technology to provide excellent  $R_{\text{DS(ON)}}$  and low gate charge .This device is suitable for use as a load switch or in PWM applications.

#### **General Features**

•  $V_{DS} = 30V, I_D = 3.6A$ 

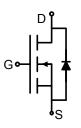
 $R_{DS(ON)}$  < 73m $\Omega$  @  $V_{GS}$ =4.5V

 $R_{DS(ON)}$  <46m $\Omega$  @  $V_{GS}$ =10V

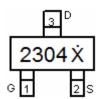
- High power and current handing capability
- Lead free product is acquired
- Surface mount package
- Pb free terminal plating
- RoHS compliant
- Halogen free

#### **Application**

- Battery protection
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2304 X	NCE2304	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	V	
Gate-Source Voltage	V <sub>G</sub> S	±20	V	
Drain Current-Continuous	I <sub>D</sub>	3.6	А	
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	15	А	
Maximum Power Dissipation	P <sub>D</sub>	1.7	W	
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$ C	

## **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	73.5	°C/W

#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA





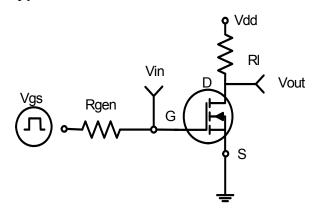
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	1.5	2.2	V
Drain Source On State Desigtance	-	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3.1A	-	61	73	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.6A	-	39	46	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =3.6A	-	11	-	S
Dynamic Characteristics (Note4)						•
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V,	-	230	-	PF
Output Capacitance	Coss		-	40	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	17	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	10	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =10V, $I_{D}$ =3.6A $V_{GS}$ =4.5V, $R_{GEN}$ =6 $\Omega$	-	50	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	10	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	20	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =15V,I <sub>D</sub> =3.6A, V <sub>GS</sub> =10V	-	4.0	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	0.75	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	0.65	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =3.6A	-	0.8	1.2	V
Diode Forward Current (Note 2)	Is		-	-	3.6	Α

## Notes:

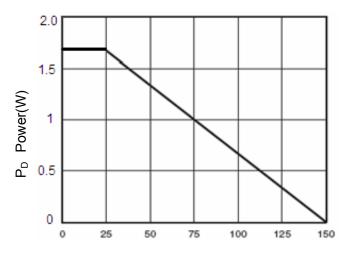
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
  3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



# **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 



T<sub>J</sub>-Junction Temperature(°C)

**Figure 3 Power Dissipation** 

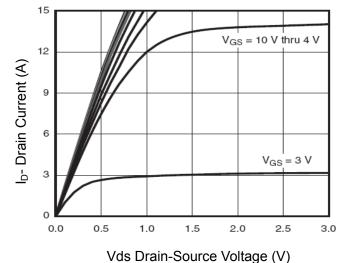


Figure 5 Output Characteristics

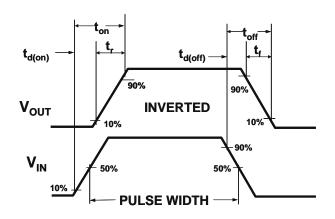
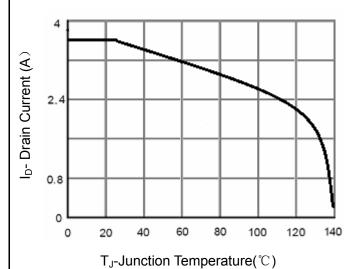


Figure 2:Switching Waveforms



**Figure 4 Drain Current** 

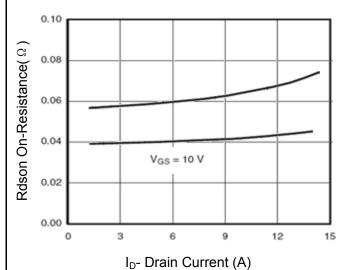
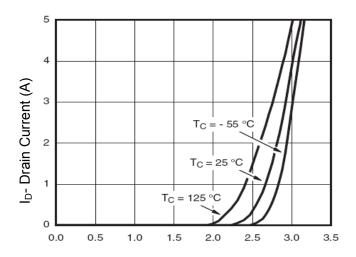


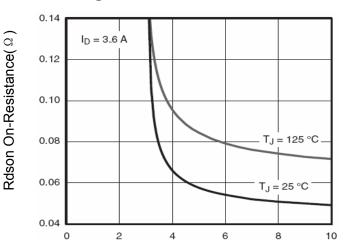
Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)

**Figure 7 Transfer Characteristics** 



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

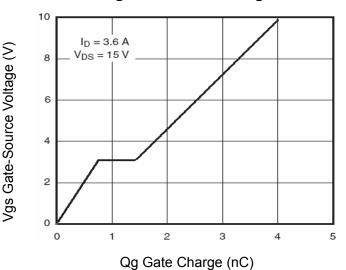
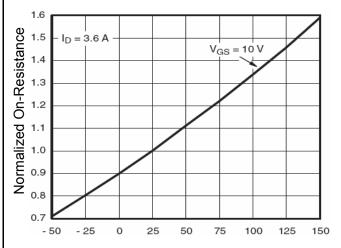
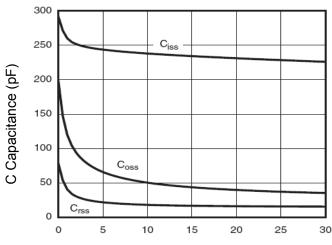


Figure 11 Gate Charge



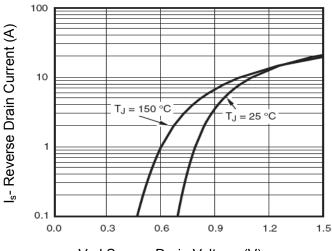
 $T_J$ -Junction Temperature ( $^{\circ}$ C)





Vds Drain-Source Voltage (V)

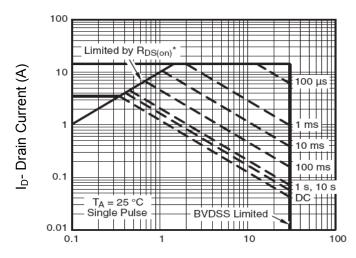
Figure 10 Capacitance vs Vds



Vsd Source-Drain Voltage (V)

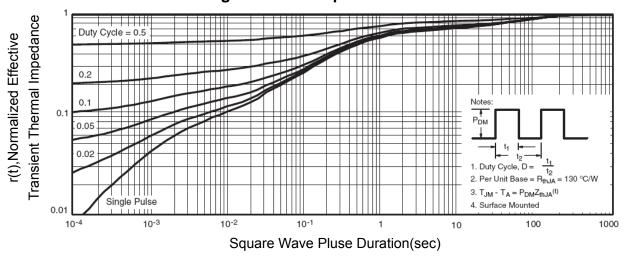
Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

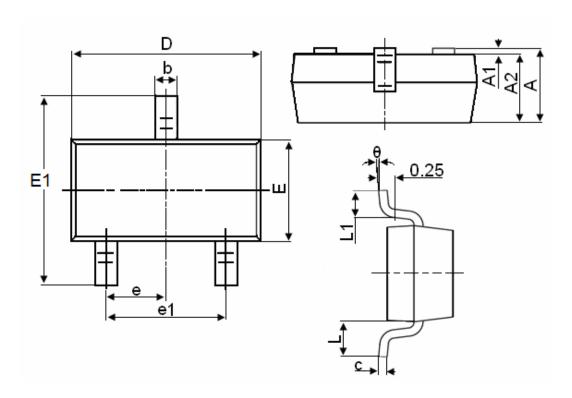
**Figure 13 Safe Operation Area** 



**Figure 14 Normalized Maximum Transient Thermal Impedance** 



# **SOT-23 Package Information**



Symbol	Dimensions in Millimeters			
Symbol	MIN.	MAX.		
Α	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е		0.950TYP		
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0°	8°		

## Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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