

# NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE6045XG uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

## **Application**

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

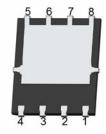
100% UIS TESTED! 100% ΔVds TESTED!

#### **General Features**

- $V_{DS}$  =60V, $I_D$  =45A  $R_{DS(ON)}$ =7.4m $\Omega$  (typical) @  $V_{GS}$ =10V  $R_{DS(ON)}$ =11.4m $\Omega$  (typical) @  $V_{GS}$ =4.5V
- High density cell design for ultra low Rdson
- Very low on-resistance R<sub>DS(on)</sub>
- Good stability and uniformity with high E<sub>AS</sub>
- 150 °C operating temperature
- Pb-free lead plating

#### **DFN 5X6**

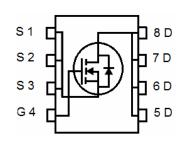




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Top View

**Bottom View** 



**Schematic Diagram** 

#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6045XG	NCE6045XG	DFN5X6-8L	Ø330mm	12mm	5000

### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vos	60	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous	I <sub>D</sub>	45	А	
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	32	А	
Pulsed Drain Current	I <sub>DM</sub>	140	Α	
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	260	mJ	
Maximum Power Dissipation	P <sub>D</sub>	60	W	
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$	

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	Rejc	2.08	°C/W

### Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =60V, $V_{GS}$ =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.2	1.9	2.5	V
Danier Courses On Otata Basistana	_	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	7.4	9	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	-	11.4	15	mΩ
Forward Transconductance	<b>g</b> FS	$V_{DS}$ =5 $V$ , $I_D$ =20 $A$	25	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ 00\/\\ 0\/	-	2750	-	PF
Output Capacitance	Coss	$V_{DS}$ =30V, $V_{GS}$ =0V,	-	170	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	152	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	9	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =30V, $R_L$ =1.5 $\Omega$	-	7	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{GEN}$ =3 $\Omega$	-	32	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	6	-	nS
Total Gate Charge	Qg	\/ 20\/ L 00 A	-	60	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =30V,I <sub>D</sub> =20A,	-	10	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	14	-	nC
Drain-Source Diode Characteristics	<u> </u>		•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	45	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF=20A	-	31	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	45	-	nC

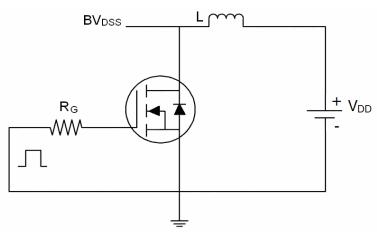
#### Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.**E<sub>AS</sub> condition: Tj=25  $^{\circ}$ C,V<sub>DD</sub>=30V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

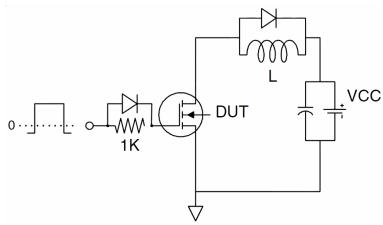


## **Test Circuit**

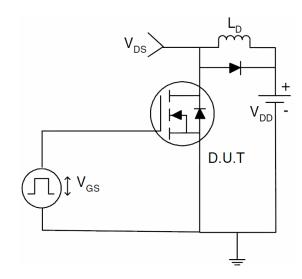
# 1) E<sub>AS</sub> test Circuit



## 2) Gate charge test Circuit

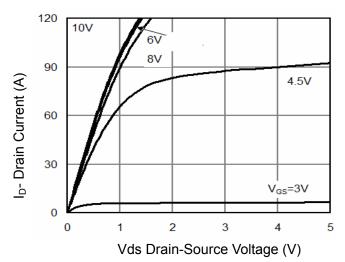


## 3) Switch Time Test Circuit

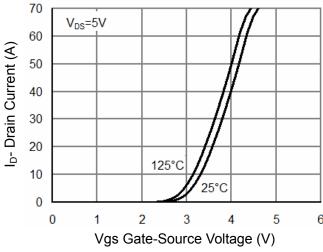




### **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

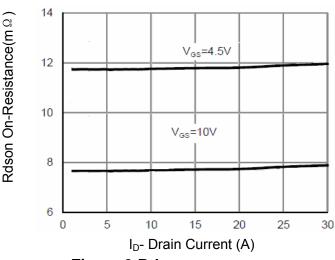


Figure 3 Rdson- Drain Current

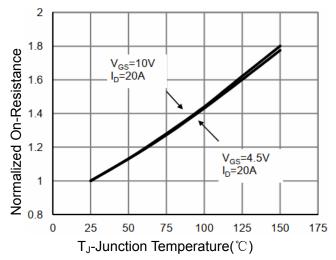


Figure 4 Rdson-JunctionTemperature

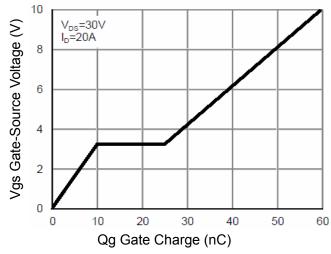


Figure 5 Gate Charge

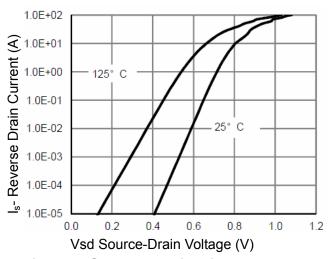
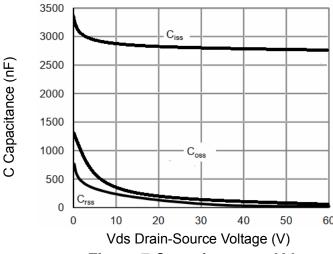


Figure 6 Source- Drain Diode Forward





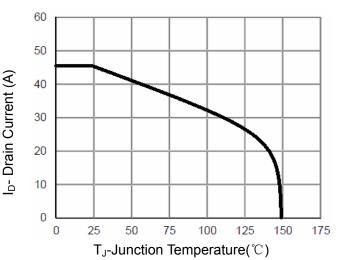
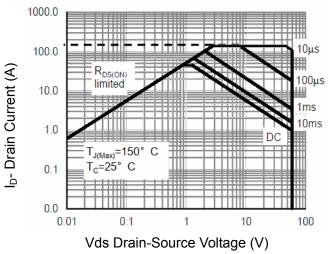
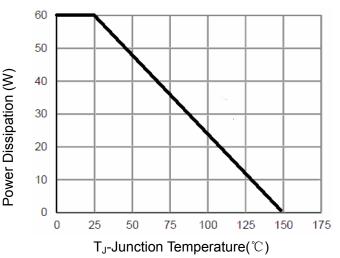


Figure 7 Capacitance vs Vds

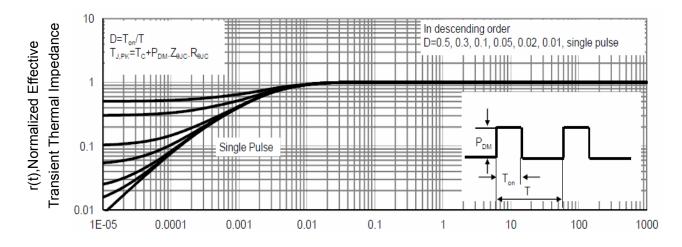
Figure 9 Current De-rating





**Figure 8 Safe Operation Area** 

Figure 10 Power De-rating

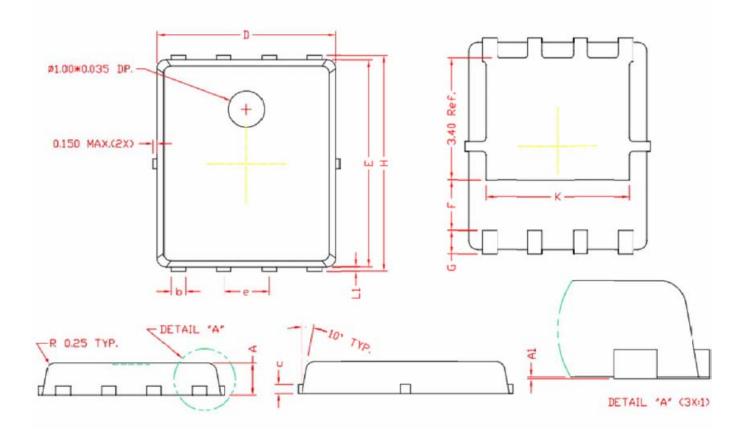


Square Wave Pluse Duration(sec)

**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **DFN5X6-8L Package Information**



# COMMON DIMENSIONS

# (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX	
A	0.80	0.90	1.00	
A1	0.00	0.03	0.05	
b	0.35	0.42	0.49	
С	0. 254 REF.			
D	4.90	5.00	5. 10	
F	1.40 REF.			
E	5. 70 5. 80		5. 90	
е	1. 27 BSC.			
Н	5.95	6.08	6. 20	
L1	0.10	0.14	0.18	
G	0.60 REF.			
K	4.00 REF.			



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