



#### 30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

60 A

#### **Features**

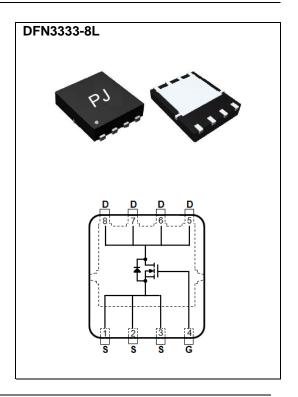
- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@10A<6m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@8A<9m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

• Case: DFN3333-8L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.001 ounces, 0.03 grams



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	30	V	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V	
Continuous Drain Current	T <sub>C</sub> =25°C	I <sub>D</sub>	60	А	
	T <sub>C</sub> =100°C		38		
Pulsed Drain Current(Note 1)	T <sub>C</sub> =25°C	I <sub>DM</sub>	240		
Power Dissipation	T <sub>C</sub> =25°C	Po	31	W	
	Tc=100°C		12.4		
Continuous Drain Current	T <sub>A</sub> =25°C	Ι <sub>D</sub>	15	Α	
	T <sub>A</sub> =70°C		12	Α	
Power Dissipation	T <sub>A</sub> =25°C	ı	2.0	107	
Power Dissipation	T <sub>A</sub> =70°C	Pb	1.3	W	
Operating Junction and Storage	Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Typical Thermal Resistance <sup>(Note 4,5)</sup>	Junction to Case	$R_{ heta JC}$	4.0	°C/W	
	Junction to Ambient	$R_{\theta JA}$	62.5		

• Limited only By Maximum Junction Temperature





### **Electrical Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250uA	1.0	1.6	2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =10A	-	5	6	mΩ
		V <sub>GS</sub> =4.5V,I <sub>D</sub> =8A	-	6.6	9	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	Igss	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic <sup>(Note 6)</sup>						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =15V, I <sub>D</sub> =20A, V <sub>GS</sub> =4.5V <sup>(Note 1,2)</sup>	-	12	-	nC
Gate-Source Charge	$Q_{gs}$		-	3.8	-	
Gate-Drain Charge	$Q_{gd}$		-	4.3	-	
Input Capacitance	Ciss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	1323	-	pF
Output Capacitance	Coss		-	219	-	
Reverse Transfer Capacitance	Crss		-	136	-	
Turn-On Delay Time	td <sub>(on)</sub>	$\begin{array}{c} V_{\text{DS}}\text{=}15\text{V,RL}\text{=}1\Omega, \\ V_{\text{GS}}\text{=}10\text{V, R}_{\text{G}}\text{=}3.3\Omega \\ \text{(Note 2,3)} \end{array}$	-	5.0	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	42	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	36	-	
Turn-Off Fall Time	t <sub>f</sub>		-	5.5	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I.		-	-	60	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A,V <sub>GS</sub> =0V	-	0.83	1	V

#### NOTES:

- 1. Pulse width<a></a>300us, Duty cycle<a></a>2%
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
- 6. Guaranteed by design, not subject to production testing.





#### **TYPICAL CHARACTERISTIC CURVES**

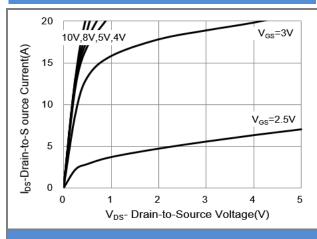
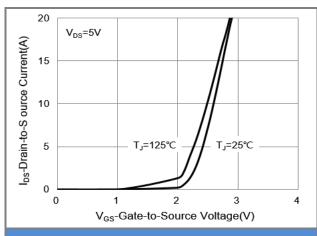


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

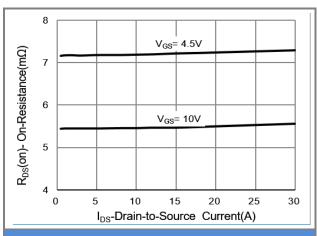


Fig.3 On-Resistance vs. Drain Current

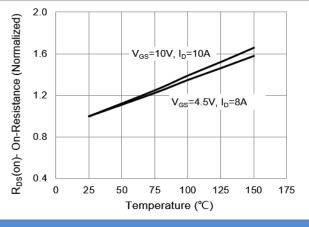


Fig.4 On-Resistance vs. Junction temperature

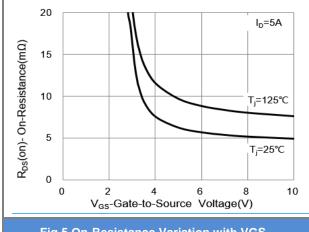


Fig.5 On-Resistance Variation with VGS.

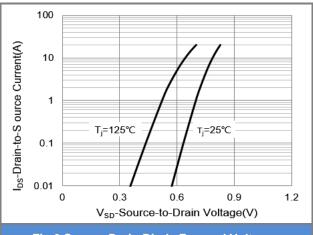


Fig.6 Source-Drain Diode Forward Voltage





#### TYPICAL CHARACTERISTIC CURVES

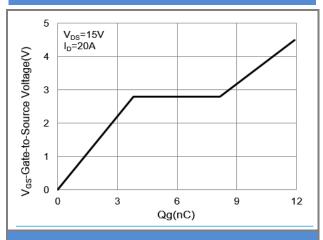


Fig.7 Gate-Charge Characteristics

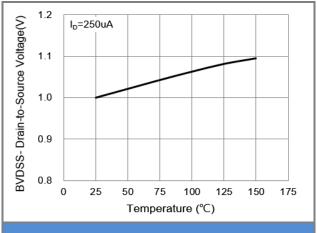


Fig.8 Breakdown Voltage Variation vs. Temperature.

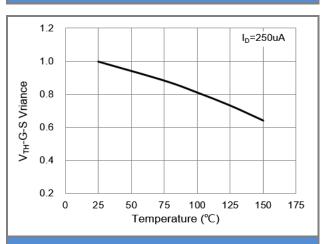


Fig.9 Threshold Voltage Variation with Temperature

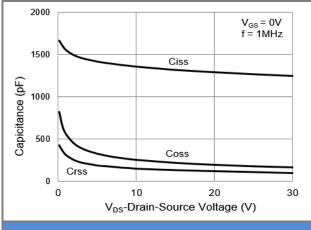
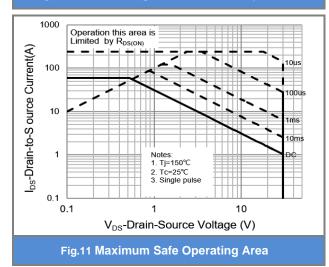


Fig.10 Capacitance vs. Drain-Source Voltage.







#### **TYPICAL CHARACTERISTIC CURVES**

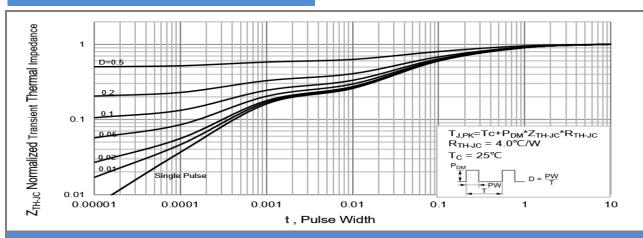


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

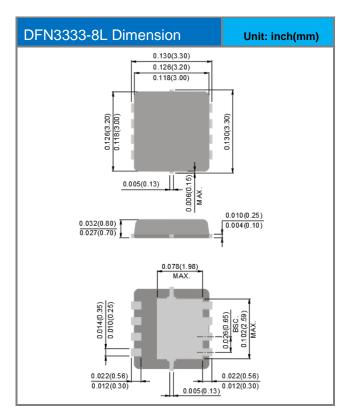


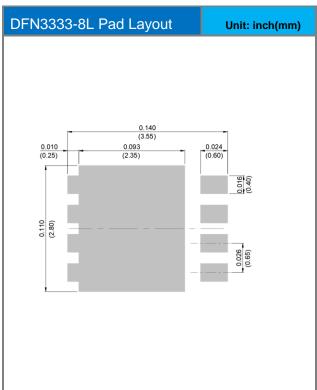


### Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ4404P-AU_R2_000A1	DFN3333-8L	5K pcs / 13" reel	4404	Halogen free RoHS compliant

## **Packaging Information & Mounting Pad Layout**









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