



■ 特性 (FEATURES)

频率范围: 3.5-70MHz

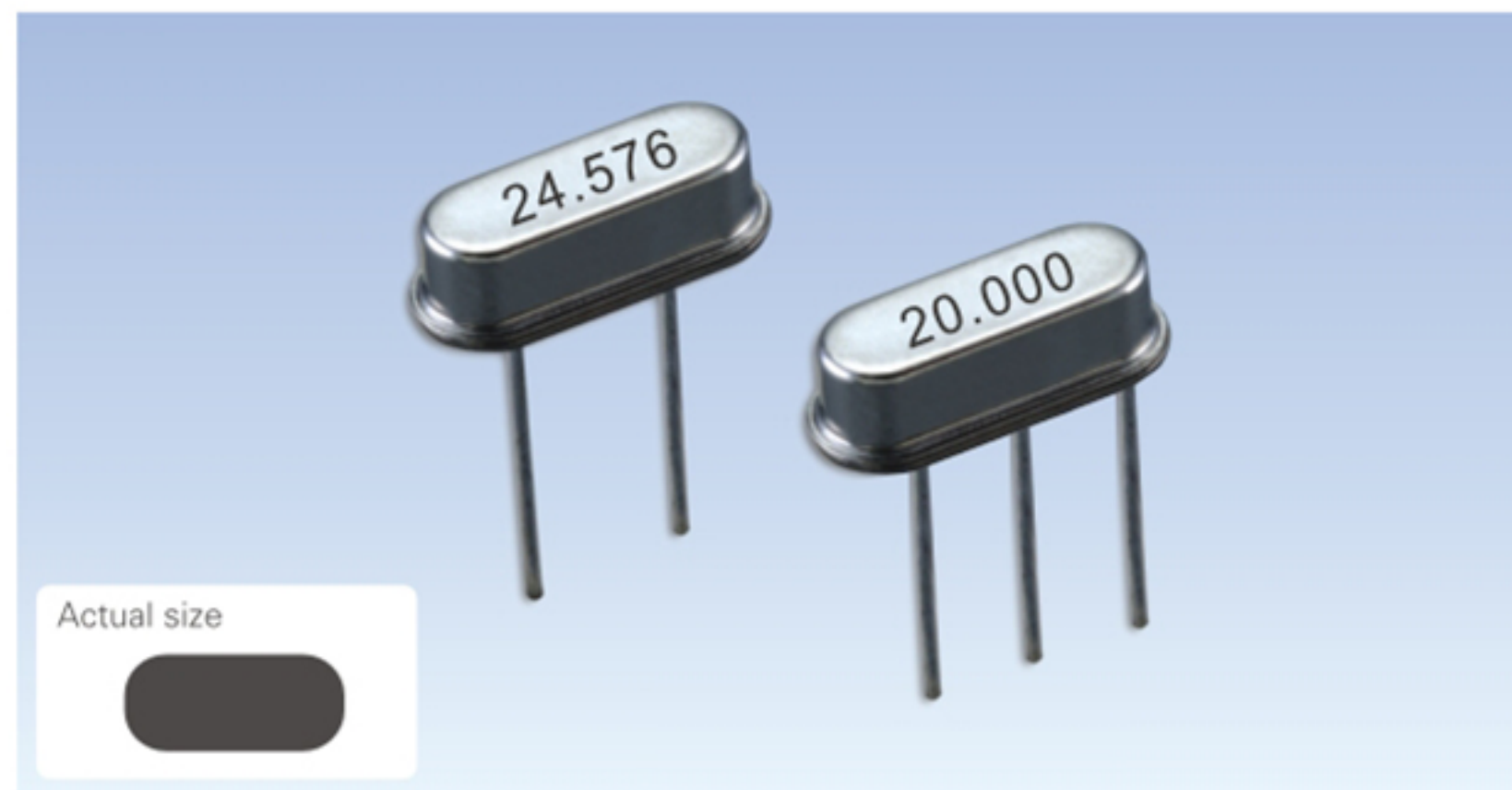
Frequency Range:3.5-70MHz

适用于卓越的CPU时钟信号发生器

Excellent clock signal generator for CPU's

适用于宽广的温度范围

Available in extended temperature range



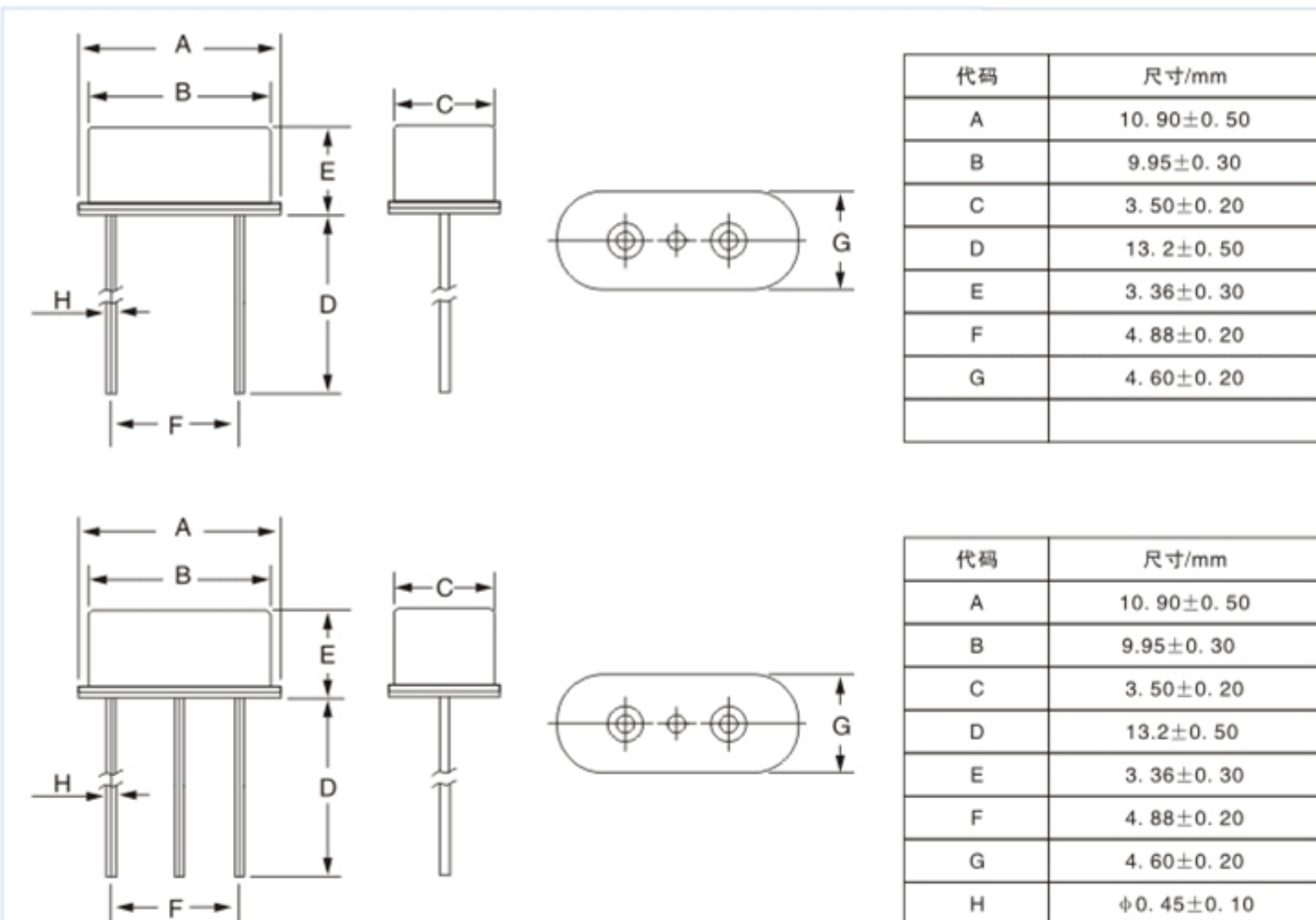
■ 标准规格 (STANDARD SPECIFICATION)

Item	Model	HC-49S/2PIN/3PIN	Conditions
频率范围 (Frequency Range)		3.5-30.0MHz(Fund.) 30.0-70.0MHz(3rd Overtone)	Need to specify
调整频差 (Frequency Tolerance)		±30ppm	At 25°C
温度频差 (Freq. Tol. Over Temp)		±50ppm	-10~+60°C See fig. As below
工作温度范围 (Operating Temp. Range)		-40~+85°C	
保存温度范围 (Storage Temp. Range)		-55~+125°C	
谐振电阻 (Series Resistance)		Refer to the table as below	at 25°C
负载电容 (Motional Capacitance)		20pF	Need to specify
静态电容 (Shunt Capacitance)		7.0pF Max.	
激励电平 (Drive Level)		100 μw typical	
年老化率 (Aging)		±5ppm Max.	25 ± 3°C
绝缘阻抗 (Insulation Resistance)		500Mohm Min.	DC100V ± 15V

■ 谐振电阻 (SERIES RESISTANCE)

频率范围(Freq. Range)	3.5-4MHz	4.01-6MHz	6.01-10MHz	10.01-27MHz	27.01-36MHz	36.01-70MHz
振动模式(Mode)	Fund.	Fund.	Fund.	Fund.	Fund.	3rd Overtone
谐振电阻(Series Resistance)	120ohmMax.	90ohmMax..	70ohmMax.	50ohmMax.	30ohmMax.	100ohmMax.

■ OUTLINE DRAWINGS (mm)



■ FREQUENCY VS. TEMPERATURE CURVE

